A New HEMT Breakdown Model Incorporating Gate and Thermal Effects

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This paper presents a comprehensive physical model for the breakdown process in HEMTs. The model is integrated into a fast quasi-two-dimensional HEMT physical simulator. The work is based on a full study of the complex interactions between the different breakdown mechanisms and the influence of design parameters. The model takes account of tunnelling effects in the region of the gate metallization, and of the thermal effects in the active channel under the gate region.

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1. INTRODUCTION

Microwave circuits such as power amplifiers operate under large-signal conditions. Their ability to perform efficiently is limited by the devices’ breakdown characteristics, which limit the transistor’s performance and power output. The accuracy of the large-signal design relies on the availability of suitable breakdown models. Popular breakdown theories have not been adequate to independently explain the full picture of the breakdown process in HEMTs. The effects of the gate leakage and substrate conduction in HEMTs on this have not been simulated. Analytical models for avalanche breakdown, such as Frensley’s [1], were based on physical simulations of the active channel around the gate. The effects of the gate leakage and substrate conduction on this have not been included. This paper considers breakdown more comprehensively including the effects of reverse gate conduction, thermal fluctuations within the active region and substrate conduction [2].

2. MODEL DESCRIPTION

The Quasi-Two-Dimensional (Q2D) model used here is based on the earlier work of Snowden [3].
The Q2D method is based on the assumption that the fundamental driving force for electron transport is the $x$-directed electric field. The HEMT Q2D model important features have been reported previously [4]. In contrast to MESFETs, spurious substrate current occurs in HEMT buffer layers due to the lateral $E_x$ field component. This current is drawn around the depleted channel and reduces the magnitude of the field. It is assumed in this paper that, as a result of the reduction in the electric field, an increase in the breakdown voltage is possible. Figure 1 shows a schematic diagram of a HEMT device.

The computational interpretation of the breakdown model presented in this paper is based on the interactions between the avalanche and gate leakage mechanisms. The relation that both processes simultaneously have with the device design parameters and power dissipation inside the device is included. The flow chart of the model is shown in Figure 2. The gate leakage, conventionally termed ‘soft breakdown’, is assumed to always occur in devices prior to avalanche. The flow of electrons from the gate into the semiconductor would then influence the impact ionisation process. Adding the leaked electrons to the channel electrons constituting the increasing drain current imposes this effect. The leakage is allowed in the three pinchoff stages.

3. THERMALLY DRIVEN GATE LEAKAGE CURRENT

The gate leakage model is principally based on a combination of Padovani [5] and Rideout [6] equations. These equations involve complex functions of temperature, barrier height and semiconductor parameters. The form that these equations are presented in the above references is quite complicated. In order to maintain the numerical efficiency of the physical device simulator, new approximations are introduced in this paper, which simplify the tunneling functions and maintain the accuracy of the solution. Tunneling through the gate metal-semiconductor barrier becomes significant in the reverse direction than in the forward direction because the bias voltages involved are usually greater. This cause the potential barrier to become thin enough such that tunneling is dominant. The Thermionic (T), Thermionic-Field (T-F) and Field Emission (FE) gate leakage currents are dependent on the lattice temperature and material specifications. What determines the current mechanism is the temperature of the channel and the applied bias. A new thermal model [7] incorporated into the Q2D physical model computes the instantaneous temperature of the active channel. The output of this model is linked with the breakdown model. This assists the dynamic update of the gate leakage current mechanism.
3.1. Thermionic-field Tunneling Current

This mechanism is the major form of tunneling. The tunneling current is defined by the equation

\[
I_{T-F} = A J_s \exp \left( \frac{E}{e'} \right) \tag{1}
\]

Where \(A\) is the gate area, \(E\) is applied energy calculated from \(q V_r\), \(V_r\) being the applied reverse bias voltage given a positive sign throughout this work, \(q\) is the electron charge and \(e'\) is an energy term defined as:

\[
e' = E_{oo} \left[ \frac{E_{oo}}{KT} - \tanh \left( \frac{E_{oo}}{KT} \right) \right]^{-1} \tag{2}
\]

The term \(E_{oo}\) quantifies the diffusion potential from metal into semiconductor. It has two equivalent definitions given in [5] and [6]. The latter definition is adopted in this work, however, slight numeric alteration is needed for consistency of units:

\[
E_{oo} = 18.57 \times 10^{-15} \left( \frac{N}{m e_r} \right)^{1/2} \tag{3}
\]

\(N\) is the doping density per \(m^{-3}\). In order to use equation (3) in (2), \(E_{oo}\) must be converted back by multiplying by \(q\), the electron charge. The type of HEMT used connects the gate metalisation directly to the \(N\)-doped AlGaAs layer.

In equation (1), \(J_s\) is defined as the saturation current given in [5] as:

\[
J_s = \frac{R(\pi E_{oo})^{1/2}}{KT} \left[ q V_r + \frac{q \phi_b}{\cosh^2 \left( \frac{E_{oo}}{KT} \right)} \right]^{1/2} \tag{4}
\]

\[
\times \exp \left( -\frac{q \phi_b}{E_o} \right)
\]

where \(\phi_b\) is the Schottky barrier height. \(E_o\) is a term defined as:

\[
E_o = E_{oo} \coth \left( \frac{E_{oo}}{KT} \right) \tag{5}
\]

It was observed during the course of this work that in the saturation current equation (4), the square root term was dominated by the first term. Hence a simpler approximate numerical expression for the saturation current was deduced:

\[
J_s = \frac{R}{kT} \left( \frac{\pi E_{oo} q V_r}{1/2} \right)^{1/2} \exp \left( -\frac{q \phi_b}{E_o} \right) \tag{6}
\]

which is integrated into the model. An empirical difference limit between equations (4) and (6) was reached after some experimentation's beyond which if this limit is exceeded, the solution obtained from equation (6) was observed to affect the numerical accuracy of the tunneling current. The model then switches to the more stringent expression of equation (4). The thermionic and field emission currents are calculated using similar equations. Threshold equations are incorporated which, according to the thermal status of the device, the appropriate leakage mechanisms is invoked.

4. SIMULATION RESULTS

Figure 3 shows the effect of varying the temperature on the tunnelling currents at various
doping levels. An increase in the T-F current is observed with an increase in the doping concentration. Figure 4 compares between the thermionic-field and field emission currents with respect to temperature. The T-F current is clearly of more prominent effect. The simulation of the DC I-V characteristics of a HEMT device enabling gate current tunnelling mechanism is shown in Figure 5. The soft breakdown mechanism is influenced by the design parameters and applied bias. It can be practically and numerically observed when the stimulating conditions are met. Comparisons with measured data in [8] shows good agreement between simulated and measured results of a MESFET case.

5. CONCLUSION

The Q2D physical device simulation of HEMTs has been expanded to include soft thermal breakdown effects using tunnelling current mechanisms. The influence of soft breakdown mechanism on the DC characteristics is influenced by the design parameters and applied bias. The thermal effects in the active channel under the gate region are included. This is conducted using an incorporated thermal model, which calculates the channel temperature and updates the thermal breakdown model.

References


Authors’ Biographies

Lutfi Albasha received his B.Eng. (Hons.) degree in Electronic and Electrical Engineering from the
Christopher M. Snowden received the B.Sc., M.Sc. and Ph.D. degrees from the University of Leeds. After graduating in 1977 he worked as an Applications Engineer for Mullard, Mitcham. His Ph.D. studies were conducted in association with Racal-MESL and were concerned with the large-signal characterisation of MESFET microwave oscillators. In 1982 he was appointed Lecturer in the Department of Electronics at the University of York. He joined the Microwave Solid State Group in the Department of Electrical and Electronic at the University of Leeds in 1983. He now holds the Chair of Microwave Engineering in the Microwave and Terahertz Technology Research Group and is also currently Head of the Department of Electronic and Electrical Engineering. During 1987 he was a Visiting Research Associate at the California Institute of Technology. He has been a Consultant to M/A-COM Inc., Corporate Research and Development since 1989, where he was on sabbatical leave during the period 1990–91. During this year he represented M/A-COM as Senior Staff Scientist. He was Chairman of the 1995 international Microwaves and RF Conference. He is a Member of the MIT Electromagnetics Academy. He is also a Top Scientist at the International Research Centre for Telecommunications-Transmission and Radar, Delft University of Technology, Netherlands. Professor Snowden is a Fellow of the IEEE and a Fellow of the IEE. He is a Distinguished Lecturer (1996/7) for the IEEE (Electron Devices Society). He is co-Chairman of the MTT-1 Committee and a Member of the 1997 IEEE MTT-S Technical Program Committee. His main research interests include compound semiconductor device modelling, microwave, terahertz and optical nonlinear subsystem design and advanced semiconductor devices. He has written 7 books and over 190 papers.

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