A Monte Carlo study of Electron Transport in Strained Si/SiGe Heterostructures

MAHBUB RASHED\textsuperscript{a}, W.-K. SHIH\textsuperscript{a}, S. JALLEPALLI\textsuperscript{a}, R. ZAMAN\textsuperscript{a}, T. J. T. KWAN\textsuperscript{b} and C. M. MAZIAR\textsuperscript{a}

\textsuperscript{a}Microelectronics Research Center, The University of Texas at Austin
\textsuperscript{b}Los Alamos National Laboratory, New Mexico

Electron transport in pseudomorphically-grown silicon on relaxed (001) Si\textsubscript{1-x}Ge\textsubscript{x} is investigated using a Monte Carlo (MC) simulation tool. The study includes both electron transport in bulk materials and in nMOS structures. The bulk MC simulator is based on a multiband analytical model, “fitted bands”, representing the features of a realistic energy bandstructure. The investigation includes the study of low- and high-field electron transport characteristics at 77 K and 300 K. Single particle MC simulations are performed for a strained silicon nMOS structure at room temperature. Both calculations show saturation of mobility enhancement in strained silicon beyond germanium mole fraction of 0.2.

I. INTRODUCTION

The scaling of silicon MOSFET channel lengths to increase functional density, speed and drive current has pushed the limit of gate length towards 0.1 \textmu m. An alternate approach to enhance drive current is through increasing the mobility of the charge carriers, that is the electrons or holes in the channel. Recent reports have shown that electron mobility is enhanced when electrons flow in a strained silicon channel pseudomorphically grown on relaxed Si\textsubscript{1-x}Ge\textsubscript{x} [1-4]. These demonstrations of higher mobility hold significant promise for silicon based heterostructure devices in high speed digital and analog applications. When silicon epi-layers are pseudomorphically grown on a (001) Si\textsubscript{1-x}Ge\textsubscript{x} substrate, the epi-layers experience a biaxial in-plane extension. This tensile biaxial strain raises the energy of the four conduction band minima on the in-plane <100> axes with respect to the two minima on the <001> axes. The magnitude of separation of the two sets of valley minima has been measured to be $\Delta E \approx 0.67 \times x \text{ eV}$, where $x$ is the Ge mole fraction [2]. This results in a redistribution of the electrons between the two sets of valleys, with more electrons populating the lower valleys, where they exhibit a lower effective mass for transport. The enhancement of mobility is due to the realization of lower effective mass transport and the suppression of intervalley scattering because of the lifting of the degeneracy. In this paper, we report a detailed investigation of the electron transport in strained silicon using a fitted-band MC code [5] for bulk simulation and a MC tool that includes channel quantization effects in nMOS structures. Single particle simulation results are presented for both 300 K and 77 K. In the case of nMOS structures, a single particle MC simulation is performed by solving a two-dimensional multi-subband Boltzmann Transport Equation (BTE). Effective mobility enhancement in strained-silicon nMOS is compared with recent experimental results.
II. MODELS

As stated earlier, the tensile biaxial strain splits the conduction bands along \( A \). The bands along \([001] \) and \([00-1] \) are lowered in energy with respect to the mean energy while bands along \([100], [T00], [010] \) and \([010] \) are raised in energy. The bands along \( L \) are unaffected since there is no strain tensor along this direction. The MC simulator for silicon, SLAPSHOT [5], has been modified and enhanced to investigate electron transport in strained-Si (001) layers grown on relaxed (001) substrates. The scattering rate computation is based on a nonlocal pseudopotential band structure. The effective masses and nonparabolicities of different valleys are assumed to be unaffected by the strain [2]. Changes of the phonon coupling constants for the various scattering modes due to strain and other second-order effects are ignored. Acoustic intravalley and intervalley phonon scattering, optical phonon scattering, ionized impurity scattering and carrier-carrier scattering are included. Ionized impurity scattering is taken into account for a background doping concentrations of \( 10^{14} \) cm\(^{-3} \) and an electron density of \( 10^{18} \) cm\(^{-3} \) is used to approximate the sheet charge of \( 10^{12} \) cm\(^{-2} \) in a 10 nm modulation doped channel.

In the second part of the work, where the quantization effects are included, the subband structure is calculated by applying a formalism based on the effective-mass approximation with a bulk non-parabolic \( E(K) \) relation, as described in detail in [6] for the case of silicon MOSFETs. In the Si/SiGe system of interest, the following zeroth-order effective-mass equation applies:

\[
-\frac{\hbar^2}{2m_z^{(i)}} \frac{\partial^2}{\partial z^2} + E_c^{(i)}(z) + \gamma_{2D}^{(i)}(k_x, k_y) \zeta(z) = E\zeta(z),
\]

where

\[
\gamma_{2D}^{(i)}(k_x, k_y) = \frac{\hbar^2 k_x^2}{2m_x^{(i)}} + \frac{\hbar^2 k_y^2}{2m_y^{(i)}}.
\]

It is readily seen that using identical effective masses for Si and SiGe allows \( \gamma_{2D} \) to be dropped globally and renders \( m_z \)-independent. As a result, the calculation of the subband dispersion relation in the Si/SiGe system is essentially the same as in silicon nMOS except that the bulk conduction-band offset \( \Delta E \) at the Si/SiGe interfaces must be included in the potential term \( E_c(z) \) in the final 1D effective-mass Schrödinger equation. As in the case of silicon, the resulting subbands can be classified into ladders according to the bulk valleys from which they originate. For the strained channel, the values of the conduction band offsets were found to be \(-0.45 \times x\) eV for the twofold out-of-plane bands and \(0.22 \times x\) eV for the fourfold in-plane bands [2]. The scattering of the 2D electrons due to bulk phonons and surface roughness is included. In the phonon scattering rate calculation, we have adopted the formalism developed by Price [7]. Scatterings due to ionized impurities, being important only in the presence of large interface charge density or when the channel is weakly inverted, are ignored. Surface roughness scattering is modeled with a simple formalism introduced by Cheng [8].

In the case under study, a long-channel strained-silicon nMOS structure with uniform substrate doping concentrations and zero source-drain bias is simulated. For a given substrate doping concentrations, the gate voltage is ramped from weak inversion to the strong inversion regime. At each gate bias, a two dimensional potential profile for the device is obtained by solving the drift-diffusion (DD) and Poisson equations using the DD formalism in UT-MINIMOS [10]. Since a uniform inversion layer is assumed, a slice along the depth is selected at the middle of the channel. One dimensional Schrödinger and Poisson equations are then solved simultaneously for the slice in order to calculate subband structures. This is followed by a single particle MC simulation, where a single particle is launched according to the density-of-states (DOS) weighted equilibrium distribution. An adequate number of subbands (fifteen subbands in this study) and an upper energy boundary are chosen in order to avoid running out of DOS in the simulation. The Pauli exclusion principle is included in the region of ohmic transport, where scattering rates are reduced due to the reduced availability of final states.
III. RESULTS OF THE SIMULATION

The results presented here were obtained from simulations for fixed fields at 77 K and 300 K for the bulk case. The fields were applied along the <100> and <001> directions. The mobility of the electrons in the strained-silicon depends on the direction of the field due to the valley repopulation. When the field is applied along the <100> direction, the lighter transverse mass transport is realized. On the other hand when the field is applied along <001>, the heavier longitudinal mass reduces the electron mobility. The mobility for the field along <001> is even lower than that of unstrained silicon due to the heavier-effective-mass transport. Since fields along <100> offer superior transport characteristics, we have chosen to compare the results of transport simulations for strained and unstrained silicon along this direction. Fig. 1 displays the velocity-field characteristics at 300 K for several mole fractions of relaxed Si<sub>1-x</sub>Ge<sub>x</sub> for different germanium mole fractions at 300K. Mobility enhancement saturates beyond x = 0.2 found to be about 26000 cm<sup>2</sup>/V·s for strained silicon and 19000 cm<sup>2</sup>/V·s for unstrained intrinsic silicon. The low-field mobility in strained silicon is about 1.4 times that of unstrained intrinsic silicon. This corresponds to the ratio of conductivity effective masses of strained and unstrained silicon. This result is in good agreement with [2]. At 77 K, the suppression of intervalley phonons becomes insignificant due to the sharp reduction of intervalley phonon scattering at such low temperatures, hence the enhancement is lower as compared to the 300K case. As fields increase, electrons are distributed over the entire BZ and the curves merge. The saturation velocity obtained in our simulations is about 1.2 × 10<sup>7</sup> cm/s at 77 K.

The MC simulations for the nMOS structure were performed at room temperature. The enhancement of electron effective mobility in strained silicon channel nMOS structures as compared to conventional silicon nMOSFETs is presented for different strain conditions in Figure 3. This result compares well with the enhancement of mobility. The low-field mobility is about 2640 cm<sup>2</sup>/Vs at a Ge mole fraction of 0.3 as compared with that of unstrained silicon, 1450 cm<sup>2</sup>/Vs. Recently reported room temperature Hall mobility has achieved the record high result of 2700 cm<sup>2</sup>/V·s for a Ge mole fraction of 0.3 [2]. The velocity-field characteristics for several Ge mole fractions at 77 K are shown in Fig. 2. The low-field mobility is
FIGURE 3 Effective mobility as a function of effective field for different strain conditions in a strained silicon nMOSFET. Significant enhancement of mobility is observed over a wide range of transverse fields.

experimentally observed mobility enhancement that shows the saturation of mobility enhancement beyond germanium mole fractions of 0.2 [9].

IV. CONCLUSIONS

In this present work, the electron transport in strained silicon pseudomorphically grown on relaxed Si\textsubscript{1-x}Ge\textsubscript{x} (001) is explored using a MC tool. The drift velocity is calculated for different amounts of strain at 300 K and 77 K using a bulk MC simulator. Superior transport characteristics are simulated for strained silicon along <100> as compared to unstrained silicon. This is because of the reduction of intervalley phonon scattering due to the splitting of the six-fold degeneracy of the bands and to transport characterized by the lighter transverse mass. Low field mobility shows significant enhancement for the strained case, however, only a slight increase in the saturation velocity is observed. Effective electron mobility in strained layer nMOS structure has also been studied for different strained conditions. These tools will be used in future work to design and analyze strained Si/Si\textsubscript{1-x}Ge\textsubscript{x} devices.

Acknowledgements

This work was sponsored, in part, under the Joint Services Electronics Program, Contract Number F49620-95-C-0045, by the Semiconductor Research Corporation, by Motorola and by the Texas Advanced Technology Program. The United States Government is authorized to reproduce and distribute reprints for governmental purposes notwithstanding any copyright notation hereon.

References
