Cognitive Radio RF: Overview and Challenges

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Cognitive radio system (CRS) is a radio system which is aware of its operational and geographical environment, established policies, and its internal state. It is able to dynamically and autonomously adapt its operational parameters and protocols and to learn from its previous experience. Based on software-defined radio (SDR), CRS provides additional flexibility and offers improved efficiency to overall spectrum use. CRS is a disruptive technology targeting very high spectral efficiency. This paper presents an overview and challenges of CRS with focus on radio frequency (RF) section. We summarize the status of the related regulation and standardization activities which are very important for the success of any emerging technology. We point out some key research challenges, especially implementation challenges of cognitive radio (CR). A particular focus is on RF front-end, transceiver, and analog-to-digital and digital-to-analog interfaces which are still a key bottleneck in CRS development.

1. Introduction

The information and communication technology industry is today faced with a global challenge: develop new services with improved quality of service (QoS) and at the same time reduce its environmental impact. Clearly, there is a deep need of global efficiency not only in the energy domain, but also in the spectral domain.

Indeed, the gap between wireless supply and demands widens. It is feared that an imminent spectrum crisis, in which exploding demand from smart phones will soon overwhelm the wireless capacity, will occur. The problem is the lack of new spectrum available to wireless data carriers. Smartphone data traffic is growing so fast that if nothing is done they will use up the available spectrum. This just affects not only smart phones but also all wireless devices. A huge amount of spectrum is required for broadband use in the future as suggested in National Broadband Plan (NBP) in USA and the digital agenda in Europe.

In order to address the problem of spectrum usage efficiency, the cognitive radio (CR) concept was proposed [1, 2]. The detailed definition of cognitive radio systems (CRS) will be given in Section 2. Cognitive radio technology has the potential of being a disruptive force within spectrum management.

A very popular example is opportunistic radio or opportunistic spectrum access whose principle is temporal, spatial, and geographic “reuse” of licensed spectrum as shown in Figure 1 where an “unlicensed” secondary user (SU) can be permitted to use licensed spectrum, provided that it does not interfere with any primary users (PUs). In that way, the efficiency of spectrum usage is significantly improved. Various measurements of spectrum utilization have shown that spectrum is underutilized, in the sense that the typical duty cycle of spectrum usage at a fixed frequency and at a random geographical location is low. This means that there are many “holes” in the radio spectrum that could be exploited [3]. Opportunistic radio system should be able to exploit these spectrum holes by detecting them and using them in an opportunistic manner. Because of the outstanding propagation characteristic in the television (TV) bands with strong wall and floor penetration capability, long
range, and flexible bandwidth, it could be used to allow a brand-new class of services and increase the limited capacity of existing systems.

But CRS is not only limited to opportunistic spectrum access. Also, it includes heterogeneous networks where a heterogeneous radio framework management is performed. Current spectrum allocation approaches, fixed by nature, do not allow for the allocation of frequency bands to different radio access technologies (RATs) dynamically. However, the coexistence and cooperation of diverse technologies, which form part of a heterogeneous infrastructure, have brought about the possibility of flexibly managing the spectrum in a dynamic manner. No longer are fixed frequency bands guaranteed to apply to specific RATs, but conversely, through intelligent management mechanisms, bands can be allocated to RATs dynamically in a way such that the capacity of each RAT is maximized and interference is minimized. On long terms, will be considered also the flexibility in spectrum management where network operator may employ different RATs dynamically over time/frequency/location and acquire or exchange the spectrum usage rights. The devices may autonomously and dynamically adapt to the diverse heterogeneous radio networks [4].

This new technology opens up many potential applications and exciting opportunities [5]. For example, the rural connectivity, content distribution networks, city and campus wide coverage, and giant wireless hotspots could benefit from the new spectrum access and management.

This paper is organized as follows: after this introduction, the definition and high level concept of CRS is presented in Section 2. In Section 3, the status on regulation and standardization activities are described. Section 4 will present the research challenged related to CRS. Section 5 will describe the implementation challenges of CR devices. The conclusion of the paper is drawn in Section 6.

2. Definition and High Level Concept

There are different definitions of CRS, from many authors and organizations. The definition giving the common understanding about CRS and now adopted for most is from International Telecommunication Union (ITU) [6]. CRS is a radio system employing technology that allows the system:

(i) to obtain knowledge of its operational and geographical environment, established policies, and its internal state (cognitive capability);

(ii) to dynamically and autonomously adjust its operational parameters and protocols according to its obtained knowledge in order to achieve predefined objectives (reconfigurable capability);

(iii) to learn from the results obtained (learning capability).

At high level concept presented in Figure 2, the main components of the CRS are the intelligent management system and reconfigurable radios [7, 8]. CRS is also able to take action including obtaining knowledge, making decision, reconfiguration, and learning. The knowledge used by the CRS includes knowledge about operational radio and geographical environment, internal state, established policies, usage patterns, and users’ needs.

The methods to obtain this knowledge include getting information from component of the CRS, spectrum sensing, access to the cognitive pilot channel (CPC), geolocation, and database access. Using the obtained knowledge, the CRS
dynamically and autonomously makes reconfiguration decisions according to some predefined objectives, for example, in order to improve efficiency of spectrum usage. Based on the decisions made, the CRS adjusts operational parameters and protocols of its reconfigurable radios. Such parameters include output power, frequency range, modulation type, and radio access technology (RAT) protocols.

Software-defined radio (SDR) approach is used to implement the reconfigurations. Also, the CRS can learn from its decisions to improve its future decisions. The results of learning contribute to both obtaining knowledge and decision making.

CRS can be classified into two types: heterogeneous CRS and spectrum sharing CRS. The first type uses the network centric approach where one or several operators operate several radio access networks (RANs) using the same or different RATs. Frequency bands allocated to these RANs are fixed. Cognitive network optimizes radio resources and improves the QoS. The second type of CRS is sharing CRS, where several RANs using the same or different RATs can share the same frequency band by using the unoccupied subbands in an intelligent and coordinated way. Most of standardization activities are related to this type of CRS.

3. Status in Regulations and Standardizations

3.1. Regulations. The major regulatory agencies are developing rules for the unlicensed use of TVWS such as the FCC in the United States, Ofcom in the UK, and the Electronic Communications Committee (ECC) of CEPT in Europe.

The FCC provided the final rules for TVWS in 2010 [9]. There is ongoing proceeding for secondary use of the 2.36 GHz to 2.4 GHz band for medical area networks. Other opportunistic spectrum access beyond the already completed TVWS proceedings and cognitive techniques to better utilize the radio spectrum are currently under investigation.

Ofcom has also made significant progress in developing regulations for the TVWS with a first public consultation in 2009 [10]. The statement on white spaces devices and implementation of geolocation databases was released on September 1st, 2011 [11]. The detailed rules will be released in the future.

The ECC studied the technical and operational requirements for the operation of CRS in the WS of the UHF broadcasting band (470–790 MHz) [12]. This work is used as the starting point for regulatory activities within the ECC.

3.2. Standardizations. Currently international standardization of CRS is performed at all levels (ITU, IEEE, ETSI, and ECMA) [7, 13]. They are considering multiple deployment scenarios and business directions.

In ITU, Working Party (WP) 1B has worked on the definition of SDR and CRS and their relationship and summarized the technical and operational studies, and relevant recommendations. It has considered the SDR and CRS usage scenarios in different radio services and regulation...
implications. The WP 5A is currently addressing the definition, description, and application of CRS in the land mobile service.

IEEE is very active in CRS. In 802 WGs (LAN/MAN), the activity to define CRSs is currently performed in the 802.11 and 802.22, while the activity to specify components of a CRS is currently performed in 802.19, 802.21, and 802.22. 802.11y is an amendment for 3650–3700 MHz operation in USA defining new regulatory classes, transmit power control, and dynamic frequency selection for 802.11 to share frequency bands with other users. Draft standard P802.11af is an amendment for TVWS operation defining standardized modifications to both the 802.11 physical (PHY) layers and medium access control (MAC) sublayer to meet the legal requirements for channel access and coexistence in the TVWS. Draft standard P802.19.1 concerns TVWS coexistence methods. IEEE 802.21 focuses on media independent handover services enabling the optimization of handover between heterogeneous IEEE 802 networks, and facilitating handover between IEEE 802 networks and cellular networks. The draft standard P802.22 is on policies and procedures for operation in the TV bands. It specifies the air interface, including the cognitive MAC and PHY, of point-to-multipoint wireless regional area networks, operating in the unlicensed TV bands between 54 MHz and 862 MHz. Draft standard P802.22.1 is to enhance harmful interference protection for PUs operating in TV bands.

The IEEE DySPAN standards committee, whose predecessor is the IEEE P1900 standards committee, develops IEEE standards for radio and spectrum management with focus on improved use of spectrum. Its working groups and resulting standards numbered in the 1900 range propose new techniques and methods of dynamic spectrum access (DSA), which requires managing interference and coordination of wireless technologies and includes network management and information sharing. The IEEE 1900.1 standard related to terminology and concepts was published in 2008. The IEEE 1900.4 published in 2009, defines management system to support network-terminal distributed optimization of radio resource usage and improvement in QoS in heterogeneous wireless networks. The IEEE 1900.4a working group is defining architecture and interfaces for DSA networks in TVWS. The IEEE 1900.5 standard is dedicated exclusively to policy language requirement and architecture. The IEEE 1900.6 standard deals with spectrum sensing interfaces and data structures. IEEE P1900.7 is a draft standard on radio interface for white space dynamic spectrum access radio systems supporting fixed and mobile operation. It specifies a radio interface including MAC sublayer and PHY layer of white space dynamic spectrum access radio systems supporting fixed and mobile operation in white space frequency bands, while avoiding causing harmful interference to incumbent users in these frequency bands. The standard provides means to support P1900.4a for white space management and P1900.6 to obtain and exchange sensing-related information (spectrum sensing and geolocation information). Recently the ad hoc on dynamic spectrum access in vehicular environments was created. The purpose is to consider interest in, feasibility of, and necessity of developing a standard radio interface for vehicular communications.

The ETSI Reconfigurable Radio Systems (RRS) Technical Committee (TC) is also active in standardizing SDR and CRS [14]. TC RRS main responsibility is to carry out standardization activities related to reconfigurable radio systems (RRS) encompassing both SDR and CR with a focus on specific needs of the European Regulatory Framework, and CR/SDR TV white space standards adapted to the digital TV signal characteristics in Europe. Two out of the four working groups within ETSI RRS have activities resulting in standardization of potential regulatory aspects of CRS and SDR. Working group 3 has proposed and investigated the feasibility of standardizing a functional architecture for management and control of reconfigurable radio systems and cognitive pilot channel. SDR-related standardization is considered for both base station and mobile device. Working group 2 relies mainly on mobile device SDR related interface standardization. ETSI RRS is also working on operation in WS frequency bands and coexistence architecture for cognitive radio and investigating security and threats issues.

ECMA-392 released in 2009, specifies MAC and PHY for personal/portable cognitive wireless networks operating in TVWS, a MUX sublayer for higher layer protocols and a number of incumbent protection mechanisms.

3GPP is also interested in standardizing CR-like features in its future releases. For example, the idea of a cognitive reference signal is proposed through which each RAN can broadcast the interference level, frequency bands, and RATs of other networks, and other information that can help newly joined user equipment to choose the best RAN.

We summarize different aspects of standardization for CRS in Table 1. There are a lot of standardization activities for CRS with a lot of overlapping. However, many open issues have not been covered at all until now, especially the security issues.

4. Research Challenges

CRS covers multidisciplinary areas attracting a large number of research works with many interesting obtained results.
The challenges remain numerous, namely, intelligence distribution and implementation, delay/protocol overhead, cross-layer design, security, sensing algorithms, and flexible hardware design. Due to the huge amount of published papers and the interdisciplinary nature of the topic, it is very difficult to provide an exhaustive analysis of all research works available on CRS. The purpose of this section is therefore to briefly describe challenges which are yet open and current under debate in the framework of research on CRS.

### 4.1. Decision Making

As CRS is driven by a decision making, the first relevant research challenge is where and how the decision (e.g., the decision on spectrum availability, strategy for selecting channel for sensing or access, or how to optimize radio performance) should be taken. The first issue is directly related to whether the cognitive process should be implemented in a centralized or distributed fashion. This aspect is more critical not only for cognitive networks, where intelligence is more likely to be distributed, but also for cognitive radios, as decision making could be influenced by collaboration between them and also with other devices. The second issue is the choice of the decision algorithms (e.g., neural networks, genetic algorithms, ant-colony optimization, etc.) which should be customized to fulfill the CRS requirements.

### 4.2. Learning Process

Research in machine learning has grown dramatically recently, with significant amount of progress. One of the important aspects of the learning mechanisms is whether the learning performed is supervised or unsupervised. In the context of a CRS, either technique may be applied. The first challenge of learning is to avoid wrong choices before a feasible decision, especially in autonomous or unsupervised learning process. The second issue is to concretely define learning process in the context of CRS, its objectives and contributions.

In terms of implementation and algorithm design, the cognitive functionalities, which are related to enabling devices or networks to learn from past decisions to improve their behavior, are too much complex. The design of the learning algorithm represents by itself a challenge, and measurements which should be employed by learning open new issues related to which measurements to use and how to perform them.

### 4.3. Cross-Layers

While the aspect of interprotocol interaction is included in the concept of cognitive network as means to support user and applications requirement, no relevant and comprehensive analysis is available to address the performance and, in general, the behavior of applications and networks based on CRS technology. The design of cognitive or self-organized network is itself a challenging task, in particular, the outer and inner loops coordination, the networking middleware for knowledge exchange, and intersystem networking for sharing and cooperation.

Challenge is also in the design of high layers including MAC sublayer and network layer, spectrum management functions integrated at the different layers of the network protocol stack, cognitive radio resource management and coordination, various protocols and routings. Many technologies will be using multiple frequency bands. As a result, challenges in interoperability, including coexistence, cooperation and collaboration for devices, and networks signaling with cross-layer interfaces and interlayer signaling are to be solved.

Although many papers deal with the cross-layer design, the issues addressed are still specific and tailored to a given technology with the lack of its general. We need to provide cross-layer designs for more general classes of communications schemes.

### 4.4. Security

The challenges of employing CRS include that of ensuring secure devices operations. Security in this context includes enforcement of rules. Enforcement for static systems is already a challenge due to the amount of resources necessary to authorize equipment, the requirement of obtaining proof that violations have occurred, and the determination of the violator’ identities. As the systems become more dynamic, there is an increase in the number of potential interaction that can lead to a violation. Additionally, this leads to a decrease of the time and special scales of these interactions. Both of these changes will amplify the enforcement challenges.

The first issue is on equipment authorization, especially on evaluation criteria and security certification. It becomes even more problematic with the employment of self-learning mechanisms. Software and hardware certification will not provide sufficient assurances that the device conforms to the operational envelopes.

Software certification and the security of the software are also challenging area, especially when software provides the control of dynamic systems. The security of that software is critical to ensure that rogue behavior is not programmed into the devices.

The number of combination of interactions is high and the mobility and the agility of CRS is great, so the monitoring mechanisms are challenging tasks.

Also, security is to deal at the protocol layer with key exchanges which are very adapted to a highly reliable physical layer and centralized network. However, in CRS with distributed cognitive networks, traditional cryptographic schemes are not adapted.

### 4.5. Sensing

Following challenge is about spectrum sensing, especially on the accuracy on spectrum occupancy decision, sensing time, and malicious adversary, taking into account the fundamental limits of spectrum sensing algorithms due to noise uncertainty multipath fading and shadowing [15]. In order to solve hidden PU problem and mitigate the impact of these issues, cooperative spectrum sensing has been shown to be an effective method to improve the detection performance by exploiting spatial diversity in the observations of spatially located CRs [16]. Challenges of cooperative sensing include reducing cooperation overhead, developing efficient information sharing algorithms. The
coordination algorithm for cooperation should be robust to changes and failures in the network, and introduce a minimum amount of delay.

The most prominent hardware trial for spectrum sensing thus far has been the FCC field trial conducted in 2008 by the office of engineering and technology [17]. Although the spectrum sensing approach exhibited good sensitivities satisfying stringent regulation requirements, the future spectrum sensing hardware should improve the receiver selectivity and receiver desensitization, especially when the adjacent channels have high powers. The geolocation database-based approach is able to identify occupied channels with 100% accuracy. However, for identification of unoccupied channels, it did not exhibit the best performance, presumably due to incomplete information in the database. This shows that the spectrum sensing alone works to some degree, but the performance could be further enhanced especially in the identification of occupied channels. Combining a geolocation database with spectrum sensing may be a better option provided that the CR device cost and power dissipation are decreased.

4.6. Geolocation. Geolocation is an important CR enabling technology due to the wide range of applications that may result from a radio being aware of its current location and possibility being aware of its planned path and destination. When CRS uses the geolocation technology for location determination combined with a database look-up, each access point (AP) may be connected to one, or multiple databases which provide information on the unused TVWS channels that are available at the location of AP and they provide also information on maximum transmit power levels usable in each channel. Furthermore, the use of master-slave technology is encouraged so that the necessary functionalities for database lookup and channel selection need to be implemented only in APs. It keeps the complexity and cost of end-user devices to a minimum.

However, the challenges in this area are who and how to implement the data base, how to feed it. Providing incumbent (or PUs) databases requires knowledge of the locations of CR devices whose precision should be specified. If global positioning service (GPS) is equipped and CR devices are outdoor, obtaining their geolocations may still be less a technical challenge. If no GPS is available or if CR devices are indoor, then obtaining geolocations becomes a challenging task.

5. Implementation Challenges

Although the theoretical research for CRS is blooming, with many interesting results, hardware implementation and system development are progressing at a slower pace, because of the complexities involved in designing and developing CRS [17]. In this section, we present the implementation challenges of CR in the system on chip (SoC) integration’s perspective.

The first SDR architecture was proposed by Mitola and Maguire [1], in which the RF and analog processing are reduced to only a pair of data converters, thus providing the maximum flexibility and programmability through the digital processing block. This idealistic approach, however, suffers from the poor tolerance of to the interferers. In many wireless applications, a small desired signal could be accompanied by several large in-band signals created by nearby transmitters of the same communication standard or out-of-band blockers caused by any transmitter. At times, these blockers could be as much as about 100 dB larger than the desirable received signal, which, due to the lack of any filtering in this idealistic approach would demand an impractical dynamic range of about 100 dB on the ADC. This long-term requirement is very far beyond the limits of the technology available as will be shown in ADC/DAC challenges subsection. Research on high-performances ADC/DAC is going on with significant progress, especially with hybrid-filter bank and time-interleaving architectures.

For short- and midterm implementation, the block diagram of a CR may be presented in Figure 6. In order to operate on very wide band or multibands simultaneously, parallel processing is employed from antennas to analog to digital interfaces as shown in Figure 3. Multiantennas are necessary for multi-input-multipoutput (MIMO) operation and/or multibands operation. After antennas, a passive module is used for switching or duplexing, RF filtering, and impedance matching between antennas and power amplifiers (PAs). This module is composed of a range of submodules in order to cover a wide bandwidth or enable simultaneous communications. Then multireceiver (Rx) and multitransmitter (Tx) are followed before a multi-ADC/DACs module. A high performance and very flexible digital baseband carries out not only all conventional processing for modulation and demodulation, coding and encoding, and so forth, but also digital filtering, dc offset cancellation, digital automatic gain control, calibration and correction of analog errors and non lineaities. Combining with control plane and sensor, a feedback from baseband to RF front-end and transceiver are necessary to boost the performance of the analog part. The challenges of RF front-end and transceiver in the short-/midterm are to reduce the off-chip and passive components, increase their frequency-agility, minimize the power dissipation, and reduce area. These challenges will be discussed more in detail in the next two subsections.

CR needs to adapt transmission and receiver parameters to avoid causing interference to PUs and maximize spectral efficiency. To avoid causing interference, numerous techniques can be used and combined such as frequency tuning (adaptive frequency hopping, dynamic frequency selection and RF band switching), OFDM subchannelization, channel aggregation, time multiplexing, power control, modulation and coding for QoS adaptability, beam-forming, and space-time coding for MIMO. To maintain link in adverse conditions, wide dynamic range especially for analog-to-digital converter (ADC) and high sensitive receiver with rapid adaptation to changes in interference temperature are required.

CR will be also based on strong cross-layer interactions. For example, the cognitive spectrum management involves
5.1. RF Front-Ends Transceiver Challenges. A key bottleneck in CR has always been and continues to be the frequency-agile RF front-ends that can easily be coupled with the parts of the CR that carry out the digital processing—be they pure software systems or a mix of hardware and software. This subsection presents short-/midterm solution/requirements for RF front-end transceiver of CR devices. The state of the art related to this topic is described and design challenges are also presented.

CR transceiver should be able to use any available band, adapt to multiple access methods and adaptive modulation scheme, switch quickly between links, and communicate with 2 or more points at a time. Therefore, the RF section needs to be particularly flexible. In addition, CR receiver should be able to sense unused frequency bands if necessary.

One of the most difficult engineering concerns in the RF portion of SDR/CR, much more difficult than in traditional OFDM modems (e.g., in simultaneous communications of diverse RATs), is handling very large peak-to-average power ratios (PAPRs) \[18\]. The first issue related to high PAPRs is the efficiency of the PA which suffers dramatically because the PA must be oversized in terms of its average power requirement. In addition to creating design problems for the PA, the high PAPR also requires highly linear upconverters leading to high power consumption.

OFDM modulation and simultaneous usage of multi-RATs are also very sensitive to the intermodulation distortion (IMD) that results from mild nonlinearity in the RF. For the RF components in the transmitter, the linearity requirement must be met at reasonably high power levels. With the associated requirement for high efficiency, this places extreme design challenges upon the architectural realization. For the RF components in the receiver, the power levels are much lower but the linearity requirement is even more challenging. This arises from the possible presence of adjacent channel interference from other radio systems, independent but closely located.

Resulting from the possibility of the interfering transmitter location being nearer than the desired transmitter, as well as the possibility of channel shadowing on the desired signal, the adjacent channel blocker is often received with a significantly stronger signal power level than the desired channel. The receiver selectivity becomes therefore more and more important. The linearity requirement for the receiver components must include this anticipated level of blocker power above the desired channel. CR receiver should have a wide dynamic range, that is with the ability to handle a large interferer whilst simultaneously receiving a small wanted signal. It means also that it is able to handle large amplitude signal whilst also having a low noise floor. Two direct consequences of this are difficulty in achieving acceptable noise figure performance for the overall receiver, and difficulty in achieving sufficient dynamic range for the ADCs.

As in OFDM system, phase noise and distortion should be taken carefully in SDR/CR, the problematic is almost the same. In terms of receiver requirements, it should exhibit a good sensitivity, leading to low noise amplifier (LNA)
achieving low noise factor (NF) (<3 dB) and filter design resulting in low insertion losses (<1 dB). It needs to achieve a high LNA and mixer linearity (IP3, IP1), adequate filtering, and low local oscillator (LO) phase noise, and spurious to have a good blocker immunity.

In transmitter, the key requirements are high PA linearity as explained above and low PA noise necessary for low adjacent channel leakage power (ACLP), high PA efficiency and heat removal, and low filter insertion loss to reduce power consumption.

Traditional front-end technology cannot handle these requirements because they are generally band limited, both for the form of modulation used and the frequency band in which they operate. Even so called multimode/multiband and wide band transceivers have limitations and generally operate by switching front ends as required [19].

Typically linearity, selectivity, phase distortion, and phase noise issues are addressed through the addition of costly and power-hungry external components including surface acoustic wave (SAW) filters and crystal oscillators that contribute to a higher system bill of materials (BOMs) and increased power consumption and lack of flexibility required for SDR/CR. The combination of innovative architectures and on-chip filters reduce BOM costs and power dissipation; however, the signal corruptions previously discussed can become problematic.

Solutions for improved performance in SoC’s perspective include process selection, innovative architectures, the use of on-chip passive components when possible. Although there are some advantages to using a GaAs process for higher linearity or a bipolar process to reduce noise for RF signal processing, the all CMOS radio is the preferred choice for cost, especially when designing radios with significant digital subsections as the case in SDR/CR. Innovative architectures use complex domain RF and digital signal processing techniques to mitigate the effects of analog imperfections and smart calibration sequences at adequate timeframe to improve performance of the system. In the following, we will present some research directions in all CMOS radio for SDR/CR and associated challenges.

As shown in Figures 4 and 5, CR transceiver architecture can be wideband radio or tunable radio. Considering wideband radio, the challenges are not only on ADC/DAC which is presented in the next subsection, but also the high dynamic range and high linearity LNA, and narrowband impedance matching network. Considering tunable radio, the key issues
are related to the frequency-agile antenna, duplexer, and passive filters. In this direction, some solutions have been proposed, for example, a tunable integrated duplexer in [20], tunable RF BAW filters in [21], using metamaterials as BST in [22], or RF MEMS [23]. However the performances are not good enough and the tuning range is quite small. The challenge is still a low-loss pass-band filter with a small size which has an electrically tunable center frequency with a wide tuning range (of 20% would be truly disruptive).

An alternative solution for tunable receiver performing blocker rejection is based on circuit architecture using translational loops with feedforward structure [24]. However, it requires very high linearity LNA while sacrificing the NF and introducing an LO leakage issues. The feedback structure proposed in [25] achieves higher out of band (OOB) linearity while maintaining a lower NF. Another solution improving the OOB linearity and the receiver flexibility has been proposed in [26], using both direct conversion receiver and continuous-time delta sigma converter with upconverted RF feedback loop but for a single band and at the cost of a substantial increase of noise figure due to the feedback loop. The OOB filtering improvement is limited to 15 dB in practice which limits the OOB linearity enhancement. The future design challenge for [25, 26] is related to higher bandwidth operation (up to 40 MHz). In addition concerning [26], future design challenges are also related to multiband capability, OOB filtering enhancement without compromising the noise figure performances and the power consumption.

Receiver architectures particularly suited to partial SDR make use of impedance reflection from baseband to RF port. Connecting the antenna directly to a passive mixer without an RF LNA provides very increased tuning range and linearity [27]. Complex mixer-first, LNA-less architecture [28], provides very good OOB linearity and low NF with baseband programmable RF band-pass filter, wide tunable frequency range, and complex impedance matching. The design challenge for this approach is related to the extension of frequency range (up to 6 GHz) without degradation of performances.

The RF transmitter limitations are presented in Figure 6. With the success of the all-digital techniques for RF frequency synthesis in all-digital PLL (ADPLL) [29] and the apparition of digital PA concept showing direct reconstruction of the signal envelope in the RF domain, mostly digital transmitters have been demonstrated such as digital polar transmitter [30] and digital transmitter using direct modulation [31]. Although the design of almost digital transmitter is not as challenging than that of frequency-agile receiver, some issues are still remaining, especially decreased ACLP, increased PA efficiency using digital predistortion technique.

5.2. ADC and DAC Challenges. In ideal SDR and CR, the RF signals are converted into the digital domain as close to the antenna as possible. In this way all the processing is handled by the digital signal processing. In this case, not only must the ADC and DAC have a 100 dB dynamic range (with NF = 4 dB, noise floor in 10 MHz is at ~100 dBm, interference and maximum blocker are supposed at 0 dBm), and be able to operate over a very wide range, extending up to 6 GHz at least, but the transmitter must be able to handle significant levels of power. The power consumption of the ADC could be 1 kW if the 1 pJ/conversion is assumed.

As illustrated in Figure 7, these requirements are very far beyond the limits of the technology available [32]. Therefore, as mentioned earlier, realistic CR receivers for short-/midterm are based on downconversion and filtering.
in front of the ADC to reduce both the required dynamic range and the conversion bandwidth.

For long-term research, wideband radio with very few amount of off-chip and passive components reduce analog signal processing and is still a desired solution. In this case, the challenge is mainly on ADC/DAC. In this optic, parallelism can be used in order to widen the conversion bandwidth. Parallel continuous-time ΔΣ ADC presented in [33] needs very complex digital synthesis filters which become even more complex when the channel number is increased. Hybrid filter bank based ADC [34] could become even more complex when the channel number is increased. Hybrid filter bank based ADC [34] could be a promising solution for CR applications. However it is really sensitive to analog filter errors and imperfections, necessitating high resource calibration technique. The most natural solution employs time-interleaving [35] giving outstanding performance in terms of speed. But it lacks the resolution and dynamic range. The challenges are background calibration of frequency-dependent channel mismatch and time skew errors corrections without the need for any special calibration signal or postproduction trimming. Digital postlinearization is desired to suppress low order nonlineairities of parallel ADCs and nonlinearities caused by preADC analog components. Another technique is to combine time multiplexing and frequency multiplexing by using band-pass charge sampling filters as analysis filters in hybrid filter banks architecture [36, 37]. This leads to the reduced complexity of analog analysis filters, and at the same time the sensitivity to analog errors and imperfections are reduced. However, a deeper investigation on practical implementation to widen the bandwidth and sensitivity is really needed.

Almost digital RF transmitters employ an RF-DAC [30, 31] As shown in Table 2, the embedded CPU hosts are more and more powerful. As a result, a part of the digital filters in addition to the demodulation functions are implemented in the software domain. This is already the case in low latency systems as GPS and digital broadcast radio system. This trend is expected to continue paving the way of "true" SDR receiver.

In SDR/CR, the baseband architecture requires dynamic reconconfigurations, high computational demand, up to 1 TOPS (100 GOPS are now required for 4G mobile terminal), and low power (roughly 500 mW) under real time constraints. In order to fulfill the unique requirements of embeded applications (real-time, low-power, and multitasking) and improve the flexibility, multiprocessor system-on-chip (MPSoC) has been introduced [38], network-on-chip (NoC) based architecture can be used to enable fast run-time reconfiguration and reduce the power consumption with distributed power management [39]. 3D NoC is a promising solution for increased modularity and scalability [40]. 3D stacking is a very interesting technology for SoC, however, the matching between 3D technology and multicore is not straightforward due to some limitations of 3D technology. Dynamic task management has been proposed in [41] to solve MPSoC scalability and programmability problem, and improve resource allocation and energy efficiency.

With real-time constraints, the challenge in baseband architecture and design is to enable flexibility with reduced overhead in terms of performances, powerconsumption, and silicon area by optimizing the tradeoff between performances (dynamic reconconfigurations, computational resources) and power efficiency, implementing efficient power management techniques and reducing the run-time management overhead with flexible/dynamic task management.

### 5.4. Spectrum Sensing Algorithm Implementation

Although, regulatory agencies beginning with FCC and then recently with Ofcom do not require the implementation of spectrum sensing as mandatory but only optional for white space devices operating in the TV bands as a secondary user, spectrum sensing stays a challenging task for CRS and very useful for a better spectrum management and would be a key feature for CR devices in the future. In terms of algorithms, the challenge is on the accuracy on spectrum occupancy decision, sensing time and malicious adversary, taking into account the fundamental limits of spectrum.

<table>
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<th>Technology</th>
<th>2007-8</th>
<th>2009-10</th>
<th>2011-12</th>
<th>Improvement</th>
</tr>
</thead>
<tbody>
<tr>
<td>ARM Processor</td>
<td>ARM11 470–700 DMIPS</td>
<td>Cortex-A8 1,200–2,000 DMIPS</td>
<td>Dual Cortex-A9 5,000 + DMIPS</td>
<td>10× + SMP</td>
</tr>
<tr>
<td>Ext. display</td>
<td>VGA</td>
<td>XGA</td>
<td>WUXGA + HDMI</td>
<td>8× + HDMI</td>
</tr>
<tr>
<td>Video</td>
<td>VGA–30 fps</td>
<td>720–30 fps</td>
<td>1080p–30 fps</td>
<td>7×</td>
</tr>
<tr>
<td>3D graphics</td>
<td>2 Mtri/s OpenGL ES1.1</td>
<td>10 + Mtri/s OpenGL ES2.0</td>
<td>20+ Mtri/s OpenGL ES2.0</td>
<td>10× + Pgm. shaders</td>
</tr>
<tr>
<td>Imaging</td>
<td>3–5 MP</td>
<td>8–12 MP</td>
<td>16–20 MP</td>
<td>7×</td>
</tr>
<tr>
<td>Audio</td>
<td>15 hrs</td>
<td>40 hrs</td>
<td>140+ hrs</td>
<td>10×</td>
</tr>
<tr>
<td>DDR memory</td>
<td>128–256 MB</td>
<td>256–512 MB</td>
<td>1–2 GB</td>
<td>8×</td>
</tr>
<tr>
<td>Mass storage</td>
<td>8–16 GB</td>
<td>16–32 GB</td>
<td>64–128 GB</td>
<td>8×</td>
</tr>
<tr>
<td>Process</td>
<td>90 nm</td>
<td>65/45 nm</td>
<td>45 nm/beyond</td>
<td>3+ nodes</td>
</tr>
<tr>
<td></td>
<td>FCC 2010</td>
<td>Ofcom 09</td>
<td>IEEE 802.22</td>
<td></td>
</tr>
<tr>
<td>------------------------------</td>
<td>----------</td>
<td>----------</td>
<td>-------------</td>
<td></td>
</tr>
<tr>
<td>Max. power adjacent to TV/other [dBm]</td>
<td>16/17</td>
<td>4/17</td>
<td>36</td>
<td></td>
</tr>
<tr>
<td>Adjacent-channel emission [dBc]</td>
<td>−72 dBc*</td>
<td>−46 dBc</td>
<td>−49 dBc</td>
<td></td>
</tr>
<tr>
<td>Digital TV sensing threshold [dBm/MHz]</td>
<td>−114/6</td>
<td>−120/8</td>
<td>116/6</td>
<td></td>
</tr>
<tr>
<td>Wireless microphone sensing threshold [dBm/MHz]</td>
<td>−107/0.2</td>
<td>−126/0.2</td>
<td>−10/0.2</td>
<td></td>
</tr>
<tr>
<td>Probability detection/probability false alarm</td>
<td>90%/10%</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Backoff time(s)</td>
<td>2</td>
<td>&lt;1</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>Sensing frequency (1/minutes)</td>
<td>1</td>
<td>60</td>
<td>30</td>
<td></td>
</tr>
<tr>
<td>Bandwidth</td>
<td>6</td>
<td>8</td>
<td>6, 7, 8</td>
<td></td>
</tr>
<tr>
<td>Transmitter power control</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Modulation</td>
<td>Free</td>
<td>Free</td>
<td>OFDM</td>
<td></td>
</tr>
</tbody>
</table>

* Measured in 100 kHz with reference to total power in 6 MHz.

sensing algorithms due to noise uncertainty multipath fading and shadowing and hidden PU problem as mentioned above.

A spectrum sensing algorithm is characterized by its detection, false alarm, and miss detection probabilities but also SNR regime, sensing time and frequency, and especially its implementation complexity.

In a practical implementation, it is interesting to build this function with a very low power receiver chain parallel to the main receiver path. Because of the SNR wall issue [15] and particularly the hidden node problem that should be taking and mitigated with additional margin in the threshold detection level, this function requires a very high quality RF receiver in terms of NF and linearity. Spectrum sensing algorithms are implemented in the baseband. In terms of implementation complexity and power consumption, it depends on the used algorithms, but it is quite simpler compared to conventional baseband signal processing for demodulation. The most challenging task is located in the RF front-end design, in particular low NF, high linearity, and wide dynamic range:

\[
\text{SNR}_{\text{min}}[\text{dB}] = P_{\text{min}}[\text{dBm}] - \text{NF}[\text{dB}] + 174[\text{dBm/Hz}] - 10\log_{10}B[\text{dB} \cdot \text{Hz}].
\]

The equation above represents the relation between the minimal SNR value (\(\text{SNR}_{\text{min}}\)) for a given algorithm and the minimal sensing sensitivity or threshold (\(P_{\text{min}}\)), the NF of the receiver (\(\text{NF}\)) where B is the channel bandwidth to be sensed.

As shown in Table 3, the spectrum sensing requirements are very stringent with very low sensing threshold. This sensitivity level is even lower in some cases in CEPT-SE43 (−169 dBm in the worst case). It leads to a very low NF requirement (less than 7 dB) and very high out of band linearity. Baseband implementation for algorithm itself must be optimized in terms of computational resources but also power consumption.

6. Conclusion and Discussion

This paper presented an overview and challenges of CRS with focus on implementation of cognitive radio. We summarized the status of the related regulation and standardization activity and then pointed out some key research challenges, especially implementation challenges of cognitive radio.

Having three key capabilities such as cognitive capability, reconfigurable capability, and learning capability, CRS has the potential of being a disruptive force within spectrum management. Using SDR for implementing reconfigurable radios, CRS provides additional flexibility and offers improved efficiency to overall spectrum use. It can exploit the spectrum holes or white spaces of licensed spectrum bands provided that it does not cause harmful interference to any primary users in order to significantly improve the efficiency of spectrum usage.

CRS offers also the possibility of flexibly managing the spectrum in a dynamic manner in heterogeneous radio access networks. Through intelligent management mechanisms, frequency bands can be allocated to RATs dynamically in a way such that the capacity of each RAT is maximized and interference is minimized. Network operator may employ different RATs dynamically over time/frequency/location and acquire or exchange the spectrum usage rights. The cognitive devices may autonomously and dynamically adapt to the diverse heterogeneous radio access networks.

CRS covers multidisciplinary areas attracting a large number of researches with many interesting obtained results. The challenges remain numerous, namely, intelligence distribution and implementation, security, delay/protocol overhead, cross-layer design, flexible hardware design, and so forth, CRS will always be limited by physically possible bounds. Limitations depend on the usage model, future standardization and highly on the carrier to noise ratio needed to decode the signal and the signal bandwidth. It is anticipated that semiconductor manufacturers will make steps to improve the selectivity, linearity, and agility performances. But these performances will lead to additional costs that could be offset by new design techniques, and
for the digital portions, denser CMOS node. The timeframe to make this happen is more linked to the economical and business model for CRS than real technological issues.

Acknowledgments

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