Analytical Model of Subthreshold Drain Current Characteristics of Ballistic Silicon Nanowire Transistors

Wanjie Xu, Hei Wong, and Hiroshi Iwai

1Department of Electronic Engineering, City University of Hong Kong, Tat Chee Avenue, Kowloon, Hong Kong
2Frontier Research Center, Tokyo Institute of Technology, Yokohama 226-8502, Japan

Correspondence should be addressed to Hei Wong; heiwong@ieee.org

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A physically based subthreshold current model for silicon nanowire transistors working in the ballistic regime is developed. Based on the electric potential distribution obtained from a 2D Poisson equation and by performing some perturbation approximations for subband energy levels, an analytical model for the subthreshold drain current is obtained. The model is further used for predicting the subthreshold slopes and threshold voltages of the transistors. Our results agree well with TCAD simulation with different geometries and under different biasing conditions.

1. Introduction

Performance improvement of metal-oxide-semiconductor field-effect transistors (MOSFETs) based on downsizing has now reached the bottleneck because of the severe short channel effects (SCE) and extremely large leakage current in ultrasmall scales. Several alternate structures other than the conventional planar MOSFET have been proposed [1–3]. It was shown that silicon nanowire transistor (SNT) with a surrounding gate architecture has excellent SCE immunity and is now considered to be one of the most promising candidates for future generations of nanoscale MOS technology.

Due to the superior gate control capability, the channel region of SNT needs not to be heavily doped and the penetration of source and drain electric field into the channel are still minimal. In addition, mobility degradation due to the impurity scattering of carriers in the channel can be greatly reduced also. Ultrashort channel structures further enable ballistic transport [4, 5]. Transistor working in the ballistic regime is considered to have the best electrical performance. However, subthreshold leakage is still the big concern for these devices. It will be more convenient for process and device engineers to estimate these effects by using a simple compact model as it did in the conventional device structure. The conventional drift–diffusion-based subthreshold current model will no longer be valid for the ultrashort SNTs [6, 7] as the ballistic transport has become the dominating mechanism and that makes the analytical modeling extremely difficult [8, 9]. In [10], a model for subthreshold characteristics of ballistic surrounding gate MOSFET is proposed. However, the boundary electric potential was extracted from numerical simulation without comprehensive analysis. This work aims at the development of an analytical model for the subthreshold drain current of SNT by taking the ballistic effect into account. The boundary potential is analyzed from the electron statistics point of view.

Our model is developed by firstly solving the two-dimensional Poisson equation to determine the potential distribution; subband energy levels are then obtained by using the first-order perturbation based on the available electric potentials. Finally drain current is obtained according to the Landauer theory by taking several major subband levels into consideration. The model is validated by comparing the results with TCAD simulation with Non equilibrium Green Function (NEGF) model [11]. NEGF approach has been widely used to characterize quantum confinement and ballistic transport in the ultrascale devices [10–14]. The organization of this paper is as follows. The modeling of electric potential in the channel from solving Poisson’s equation is presented in Section 2. In Section 3, the electrical characteristics of SNT, such as electric potential distribution,
subthreshold current, subthreshold slope, and threshold voltage roll-off, are presented along with the comparison with TCAD simulations. Finally, the major results are summed up in Section 4.

2. Modeling of Potential Distribution

The structures of the SNT and the coordinate system used in this work are shown in Figure 1. For the sake of simplicity, heavily doped source and drain regions are considered as ideal reservoirs. The channel region is undoped. It was found experimentally that ballisticity occurs in SNT with radius of 4 nm and channel length of about 20 nm [15]. This model uses the similar device dimensions. Typical values of device parameters used in the calculation and simulation are listed in Table 1. Considering angular symmetry, polar coordinates \( r \) and \( \theta \) were used. With the surrounding gate structure, the electric potential in the channel is predominately governed by the gate bias. While for very short channel, the influence of source and drain on electric potential cannot be neglected. To obtain the channel potential distribution, we solve the 2D Poisson equation in the undoped channel as follows:

\[
\frac{1}{r} \frac{\partial}{\partial r} \left( r \frac{\partial \phi}{\partial r} \right) + \frac{\partial^2 \phi}{\partial z^2} = \frac{q\eta_i}{\varepsilon_{Si}} e^{(\phi-V)/k_B T},
\]

where \( \phi(r, z) \) is the electric potential, \( q \) is the magnitude of electron charge, and \( \varepsilon_{Si} \) is the silicon permittivity. \( \eta_i \) is the intrinsic electron concentration of silicon; \( V \) is quasi-Fermi potential whose value depends on position in the channel. It equals zero at source and drain-to-source voltage (\( V_{DS} \)) at the drain side. Since the subthreshold drain current is mainly governed by the electrostatic near the source region, we evaluate the case when \( V = 0 \) V. Comparison made between numerical simulations also confirms the validity of this assumption in the subthreshold regime.

![Figure 1: Schematic of structure of an n-type SNT. (a) Cross section view and (b) cut plane along the channel. \( R \) and \( L \) are the radius and the length of nanowire, respectively. \( t_{ox} \) is the gate oxide thickness.](image)

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Typical values</th>
</tr>
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<tbody>
<tr>
<td>Source doping density, ( N_S ) (cm(^{-3}))</td>
<td>( 10^{20} )</td>
</tr>
<tr>
<td>Drain doping density, ( N_D ) (cm(^{-3}))</td>
<td>( 10^{20} )</td>
</tr>
<tr>
<td>Gate length, ( L ) (nm)</td>
<td>20</td>
</tr>
<tr>
<td>Gate oxide thickness, ( t_{ox} ) (nm)</td>
<td>1</td>
</tr>
<tr>
<td>Radius of nanowire, ( R ) (nm)</td>
<td>4</td>
</tr>
</tbody>
</table>

The boundary conditions for electric potential are

\[
C_{ox} \left[ V_{GS} - V_{FB} - \phi(R, z) \right] = \varepsilon_{Si} \frac{\partial \phi}{\partial r} \bigg|_{r=R}
\]

\[
\phi(r, 0) = V_S,
\]

\[
\phi(r, L) = V_D,
\]

where \( V_{GS} \) is the gate-to-source voltage and \( V_{FB} \) is the flatband voltage which equals the difference between the workfunctions of gate material and silicon. \( C_{ox} \) is the oxide capacitance per unit area. \( V_S \) and \( V_D \) are potentials in source and drain region, respectively. The difference of \( V_S \) and \( V_D \) is drain bias; that is, \( V_D = V_S + V_{DS} \).

\( V_S \) needs to be specified to obtain the electric potential distribution. In the conventional models of SNT, \( V_S \) is fixed as built-in voltage of source junction \( V_{bi} \). For the device considered in this model, \( V_{bi} = E_g/2 \), where \( E_g \) is the bandgap of silicon. In thin nanowires, electrons are confined to discrete subbands which are well above conduction band edge. In order to maintain the charge neutrality condition in the source region, these subband levels should be further pulled down to be occupied by more electrons. The difference of the subband levels from conduction band edge is approximated by energy levels in a cylindrical quantum well; that is,

\[
E_{mn}^D = \frac{j_{mn}^2 \hbar^2}{2m_e^* R^2},
\]


where $h$ is the reduced Plank constant and $j_{mn}$ is the $n$th root of $m$th-order first-kind Bessel function $J_m(x)$. $v$ represents the valley in which subbands reside, $m'_n$ is the radicaleffective mass for the valley concerned. Electron density is the statistics of each subband from Landauer theory \[16\]. Suppose the doping concentration in the source region is $N_D$; equation of charge neutrality is

$$\sum_{\nu,m,n} g_\nu \left( \frac{2 m'_n k_B T}{\pi h^2} \right)^{1/2} F_{-1/2} \left( \frac{E_{FS} - E_{mn}^\nu - q V_S}{k_B T} \right) = \pi R^2 N_D, \quad (5)$$

where all subband levels are assumed to be lowered by the source potential. $g_\nu$ and $m'_n$ are the degeneracy and the $z$-direction effective mass of the valley, respectively. $E_{FS}$ is the source Fermi level; $F_{-1/2}(x)$ is the Fermi-Dirac integral. Here we used analytical approximation \[17\] for the integral to reduce the cost of computation. $V_S$ can be obtained by solving (5) numerically and the result is a function of nanowire radius. The biasing conditions do not have significant influence on $V_S$. Figure 2(a) shows the dependence of $V_S$ on the nanowire radius. It is observed that, for larger radius, $V_S$ is smaller due to lower subband energy levels.

Figure 3 shows the central potential distribution along the channel obtained from TCAD simulation. In the source region, the electric potential is very close to the value calculated from (5). However, the electric potential starts to drop at some distances from interface between the source and the channel. This also occurs at the drain end of the channel. This is equivalent to an increase of channel length and is treated as a fitting parameter in the model. The increment of channel length is 1.4 nm and this value is used for other channel-length-dependent results also.

Using the proposition method given in \[18\], (1) can be solved by dividing electric potential into two parts which can be solved separately:

$$\phi (r, z) = \phi_0 (r) + \phi_1 (r, z), \quad (6)$$

where $\phi_0(r)$ is the part obtained by gradual channel approximation; that is \[19\],

$$\phi_0 (r) = \phi_c - \frac{2 k_B T}{q} \ln \left[ 1 - \frac{r^2}{R^2} e^{(\phi_S - \phi_m)/k_B T} \right], \quad (7)$$

Figure 3: Central electric potential at different positions along the channel obtained from TCAD simulation for $R = 4$ nm and $L = 10$ nm; source and drain length are both 10 nm; $V_{GS} = 0.1$ V, $V_{DS} = 0.05$ V. The horizontal axis is distance from source electrode. Interfaces between channel and source and channel and drain are depicted by dashed lines.
where \( \phi_c \) is the electric potential at the center of nanowire. \( \phi_{cm} \) is the maximum central potential that can be reached by increasing the gate voltage. In thin SNT with strong confinement, central potential also tends to be saturated when the gate voltage is very high. Because of the quantum confinement, the electron density in the channel is smaller; the maximum central potential is larger than that in the classic model \([20]\). Since \( \phi_0(r) \) in (7) is based on the gradual channel approximation which is independent of both the drain bias and the channel length, \( \phi_{cm} \) is determined by the gate oxide thickness and the nanowire radius. Figure 2(b) shows the dependence of \( \phi_{cm} \) on the radius of the nanowire for different gate oxide thicknesses obtained from simulation.

It is obvious that the influence of gate oxide thickness on \( \phi_{cm} \) is fairly governed by the radius of the nanowire only. An empirical fitting of the simulation result, as given below, is used to model this dependence:

\[
\phi_{cm} = \frac{aR + b}{R + c},
\]

where the values of the fitting parameters are \( a = 0.484 \) V, \( b = 0.092 \) V, \( c = -0.466 \) nm.

The central potential \( \phi_c \) can be obtained precisely by considering the boundary condition for \( \phi_0(r) \). Here we use an asymptotic approximation, instead, to simplify the equation evaluation \([19]\):

\[
\phi_c = \frac{1}{2} \left[ (\phi_{ca} + \phi_{cm}) - \sqrt{(\phi_{ca} - \phi_{cm})^2 + \delta \phi_{ca} \phi_{cm}} \right],
\]

where \( \phi_{ca} = V_{GS} - V_{FB} \) is the central potential when the gate voltage is far below the threshold. \( \delta \) is an empirical smoothing parameter and its value is approximated as 0.01(1 + ln(1 + \( t_{ox}/R \))).

\( \phi_1(r, z) \) in (6) is obtained by solving the residual 2D Poisson equation as an expansion of Fourier-Bessel series \([18]\):

\[
\phi_1(r, z) = \sum_{m=1}^{\infty} \left[ C_{0m}\exp^{-\lambda_m z/R} + C_{1m}\exp^{-\lambda_m z/R} \right] J_0\left( \frac{\lambda_m r}{R} \right)
\]

and the eigenvalue \( \lambda_m \) is the \( n \)th solution of the equation

\[
\frac{\epsilon_{ox}}{\epsilon_{Si} \ln(1 + t_{ox}/R)} = \lambda \frac{J_1(\lambda)}{J_0(\lambda)},
\]

where \( \epsilon_{ox} \) is the permittivity of oxide. Coefficients \( C_{0m} \) and \( C_{1m} \) are obtained by expanding the boundary conditions (3) in the Fourier-Bessel series. Since the gate length \( L \) is usually much larger than nanowire radius \( R \), the expressions for \( C_{0m} \) and \( C_{1m} \) can be further simplified as

\[
C_{0m} = \frac{2 e^{-\lambda_m z/R}}{[J_0^2(\lambda_m) + J_1^2(\lambda_m)]} \left[ \frac{J_1(\lambda_m)}{\lambda_m} V_D - V_m \right],
\]

\[
C_{1m} = \frac{2}{[J_0^2(\lambda_m) + J_1^2(\lambda_m)]} \left[ \frac{J_1(\lambda_m)}{\lambda_m} V_S - V_m \right],
\]

where \( V_m \) is the expansion coefficient of \( \phi_0(r) \) in the Fourier-Bessel series:

\[
V_m = \frac{1}{R^2} \int_0^R \phi_0(r) J_0\left( \frac{\lambda_m r}{R} \right) r \, dr.
\]  

The integral in (13) can be simplified further. Considering the expression of \( \phi_0(r) \) in (7), in the subthreshold regime, \( q(\phi_c - \phi_{cm})/k_B T \) is small; \( \phi_0(r) \) can be simplified as

\[
\phi_0(r) = \phi_c + \frac{2k_B T r^2}{qR^2} e^{q(\phi_c - \phi_{cm})/k_B T}.
\]

Taking these approximations into account, the integral in (14) can be analytically derived and the result is

\[
V_m = \frac{I_1(\lambda_m)}{\lambda_m} \phi_c
+ \frac{2I_2(\lambda_m) - \lambda_m I_3(\lambda_m) 2k_B T}{\lambda_m q} \frac{2k_B T}{qR^2} e^{q(\phi_c - \phi_{cm})/k_B T}.
\]

Figure 4 compares the central potential distribution along the channel obtained from the model and the simulations for various gate lengths. The results of model agree well with simulation ones, especially in the region near the source dominating the drain current characteristics. It is clearly shown that as gate length shrinks, the influence of the source and drain regions on the electric potential in the channel becomes more prominent; the central potential in the channel increases as a result. In the case of gate length of 10 nm, the central potential is even larger than the gate voltage. It indicates a serious SCE in such short gate length. Larger potential in the channel results in a larger electron concentration and therefore a larger subthreshold leakage current in a shorter SNT. For a device with a larger gate length, the gate controllability on the channel potential is stronger; the variation of potential near the source still remains small as the drain voltage increases. However, for gate length of 10 nm, the control of gate becomes much weaker and the increase of drain voltage would cause a substantial variation on the potential distribution in the whole channel (see Figure 4).

3. Subthreshold Current Characteristics

Electronic transportation was determined at the subband barrier located at the position with minimum electric potential along the channel. Within each subband, electrons injected from source or drain have to transmit across the barrier. The transmission probability is either one or zero depending on whether the electron energy is larger or smaller than barrier energy or not. Drain current was then calculated with Landauer’s theory \([16]\):

\[
I_{DS} = \frac{qk_B T}{\pi \hbar} \sum_{m,n} g_m g_n \ln \left[ \frac{1 + e^{(E_{FS} - E_{m,n})/k_B T}}{1 + e^{(E_{FD} - E_{m,n})/k_B T}} \right],
\]

where \( E_{FD} = E_{FS} - qV_{DS} \) is Fermi level of drain region.

Note that the subband levels in (16) are the most important parameter for determining the drain current. In
the flatband condition, the subband levels are characterized by (4). For larger gate bias, it is difficult to get an analytical expression for the subband levels and drain current. Here we revised the subband levels by taking perturbation integrals at the subband barrier:

$$\Delta E_{m,n}^u = -q \int_0^R |\psi_{m,n}(r)|^2 \phi(r, z_{\text{min}}) r \, dr ,$$  \hspace{1cm} (17)

where $\psi_{m,n}(r) = J_m(j_{mn} r/R)/N_{mn}$ is the corresponding wavefunction of the subband. The wavefunctions of the corresponding subbands in the two valleys are the same. $N_{mn}$ is a normalization factor, and $z_{\text{min}}$ is the position of subband barrier along channel and is determined by $(\partial \phi_j(r,z)/\partial z)|_{z_{\text{min}}} = 0$. Only the first term in (10) is considered when deriving $z_{\text{min}}$ because the magnitude of other high-order terms is much smaller; hence, $z_{\text{min}} = (R/2\lambda_1) \ln(C_{11}/C_{01})$. From (10) and (14), variation of subband levels can be analytically obtained at various bias conditions and drain current can be readily computed by putting subband levels into (16).

Figure 5 shows the transfer characteristics calculated from the model and TCAD simulation. In the subthreshold region, drain current shows an exponential dependence on the gate voltage and that agrees with simulation very well. Results of different gate lengths are compared and deteriorated subthreshold characteristics in shorter channels are observed. Not only does the leakage current increase significantly, but also the drain current changes slower with the gate voltage. It indicates that the switching performance gets worse in short channel devices. Drain current characteristics for different drain biases are also shown. For devices with large channel lengths, the influence of the potential near the subband barrier by the drain bias is small (see Figure 4).

Hence, the subband energy levels do not change much at different drain bias. The slight increase on the drain current for a larger drain bias is mainly due to the reduction of drain Fermi level which reduces the electron flux injecting from drain. However, for short channel device, for example, $L = 10 \, \text{nm}$, a larger drain bias leads to the increase of the potential near subband barrier and then the reduction of subband energy levels. As a consequence, the drain current increases significantly as the drain voltage increases from 0.05 V to 0.5 V (see Figures 5(a) and 5(b)). Figure 6 shows the transfer characteristics calculated from our model together with TCAD simulation results for transistors with different values of nanowire radius and gate oxide thicknesses. The agreement is quite good. It indicates that the present model can accurately predict subthreshold characteristics for different device parameters.

Subthreshold slope (SS) is used to describe the steepness of $I_{\text{DS}}$-$V_{\text{GS}}$ characteristics. It is defined as reduction of gate voltage required to reduce drain current by one decade; that is,

$$\text{SS} = \frac{\partial V_{\text{GS}}}{\partial \log (I_{\text{DS}})} .$$  \hspace{1cm} (18)

Figure 7(a) plots the SS, extracted from $I_{\text{DS}}$-$V_{\text{GS}}$ curve, as a function of channel length. The newly developed model is in good agreement with the TCAD simulation. As shown in the figure SCE is still obvious in the concerned channel length range. At shorter channel lengths, the SS values are larger as a result of degraded gate control over the channel. Reducing the radius of nanowire can suppress the SS degradation for the same channel length. As the channel length increases, SS reduces closer to its lowest limit of 60 mV/dec.
Threshold voltage $V_{th}$ is another important parameter in circuit design and is also influenced by SCE. Figure 7(b) plots the threshold voltage roll-off as a function of channel length. Here the threshold voltages were extracted from the calculated $I_{DS} - V_{GS}$ characteristics. Our compact model agrees well with TCAD simulation. Threshold voltage roll-off is defined as the decrease of threshold voltage in a short channel SNT as compared with a long channel SNT. The $V_{th}$ is defined as the gate voltage at which a specific drain current ($I_{th}$) is obtained [21]. In considering the different nanowire radii and the volume inversion effect of the thin nanowires [22], $I_{th}$ should be proportional to the cross-sectional area of
the nanowire. Here we fitted the threshold current density per unit area at 0.02 \( \mu A/nm^2 \). As shown in Figures 5 and 6, in short SNTs, \( V_{th} \) is smaller and this leads to larger leakage current. As shown in Figure 7(b), the threshold voltage roll-off is larger for shorter gate length and smaller nanowire radii, representing severer SCE in these cases. As gate length gets larger, \( V_{th} \) tends to be independent of gate length.

4. Conclusion

An analytical model for the subthreshold behaviors of ballistic silicon nanowire transistors (SNTs) was developed. In this model, the electric potential was determined by solving the 2D Poisson equation with some proper approximations. Subband levels and the drain current in subthreshold regime were calculated analytically. We further extracted the threshold voltage roll-off and subthreshold swing (SS) degradation characteristics from the computed current characteristics and the trends were studied in detail. These results were validated with TCAD simulations also.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

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