Review Article

Single-Ion Implantation for the Development of Si-Based MOSFET Devices with Quantum Functionalities

Jeffrey C. McCallum, David N. Jamieson, Changyi Yang, Andrew D. Alves, Brett C. Johnson, Toby Hopf, Samuel C. Thompson, and Jessica A. van Donkelaar

Centre of Excellence for Quantum Computation and Communication Technology, School of Physics, University of Melbourne, Melbourne, VIC 3010, Australia

Correspondence should be addressed to Jeffrey C. McCallum, jeffreym@unimelb.edu.au

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Interest in single-ion implantation is driven in part by research into development of solid-state devices that exhibit quantum behaviour in their electronic or optical characteristics. Here, we provide an overview of international research work on single ion implantation and single ion detection for development of electronic devices for quantum computing. The scope of international research into single ion implantation is presented in the context of our own research in the Centre for Quantum Computation and Communication Technology in Australia. Various single ion detection schemes are presented, and limitations on dopant placement accuracy due to ion straggling are discussed together with pathways for scale-up to multiple quantum devices on the one chip. Possible future directions for ion implantation in quantum computing and communications are also discussed.

1. Introduction

Interest in single-ion implantation is motivated in part by the desire to be able to produce electronic and optical devices in which the characteristics of the device are governed by the properties of individual atoms or defects and by extension for computational operations to be performed through controlled coupling of these atoms or defects, formed into arrays or lines, using interactions dictated by the laws of quantum mechanics [1, 2]. Some of the impetus for research into development of quantum computing derives from an address to the American Physical Society on nanotechnology given by Feynman in 1959 and a later address on simulating physics with computers [3, 4]. Considerable time past before the necessary fabrication and measurement infrastructure, and theoretical understanding was available to even approach the early vision articulated by Feynman.

In 1998, Kane proposed an architecture for a quantum computer based on encoding information on the donor nuclei of individual phosphorus atoms embedded in a silicon matrix [5]. This provided a plausible pathway to realisation of a solid-state quantum computer and prompted international attention to be focussed on developing the necessary expertise and infrastructure to allow such a device to be fabricated. Interest in single-ion implantation and single-ion detection has grown as part of this research effort. After the Kane proposal, a range of other architectures and encoding mechanisms for quantum bits, qubits, were proposed [6–8]. Researchers also realised that the Kane architecture suffered from some major technical problems including spatial oscillations in the exchange coupling strength and crosstalk in gate control prompting further improvements in architectures to be put forward [9, 10]. The key importance of being able to initialise and read out qubits also prompted research into development of electron reservoirs that could be manipulated through suitable variation of the electrostatic landscape to allow electrons to be loaded onto or removed from a nearby qubit in a precisely controlled manner. Gate-defined quantum dots have proved particularly useful in this regard and recent advances in single electron manipulation and spin readout have relied on this key breakthrough [11–14].

As device dimensions continue to shrink, counted implantation of ions and their controlled placement are also of interest for deterministic doping of conventional
metal-oxide field effect transistors (MOSFETs). The aim of deterministic doping is to remedy the variation in threshold voltage shift and off-state current that occurs due to statistical fluctuations in the number and distribution of implanted dopants [15–20]. Workshops on deterministic doping have been held as part of the International Technology Roadmap on Semiconductors Emerging Research Materials program but there is a large gap between what is achievable in research devices in the laboratory and what would be required for scale up to an industrial process, and the pathways for achieving scale-up are far from being clear at this stage [21].

Single-ion implantation is also of interest for controlled placement of individual optical centres in a suitable host matrix for development of single photon sources and other optical elements for use in optical quantum computing and quantum communications. Considerable progress has been made, for example, on engineered placement of nitrogen-vacancy centres in diamond [22–26]. Interest in measurement and manipulation of individual optical centres grows as research into quantum communications intensifies.

Single-ion implantation of optical centres is not the focus of this paper, instead the emphasis is on single-ion implantation and single-ion detection for development of electronic devices for quantum computing. The scope of international research into single-ion implantation is presented in the context of our own research in the Centre for Quantum Computation and Communication Technology in Australia. The paper begins with a brief discussion of a phosphorus in silicon quantum computing device in which single-shot readout of electron spin state has recently been demonstrated [14]. This device allows many of the important features of silicon quantum device design and construction to be understood. Single-ion detection via ion-beam-induced current (IBIC) is then introduced, and the attributes and challenges of this method are discussed including proposed future developments. Single-ion detection through measurement of changes in the source-drain current in MOSFET devices is an important alternative methodology that is suitable for some classes of devices, and research on this approach is also presented.

2. Single-Ion Detection: Ion-Beam-Induced Charge

Figure 1(a) shows a scanning electron microscope (SEM) image of a gate-defined quantum dot structure or silicon single electron transistor (SiSET) with a small number of phosphorus atoms implanted within the region bounded by the dashed line [14, 27]. The SiSET is formed using Al gates deposited on a high-quality 5 nm SiO₂ thermal
oxide grown on a high-purity Si substrate. A small reservoir of electrons is formed on the central SET island through appropriate choices of top-gate \( V_{\text{top}} \) and barrier-gate \( V_{B1}, V_{B2} \) potentials. The electrons on the SET island are tunnel-coupled to the source and drain reservoirs. With appropriate adjustment of the electrostatic landscape, an electron can also tunnel onto the island from a nearby charge centre such as an implanted phosphorus donor. This can be achieved using the plunger gate \( V_p \). Transfer of an electron from the charge centre to the island has a dramatic effect on the source-drain current, so the tunnelling event can readily be detected.

In the presence of a large externally applied magnetic field tunnelling between the charge centre and the island becomes spin dependent, as illustrated in Figure 1(b). Figure 1(c) shows the variation in the SET current as a function of the voltages on the top and plunger gates. The breaks are due to charge transfer events. Since the tunnelling probability is very sensitive to the distance between the charge centres and the island, different regions of the bias-spectroscopy diagram correspond to tunnelling events from different charge centres [14, 27]. For this particular device, timed implants were used to achieve 3–5 implanted phosphorus atoms within a 90 \( \times \) 90 nm\(^2\) aperture in the polymethyl methacrylate (PMMA) mask opened adjacent to the SET island (dashed region in Figure 1(a)). Recent measurements positively identify phosphorus as one of the charge centres based on observation of the hyperfine splitting. A double-gated nanoscale field-effect transistor, with a small number of phosphorus donors implanted into the conduction channel, has also been fabricated to study electron transport through individual phosphorus atoms [13].

Detected single-ion implantation allows precise control over the number of ions implanted through an aperture. Detection of single-ion impacts can be achieved in a number of different ways including detection of secondary electron emission from the surface [28], observation of ion-track formation in some suitable resist coated onto the surface [29], detection of changes in the source-drain current of a MOSFET device [18], or detection of the electron-hole pairs formed as the ions lose energy through electronic interactions with the substrate [30]. This latter method has the advantage that detection of the energy deposited by the ions in the lattice allows real-time differentiation between events where the ions stop in the substrate and those where they stop in an overlying resist or oxide layer or some other undesired location. Devices for quantum computation generally require implantation of low-energy ions due to the need for close proximity to surface gates. The detection of sub-20 keV ions is somewhat challenging because relatively few electron-hole pairs are produced by each ion. With care in device fabrication and packaging to reduce capacitance and through use of low-noise electronics it is possible to routinely and reliably detect implantation of the low-energy ions required for quantum computing devices.

The detection method is illustrated in Figure 2. The detector is a reverse biased PIN structure fabricated in a high-purity Si substrate. Two surface aluminium electrodes contact the boron-doped p-wells. The back contact consists of an n-type phosphorus diffused layer and Al contact. Detection of the transient signal due to ion impacts is achieved by connecting the detector electrodes to a cooled MOXTEK four-terminal junction field effect transistor (JFET) with an integrated transistor resetting circuit. An external preamplifier module controls the JFET. Each implanted ion produces approximately 1000 electron-hole pairs which drift in the internal electric field and induce the charge transient detected by the external circuitry. 14 keV \( ^{31}\text{P}^+ \) ions have a projected range of about 22 nm when implanted into Si through a 5 nm SiO\(_2\) surface layer, as predicted by the modelling code SRIM [31]. Only 34% of the initial kinetic energy produces ionization events hence the need for optimised design of the detection system so that the noise threshold is as low as possible. Other details of the detection system can be found in [30, 32, 33].

Ion-beam-induced charge (IBIC) measurements using high energy ions is a very useful tool for studying detector efficiency and optimising detector design prior to attempting a keV single-ion implantation experiment. The right-hand panels of Figure 3 show IBIC maps of a quantum computing device collected with a scanned 500 keV He microbeam from the Melbourne Pelletron. The top panel is a large area scan, while the bottom panel is a closeup of the site where the single-ion implantation will take place, showing the desired high charge collection efficiency in this region. The two left hand panels are the corresponding SEM images of the device. The purple regions of moderately high charge collection efficiency arise from a diffused channel stopper that is incorporated into the device design to prevent crosstalk.
between device components. The IBIC maps show that the channel stopper is inadvertently coupled to the detector leads increasing the capacitance. This issue is being addressed in later generations of devices and highlights the importance of IBIC with MeV ions in allowing such issues to be identified.

3. Single-Ion Implantation Challenges

Some of the key challenges facing researchers in developing single-ion implantation for quantum computing are limitations on the accuracy of placement of implanted ions due in part to ion straggling, limits on the ability to detect ion implantation events above the noise floor as the ion energy is reduced due to the decrease in the fraction of the ion energy that contributes to ionisation events, and the need to develop appropriate pathways for scale-up so that multiple implant sites and devices can be implanted sequentially.

The variation in ion range and longitudinal straggling with ion energy is illustrated in Figure 4(a) for keV P and Sb ions. Sb implanted quantum devices are being investigated by Schenkel and coworkers [34, 35]. For 14 keV P, the projected range is ~22 nm, and the longitudinal range straggling gives rise to a variation in depth of ±11 nm. The corresponding lateral range straggling is ~8 nm. The situation is better for Sb as can be seen in Figure 4(a). However, $^{121}\text{Sb}$ has a nuclear spin of $I = 5/2$ [36], giving rise to extra spin states compared to the simple two-state system for P ($I = 1/2$), and the heavier mass of the Sb ions is less compatible with implantation through a preexisting thermally grown gate oxide since the damage is likely to be more difficult to repair than is the case for P [37]. Indeed, Lo et al. adopted a process where they grew the oxide after the Sb implantation, [36] relying on the relatively small intrinsic diffusivity of Sb at the oxide growth temperature, but this approach also adds to the uncertainty in how well alignment with surface gates can be achieved. The effect of ion straggling on the uncertainty in the placement of P$^+$ ions is further illustrated in Figure 4(b) which shows the predicted probability distributions for the implantation of 100 000 14 keV P$^+$ ions through three circular apertures 10 nm (top panel) and 20 nm (bottom panel) in diameter [38]. In each case the aperture centres are spaced 30 nm apart. The ion impacts were randomly distributed over the apertures. The lateral and longitudinal range contribute significantly to the overall uncertainty in location of the implanted ions.

The system of three donors at adjacent implant sites 30 nm apart depicted in Figure 4(b) is a prototype model to test whether ion implantation could reasonably be used to produce a device that would allow demonstration of quantum transport via the coherent tunnelling adiabatic
Figure 4: The effect of ion range straggling on the uncertainty in location of keV P and Sb ions implanted into Si: (a) variation in ion range and longitudinal straggling with ion energy for keV P and Sb ions. (b) Probability distributions for the implantation of 100 000 14 keV P⁺ ions through three circular apertures spaced 30 nm centre to centre. The aperture diameter is 10 nm in the top panel and 20 nm in the bottom panel.

Figure 5: The fraction of an ions initial energy lost to electronic stopping processes by both the primary ion and recoils as predicted by SRIM ($f_{E,SRIM}$) divided by the value obtained from measurement using our single-ion detection system ($f_E$) and compared with previous data of Funsten et al. (as in [39, 40]).

The examples presented here are all for donor-based quantum computing devices but it is worth noting that acceptor-based quantum computing is also a possibility. It has been shown in lightly boron-doped silicon samples that transitions between the acceptor energy levels can be induced by an applied resonant AC electric field and that Stark tuning of level spacing can be achieved with an external DC electric field [44]. However, the authors also noted a strong sensitivity of the system to random local strain variation, and this may be problematic for boron qubits formed by ion implantation.

While ion straggling can be reduced by reducing the ion energy, the projected range of the ions still needs to be large enough to place the ions deeper than the extent of any gate oxide or other overlayers that may be present. Also, donors placed too close to the surface may hybridize with states at the oxide interface which may not be desired [45, 46]. As the ion energy decreases, the fraction of the energy that is available for ionisation events also reduces adding to the difficulty of single-ion detection via electron-hole pairs. Also, the electronic stopping predicted by SRIM has been shown to be in error in the low-energy regime [32, 39, 40, 47, 48]. SRIM overestimates the electronic stopping as illustrated in Figure 5 which shows the fraction of an ions initial energy lost to electronic stopping processes by both the primary ion and recoils as predicted by SRIM ($f_{E,SRIM}$) divided by the value obtained from measurements using our single-ion detection system ($f_E$) or from previous measurements of Funsten et al. [39, 40]. Good agreement is obtained despite the fact that the detection systems are quite different in the two cases. In the work of Funsten et al. the mean energy lost to electronic processes by incident ions, and recoils was found through measurement of the change in photocurrent...
in silicon photodiodes under ion bombardment. Large area detectors were used and relatively high fluences could be tolerated before the detectors would degrade. In our single-ion detectors the ionisation response is measured in a central region only $10 \times 10 \mu m^2$ where the ions can penetrate through the thin 5nm oxide. The ionisation is directly measured for individual ions, and the energy spectrum is collected for multiple ions but only relatively small numbers of ions can be detected before the accumulated damage reduces the detection efficiency. Only a few hundred ions can be detected before a new detector is needed. Recently Akkerman and Barak have developed a model for the energy partitioning between electronic and nuclear components for low-energy ions in silicon, and this model is found to provide a good fit to the data of Funsten et al. and to our own data [40, 49, 50].

To compensate for the decreasing number of electron-hole pairs produced with decreasing ion energy one can look for more sensitive detection systems. Avalanche photodiodes (APD) can be used to detect single-electron hole pairs generated by single photons [51]. This approach has recently been adapted for single-ion detection [52, 53]. The avalanche diodes are operated in Geiger mode by biasing past reverse breakdown for a short-time period during which the detector is sensitive to a single electron or hole injected into the junction. The high fields produce an avalanche cascade leading to a Geiger signal. Near 100% detection efficiency has been demonstrated for 250 keV $H^+$ ions [53]. In the current detector design thick surface layers and structures prevent low-energy ions from reaching the active region of the device but the methodology has considerable merit, and there is scope for further development.

Scale-up to multiple qubits and transport structures on the same device requires some method to ensure registration of the ions to the aperture array, that is, that one and only one ion is implanted through each aperture in the lithographic mask. It is impractical to use multiple lithographic steps with only one aperture open at each step. Ideally if a method could be devised to have a nanometer diameter ion beam and nanometer-level accuracy in the position of the beam with respect to a substrate, it would be possible to form qubit arrays without a lithographic mask. Proposals exist for ion-trap-based ion sources that could deliver arrays of single ions with nanometre spatial precision but these systems are still in development [54–56]. A focussed ion beam (FIB) system can produce a sub-10 nm diameter beam, and this is the approach used by Shinada et al. to produce ordered dopant arrays in the channel of a back-gated silicon on insulator MOSFET device [15]. The single-ion impacts were detected by measuring the change in the source-drain current of the device during implantation. Although the device was rather large, having channel width and length of 300 nm and 3.2 $\mu m$, respectively, theoretically single-ion implantation could be achieved in nanoscale devices via this approach, however, no beam halo can be tolerated, and this will be very difficult to achieve. An alternative approach is to use a lithographically defined mask on the sample combined with a scanned nanoaperture used to collimate the ion beam and which can be positioned with nanoscale accuracy. This is the approach adopted by Schenkel and coworkers and by our group in Melbourne [38, 57, 58].

Figure 6(a) illustrates the concept of using a scanned nanoaperture to sequentially implant ions through an array of nanoapertures lithographically patterned on a substrate. Here, the nanoaperture is a slot in a cantilever. The position of the scanned aperture can be registered to the substrate by detecting ions implanted into a registration zone. The aperture can then be repositioned to the implant zone to perform the deterministic implants. Figure 6(b) shows the apparatus used in these experiments. Nanopositioning is achieved with a three-axis Attocube stack [59]. The single-ion detector can be cooled for low-noise operation. An SEM micrograph of one of the slot nanoapertures is shown in Figure 6(c). The slot aperture is formed by FIB milling of a Si cantilever [38]. The slot is backfilled by in situ deposition of Pt dissociated from a precursor gas by the ion beam to achieve the desired aperture width. A 60 nm slot is shown here. The narrowest uniform slot width we have achieved to date is 30 nm. Figure 6(c) shows a series of irradiation lines in PMMA formed by 14 keV $Ar^+$ ion irradiation through the 60 nm wide aperture and stepping of the aperture using the nanopositioner. We are currently working towards full testing of our single-ion detection system integrated with the nanoaperture positioning system.

Finally in this section it is worth mentioning an alternative technology that can place dopant atoms with near-atomic precision. Hydrogen-resist lithography uses a H-terminated Si(001) surface and selective removal of H via a scanning tunnelling microscope tip followed by controlled adsorption of $PH_3$ molecules at the exposed sites to incorporate P atoms into Si with near-atomic control over the position [60]. Remarkable progress has been made using this technology and recently a nanoscale Si quantum dot device with approximately six P atoms in the quantum dot has been demonstrated [61]. The device features in-plane source-drain leads and gates also formed using H-resist lithography and P incorporation. This technology does not suffer from ion straggling and has the potential to produce qubits and transport arrays with appropriate positioning accuracy but it does require quite painstaking work in its continued development. In the longer term, ion implantation may have a significant role in fabrication of quantum optical devices where atoms and optical defects are coupled to optical cavities and waveguide structures [62]. In these structures, restrictions on the placement accuracy of atoms or optical defects are likely to be more relaxed.

4. Single-Ion Detection: Drain Current Modulation

In prefabricated nano-MOSFET devices where it is not possible to incorporate the previously mentioned single-ion detection system it can still be possible to perform real-time single-ion detection by monitoring the changes in the source-drain current of the device due to ion damage. This was the approach used by Shinada et al. in their back-gated silicon on insulator MOSFET device [15]. The approach has also been used by Batra et al. [63, 64]. Our own work in
Figure 6: Ion registration with a lithographically patterned substrate using a scanned nanoaperture: (a) illustration of the concept of using a scanned nanoaperture to sequentially implant ions through an array of nanoapertures patterned on a substrate. (b) The nanopositioning system integrated with the single-ion detection system. (c) A 60 nm wide slot aperture in a Si cantilever formed by FIB milling and Pt back-filling. (d) A series of irradiation stripes formed in PMMA by 14 keV Ar irradiation through the slot aperture.

this area and that of other researchers is motivated by recent observations of quantum transport through individual randomly distributed dopant atoms fortuitously located at appropriate positions in the channels of nanoscale MOSFETs to affect the conduction [20, 65]. Single-ion implantation offers the possibility of deliberately placing dopants in the channel to produce quantum effects.

We have performed single-ion implantation studies on nanoscale silicon-on-insulator (SOI) MOSFETs using 500 keV He\(^+\) and 14 keV P\(^+\) ions and observed changes in the drain current due to the ion impacts [41]. Two different finFET device structures were used in the study as shown in Figure 7. In the case of the 500 keV He\(^+\) irradiations the particle range was sufficient for the ions to pass right through the wrap-around gate and the channel and the buried oxide (BOX) and stop in the substrate. As shown in Figure 7(a), in this case discrete upward steps were observed in the drain current, \(I_D\), consistent with the expected rate of ion arrival. This was accompanied by a negative shift of the IV curve which is consistent with trapped charge being created in the BOX. For 14 keV P\(^+\) ions a different device geometry with a split-gate design was used so that the low-energy ions could reach the channel. Now, downward steps in \(I_D\) occurred, as shown in Figure 7(b), and a corresponding positive shift of the IV curve was observed consistent with negatively charged interface states being generated. The decrease in \(I_D\) was consistent with introduction of Frenkel pairs into the channel.

Subsequent experiments have shown that it is difficult to unambiguously identify low-energy single-ion impacts in these devices on a routine basis. In some cases no variation in the drain current is detected above the noise. Device modelling with the technology computer aided design (TCAD) software package may go part way to explaining this [66]. Figure 8 shows the expected current density in the split gate device. The current density is greatest under the gates where the low-energy ions cannot penetrate which may reduce the sensitivity of the devices to low-energy ion impacts. These devices offer the possibility of back-gating as was used by Shinada et al. [15] in their deterministic doping studies. We are currently investigating this avenue for improved sensitivity to low-energy ion impacts.

5. Conclusions

Single-ion implantation has contributed to rapid prototyping of devices for solid-state quantum computer development. Phosphorus implanted quantum devices exhibiting spin-dependent transport and single shot spin readout have been demonstrated. Single-ion detection schemes allow individual ions to be implanted into lithographically patterned device structures with high precision but ion range straggling places fundamental limits on the positioning accuracy and in the longer-term ion implantation may not be able to compete with the hydrogen-resist lithography approach which can produce quantum devices with near-atomic precision in dopant placement. Ion range straggling can be reduced by decreasing the ion energy, but surface proximity effects can become a problem, and there are a diminishing number
Figure 7: Changes in $I_D$ in nanoscale MOSFETs during (a) 500 keV He$^+$ and (b) 14 keV P$^+$ ion irradiation. Discrete steps represent single-ion impacts. Schematics of the devices are shown as insets. False color scanning electron microscopy, transmission electron microscopy, and atomic force microscopy images of a double gated MOSFET identical to those under study are shown in (c), (d), and (e), respectively (as in [41]).

Figure 8: TCAD modelling of current density in the split-gate finFET device, showing the low current density in the exposed sections of the device where ion impacts will occur.

of electron-hole pairs available for ion detection, although Geiger-mode avalanche detectors offer some prospect of pushing the detection limits. Ion implantation may have a significant role in fabrication of quantum optical devices where atoms and optical defects are coupled to optical cavities and where the precision in placement of the atom or optical defect is expected to be somewhat more relaxed. Single-ion detection by monitoring changes in the drain current of nano-MOSFETs has been shown to be possible in principle but still requires further development and modelling to understand the optimal gating scheme and the contributions of defects generated in the oxide layers, the channel and other structures with the changes in drain current and IV characteristics observed.

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References

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[59] Systems AG Attocube, “Konigistrasse 11a RGB 80539, Munich, Germany.”


[66] “Synopsys, Inc., 700 East Middlefield Road, Mountain View, CA 94043.”
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