Research Article

Deposition of Low Stress Silicon Nitride Thin Film and Its Application in Surface Micromachining Device Structures

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Received 26 July 2013; Accepted 23 September 2013

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Surface machining processes are responsible for creating microstructures that reside near the surfaces of a substrate and are characterized by the fabrication of micromechanical structures from deposited thin films. These films can be selectively removed to build three-dimensional structures whose functionality typically requires that they should be freed from the planar substrate. Silicon nitride thin film is one of these important materials. In this paper, by adjusting the SiH$_2$Cl$_2$/NH$_3$ gaseous ratio, low stress silicon nitride (LSSiN) is deposited by the low pressure chemical vapor deposition (LPCVD) process. The internal stress generally in 135 MPa has been detected using an FLX-2320 film stress tester. Based on the wide application in surface micromachining devices, the mechanical properties of LS SiN are measured by nanoindentation, giving the value of Young's modulus of 224 GPa and the hardness of 22.5 GPa, respectively. Dry etching and wet etching are utilized to fabricate the LS SiN thin film for structural layers. The etching rate compared with normal Si$_3$N$_4$ film by LPCVD is demonstrated for silicon chip manufacture.

1. Introduction

Bulk micromachining is an important class of micromechanical electronics system (MEMS) process [1, 2]. In bulk processes, a portion of the substrate (bulk) is removed in order to create freestanding mechanical structures (such as beam and membranes) or unique three-dimensional features (such as cavities, through-wafer holes, and mesas). There are two major categories of processes for bulk etching according to the medium of the etchant: wet etching and dry etching. Wet silicon etching processes use liquid chemical solutions in contact with silicon. Dry etching uses plasma (high energy gas containing ionized radicals) or vapor phase etchants to remove materials. Currently, the focus on the bulk etching has been transformed from the etching geometric structures to improving etching accuracy, uniformity, surface roughness, and compatibility with CMOS.

Surface micromachining is so named because the process takes place on the surface of the wafer, where films are used for structural elements and deposited using a technique such as low temperature chemical vapor deposition (LPCVD) [3, 4]. Originally, employed for integrated circuits, films composed of materials such as polysilicon, silicon nitride, and silicon dioxide can be sequentially deposited and selectively removed or machine three-dimensional structures whose functionality typically requires that they should be freed from the planar substrate. Bulk micromachining is not easily integrated with IC processing. Surface micromachining can easily be integrated with CMOS processing, allowing signal processing circuitry and MEMS devices to exist on the same chip.

There are two key process steps in surface micromachining. The first is deposition of low stress thin films that can be used for structural elements. The second is the use of a sacrificial layer to allow the structural layer to be detached from the substrate, thus allowing the motion of the structural layer. The deposition of controlled stress films is an essential process step for surface micromachining. With excellent characteristics [5, 6], silicon nitride film can block the diffusion of water and ions (such as sodium) effectively. Due to the capability of antioxidization and anticorrosion, it can be used as masks for deep etching, electronic insulation layer, and ion implantation. In this paper, we focus on the preparation process of low stress silicon-rich nitride
(LS SiN) films. The mechanical parameter of LS SiN is presented by nanoindentation. The film pattern in the surface micromachining process is demonstrated, which provides a strong basis for the MEMS device design and silicon chip fabrication.

2. Deposition of Low Stress Silicon Nitride Thin Film

Normal Si$_3$N$_4$ film is deposited at temperature over 800°C using dichlorosilane (DCS) and ammonia in a flow ratio of approximately 1:5 (DCS: NH$_3$) on a silicon wafer. This process yields a stoichiometric Si$_3$N$_4$ film with high tensile stress. By reversing the flow ratio to 10:1, a silicon-rich nitride film is deposited, and the tensile stress level is considerably lower. Table 1 lists the parameters; the reaction is

\[
\text{SiCl}_2\text{H}_2 + \text{NH}_3 \rightarrow \text{SiN} + \text{HCl} + \text{H}_2
\]  

Figure 1 depicts the stress map of LS SiN tested by FLX2320. The stress can be calculated using the Stoney equation as follows:

\[
\sigma = \frac{\delta}{t} \frac{E}{1 - \nu^2} \frac{T^2}{3R^2},
\]  

where \( \delta \) and \( t \) are the deflection of wafer center and film thickness, respectively. \( R \) and \( T \) are the radius and thickness of silicon, respectively. \( E \) is Young’s modulus, and \( \nu \) is Poisson’s ratio. The fitted stress is less than 135 MPa.

3. Mechanical Properties of LS SiN

In MEMS/NEMS design, Young’s modulus of the thin film is one of the important mechanical properties. Young’s modulus of a single-crystal material reflects the details of interatomic bond energy and lattice structure. With the rapid development of NEMS technologies, ultrathin cantilevers have been used as sensitive sensors for applications involving ultrafine resolution. Young’s modulus, \( E \), has a strong effect on the resonant cantilevers because it is proportional to the square of the resonance frequency. The fundamental resonance frequency of a cantilever is [7]

\[
f_0 \approx 0.162 \frac{t}{l} \sqrt{\frac{E}{\rho}},
\]  

where \( t \) is the thickness, \( l \) is the length, and \( \rho \) is the material density of the cantilever.

Nanoindentation is a useful technique for measuring the mechanical properties of small volumes of materials. Its attractiveness stems largely form the fact that mechanical properties can be determined directly from measurements of indentation and displacement, without the need to image the hardness impression. Based on the depth-sensing indentation method of Oliver and Pharr, the elastic modulus of the specimen was related to the measured reduced modulus, \( E_r \), by using the equation [8, 9]

\[
\frac{1}{E_r} = \left(1 - \nu_i^2\right) \frac{E}{E_i} + \left(1 - \nu_r^2\right),
\]  

\[
E = \frac{1 - \nu^2}{E_i - (1 - \nu_i^2) E_r} E_r E_r = \frac{1 - \nu^2}{E_i / E_r - (1 - \nu_i^2)} E_r,
\]  

where \( E_i \) and \( \nu_i \) are Young’s modulus and Poisson’s ratio of the diamond tip. Here, \( E_i \) and \( \nu_i \) are equal to 1140 GPa and 0.07, respectively. \( E_r \) is referred to as the reduced modulus and is expressed as a function of the displacement per unit load which is applied as follows:

\[
E_r = \frac{1}{2} \frac{\sqrt{\pi}}{A} \frac{dP}{dh}.
\]  

In the previous equation (5), \( P \) is the applied load, \( h \) is the displacement, and \( A \) is the contact area.

Nanoindentation tests were carried out on a Triboindenter (Hysitron Inc.), with a Berkovich indenter tip, whose radius was estimated to be 100 nm. Because the thickness of the LS SiN film was around 1.2 μm, the maximum indentation depth was restricted to 120 nm to avoid the influence of the substrate. Figure 2 shows the load-displacement relationship of the sample during nanoindentation. Figure 3 shows the load and unload time. Since the SiN film thickness is around 1.2 μm, the 4 mN maximum load, the 5 s loading time, the 5 s unloading time, and the 5 s no-load time are selected to avoid phase change of the material [10, 11].

<table>
<thead>
<tr>
<th>Temperature (°C)</th>
<th>Pressure (mTorr)</th>
<th>Gas (sccm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>835</td>
<td>875</td>
<td>860</td>
</tr>
<tr>
<td>289</td>
<td>NH$_3$</td>
<td>8</td>
</tr>
<tr>
<td>80</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 1: Internal stress measurement of LS SiN.
Keeping a distance above 3 μm, Young's modulus of the four samples calculated is shown in Table 2. The average Young's modulus of LS SiN is 224 GPa.

### 4. Application of Low Stress Silicon Nitride Film

Surface micromachining process can fabricate the micromechanical structures adhered to substrate surface by depositing structural layer on the sacrificial one. This can provide support for structural layer and play a role on space-based positioning during processing. The sacrificial layer was then removed to release the structural layer on it. Generally, silicon nitride films deposited by LPCVD can be used as a structural layer for their internal tensile stress and native nonporous morphology. In practical applications, except for long process time (high cost), silicon nitride films by LPCVD are less than 1.5 μm. If the thickness is too large, the film under tensile stress will bend and even fracture. As the structural layer of devices, films with appropriate internal tensile stress are beneficial for the stability of the device and possess good mechanical properties [12]. Figure 4 shows the cantilever beam structure fabricated by LS SiN films. The resistor was formed after depositing polysilicon, ion implantation doping, and photolithography. A circuit compatible with CMOS by sputtering metals was constituted to fulfill low cost mass production of the devices.

The LS SiN patterning process can be divided into dry and wet etching. LS SiN dry etching mainly includes reaction ion etching (RIE) and ion beam etching (IBE). Reaction ion etching (RIE) is a technology that uses chemical reaction and physical ion bombardment to remove material. In general, the CF₄ is used as corrosive gas and dissociated into F⁺, CF₂, CF₂, and CF, and so forth. With the highest chemical activity, fluoride free radicals F⁺ can react with LS SiN; the reaction is as follows:

$$\text{SiN} + \text{F}^+ \rightarrow \text{SiF}_4 + \text{N}_2$$  \hspace{1cm} (6)

Ion beam bombardment, using the inert gas Argon (Ar) rather than chemical incorporation of etching matter, is an absolute micromachining process with two significant advances of directionality and practicality. Since ions in the ion beam are accelerated by a strong vertical electric field and the pressure in the reactor is extremely low, the impossibility of collisions between atoms leads to the directionality of etching. Firstly, the velocity when atoms impinge the silicon surface is almost vertical. The anisotropic etching to every material can be achieved as a result of its nonchemical property. Secondly, different raw materials used for corrosion

<table>
<thead>
<tr>
<th>Sample</th>
<th>$E_r$ (GPa)</th>
<th>$E$ (GPa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1P19</td>
<td>197.29</td>
<td>189.43</td>
</tr>
<tr>
<td>1P20</td>
<td>219.53</td>
<td>211.40</td>
</tr>
<tr>
<td>1P21</td>
<td>211.28</td>
<td>195.52</td>
</tr>
<tr>
<td>1P22</td>
<td>187.31</td>
<td>223.24</td>
</tr>
</tbody>
</table>

Average (GPa) 224.08
Table 3: LPCVD nitride etching rate comparison.

<table>
<thead>
<tr>
<th>Etchants</th>
<th>Temperature (°C)</th>
<th>Ratio (A/min)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Normal Si$_3$N$_4$</td>
<td>LS SiN</td>
</tr>
<tr>
<td>BOE</td>
<td>38</td>
<td>26.36</td>
</tr>
<tr>
<td>BOE</td>
<td>25</td>
<td>7.68</td>
</tr>
<tr>
<td>KOH</td>
<td>50</td>
<td>0.05</td>
</tr>
<tr>
<td>40% HF</td>
<td>25</td>
<td>112.76</td>
</tr>
<tr>
<td>H$_3$PO$_4$</td>
<td>160</td>
<td>32.56</td>
</tr>
</tbody>
</table>

include compounds and alloys. Due to its capabilities to etch graphics on different materials, the serious disadvantages are the optical photosensitivity and the selectivity of lower layers close to 1:1.

Due to the selectivity of etching, silicon nitride film commonly functions as a mask for wet etching of polysilicon, silicon oxide, and other materials. A comparison of the etching velocity of normal Si$_3$N$_4$ and LS SiN using common etchants was studied. The result of the test is shown in Table 3. Results of the comparison show that the etching rate of normal Si$_3$N$_4$ is faster than LS SiN in five different etching solutions (38°C BOE, ambient BOE, 50°C KOH, ambient 40% HF, and 160°C H$_3$PO$_4$).

The etching rate of LS SiN in the KOH and the ambient BOE is so slow that it can be used as a good etching resistant barrier. In the ambient 40% HF, due to the sensitivity to the oxygen residue in the film, the etching rate as the content of oxygen increased, while it is slow in H$_3$PO$_4$.

5. Conclusions

The preparation of low stress silicon-rich nitride films (LS SiN) is presented in this paper. By adjusting the SiH$_2$Cl$_2$/NH$_3$ gaseous ratio, LS SiN is deposited by the low pressure chemical vapor deposition (LPCVD) process. The internal stress, generally in 135 MPa, has been detected using an FLX-2320 film stress tester. Based on the wide application in surface micromachining devices, the mechanical properties of LS SiN are measured by nanoindentation, gaining the value of Young’s modulus of 224 GPa and the hardness of 22.5 GPa, respectively. Dry etching (reaction ion etching and ion beam etching) and wet etching (38°C BOE, ambient BOE, 50°C KOH, ambient 40% HF, and 160°C H$_3$PO$_4$) are utilized to fabricate the LS SiN thin film for structural layers. The etching rate compared with normal Si$_3$N$_4$ film by LPCVD is demonstrated for the device silicon chip manufacture.

Acknowledgments

This work was financially supported by the Science and Technology Development Plan of Zhejiang Province (Public Technology Research and Industrial Projects, Grant no. 2012C21088), Natural Science Foundation of Zhejiang province (Grant no. LY13E050016), and the Opening Foundation of Jiangsu Province Material Tribology Key Laboratory (no. kjsmcx1001).

References
