NEW CURRENT-MODE OSCILLATORS USING
UNITY-GAIN CURRENT-FOLLOWERS

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(Received October 8, 1994; in final form December 29, 1994)

A catalog of current-mode oscillator circuits using negative unity-gain current-followers is presented. Each circuit uses two unity-gain current-followers, three resistors, and three capacitors. Simulation results obtained from two new circuits are presented.

INTRODUCTION

Recently, a number of current-mode RC-oscillator circuits using a single current-follower has been presented [1,2]. Unfortunately, none of these circuits use a true current-follower with unity gain as they require a current-gain greater than unity.

A current-follower is usually obtained from a second-generation current-conveyor by grounding its high impedance terminal. Although a discrete-element implementation of a current-conveyor with variable-gain has been reported [3] this requires additional active devices, which makes their IC implementation too much complex. Thus, the realization of oscillators using unity-gain current followers would be attractive as they can be implemented using available second-generation current-conveyor integrated circuits.

PROPOSED CIRCUITS

Consider the oscillator structures shown in Fig. 1. Assuming ideal current-followers with \( i_y = i_x \) and \( v_x = 0 \) routine analysis of the circuits of Fig. 1 for unity loop-gain of current, yields the characteristic equation given by

\[
(Y_3(Y_4 + Y_5) + Y_4Y_5)(Y_1 + Y_2 + Y_6) + Y_1Y_4Y_6 = 0
\]  

(1)

for the structure of Fig. 1(a),

\[
(Y_3(Y_2 + Y_6) + Y_2Y_6)(Y_1 + Y_4 + Y_5) + Y_1Y_3Y_5 = 0
\]  

(2)

for the structure of Fig. 1(b),

\[
Y_3Y_6(Y_1 + Y_2 + Y_3 + Y_4) + Y_3Y_5(Y_1 + Y_4) + Y_4Y_6(Y_2 + Y_3) = 0
\]  

(3)

for the structure of Fig. 1(c),

\[
Y_4Y_5(Y_1 + Y_2 + Y_3 + Y_6) + Y_2Y_3(Y_5 + Y_6) + Y_6(Y_2Y_5 + Y_3Y_4) = 0
\]  

(4)
Figure 1 Proposed Oscillator Structures
CURRENT-MODE OSCILLATOR

FIGURE 1 (Continued)

for the structure of Fig. 1(d),
\[ Y_1 Y_4(Y_3 + Y_2 + Y_6) + Y_3 Y_6(Y_1 + Y_2 + Y_3) + Y_4(Y_5 + Y_6)(Y_2 + Y_3) = 0 \]  
(5)

for the structure of Fig. 1(e),
\[ Y_4(Y_3 + Y_6)(Y_1 + Y_2 + Y_3) + Y_2 Y_5(Y_4 + Y_6) = 0 \]  
(6)

for the structure of Fig. 1(f), and
\[ Y_4(Y_1 + Y_2)(Y_2 + Y_3 + Y_6) + Y_3(Y_4(Y_2 + Y_6) + Y_5 Y_6) = 0 \]  
(7)

for the circuit of Fig. 1(g).

Using equations (1)–(7) oscillator circuits can be derived from the structures of Fig. 1(a)–1(g), respectively. Table I shows the admittances, the frequencies of oscillation, and the conditions of oscillation of the obtained circuits. It is interesting to note that by interchanging the resistors and capacitors of Table I, another set of oscillator circuits is obtainable.

SIMULATION RESULTS

The oscillator circuits proposed in this paper have been checked using PSPICE Student Version 5.0a. Although there are several ways to simulate the current-follower required, the simulation was performed using a second-generation current-conveyor CCII. The current-follower can be obtained using a CCII with its high impedance terminal grounded. The CCII was simulated using the circuit proposed by Senani [4] because of its simplicity. The kernel of the work presented here is independent of the particular simulation selected. The CCII was simulated using
<table>
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<tr>
<td>a</td>
<td>$G_1 sC_2 sC_3 G_4 sC_5 G_6$</td>
<td>$\frac{G_1 G_4 G_6}{\frac{1}{C_2} \left( G_1 + G_6 \right) + \frac{1}{C_3} G_4 \left( C_3 + C_5 \right)}$</td>
<td>$G_1 G_4 G_6 C_2 C_3 C_6 = G_4 (G_1 + G_6) (C_3 + C_5)$</td>
</tr>
<tr>
<td>b</td>
<td>$sC_1 G_2 sC_3 G_4 sC_5 G_6$</td>
<td>$\frac{G_2 G_4 G_6}{\frac{1}{C_3} \left( G_2 + G_6 \right) \left( C_1 + C_5 \right)}$</td>
<td>$G_2 G_4 G_6 C_1 C_3 C_5 = C_1 (C_1 + C_3) G_2 + G_6$</td>
</tr>
<tr>
<td>c</td>
<td>$sC_1 G_2 sC_3 G_4 sC_5 G_6$</td>
<td>$\frac{G_2 G_4 G_6}{\frac{1}{C_3} C_5 \left( G_4 + G_6 \right) + \frac{1}{C_4} G_3 G_6}$</td>
<td>$G_2 G_4 G_6 C_1 C_3 C_5 = C_1 (C_1 + C_3) G_2 + G_6$</td>
</tr>
<tr>
<td>d</td>
<td>$G_1 sC_2 sC_3 G_4 G_5 sC_6$</td>
<td>$\frac{G_1 G_4 G_5}{\frac{1}{C_2} G_3 \left( C_3 + C_6 \right) + \frac{1}{C_4} G_5 G_6}$</td>
<td>$G_1 G_4 G_5 C_2 C_3 C_6 = G_4 G_5 (C_2 + C_3 + C_6)$</td>
</tr>
<tr>
<td>e</td>
<td>$G_1 sC_2 G_3 G_4 sC_5 G_6$</td>
<td>$\frac{G_1 G_3 G_4}{\frac{1}{C_4} \left( C_3 + C_6 \right) \left( G_1 + G_3 \right)}$</td>
<td>$G_1 G_3 G_4 C_2 C_3 C_6 = G_4 (C_3 + C_6) (G_1 + G_3) + G_3 G_6$</td>
</tr>
<tr>
<td>f</td>
<td>$G_1 sC_2 G_3 G_4 sC_5 sC_6$</td>
<td>$\frac{G_1 G_3 G_4}{\frac{1}{C_4} \left( C_3 + C_6 \right) \left( C_3 + C_5 \right)}$</td>
<td>$R_4 = \left( \frac{1}{C_2} + \frac{1}{C_5} \right) \left( C_2 R_1 + C_5 R_1 + C_6 R_3 \right)$</td>
</tr>
<tr>
<td>g</td>
<td>$sC_1 sC_2 G_3 sC_4 G_5 G_6$</td>
<td>$\frac{G_1 G_3 G_4}{\frac{1}{C_4} \left( C_3 + C_6 \right) \left( C_3 + G_3 + G_5 \right) \left( C_3 + G_3 \right)}$</td>
<td>$\frac{1}{C_4} = \frac{G_3}{C_2} + \frac{G_5}{C_1} + \frac{G_3}{C_2} + \frac{G_6}{C_2} \left( \frac{1}{G_3} + \frac{1}{G_2} + \frac{1}{G_6} \right)$</td>
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</table>
one operational amplifier and one operational transconductance amplifier, as shown in Fig. 2. The operational amplifier was simulated using the uA741 model contained in the file called EVAL.LIB available in the PSPICE Student Version. The start of oscillation is caused by the offset voltage of the operational amplifier [5]. To demonstrate that the oscillations are self starting, the voltages across the capacitors are initialized at zero voltages. The operational transconductance amplifier was simulated assuming a finite input resistance of 2 MOhm. To avoid the possible latch-up resulting from the non-zero offset current at terminal Y of the current-follower an antiparallel diode pair is connected between terminal Y and the ground. Fig. 3(a) and (b) shows the simulated oscillations obtained from the circuits of Fig. 1(d) and 1(f). The simulation results are in good agreement with the theory presented.

Date/Time run: 05/10/04 10:38:03 Temperature: 27.0

FIGURE 3 (a) Oscillations obtained from Fig. 1 (d) with $C_2 = C_3 = C_4 = 1$ nF, $R_1 = R_3 = 1$ K and $R_u = 8.2$ K
CONCLUSION

In this paper, new oscillator circuits using unity gain current followers have been presented. Each circuit uses two current followers, three resistors, and three capacitors. The practical implementation of these oscillator circuits is straightforward, as they require unity-gain current-followers, which can be easily obtained from available IC second-generation current-conveyors by grounding their high impedance terminal.

REFERENCES

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