DIGITALLY PROGRAMMABLE INTEGRATOR AND DIFFERENTIATOR

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Digitally programmable integrator and differentiator circuits are presented. Each circuit uses at most one operational amplifier, two operational transconductance amplifiers, and one capacitor. The time constants of the circuits are decided by the biasing currents of the operational transconductance amplifiers. The circuits can be easily interfaced with microprocessor-based systems. Experimental results are included.

INTRODUCTION

The operational transconductance amplifier (OTA) provides highly linear electronic tunability of its transfer gain ($g_m$), requires just a few or even no resistors for its internal circuitry, and has more reliable high-frequency performance than that of the operational amplifier. This justifies the growing interest in designing OTA-based circuits [see for example 1–5 and the references cited therein]. The purpose of this paper is to investigate the feasibility of designing digitally programmable OTA-based integrators and differentiators.

PROPOSED CIRCUITS

Figures 1–3 show the proposed circuits for providing digitally programmable integrators and/or differentiators.

For the circuit of Fig. 1 the transfer function of the integrator is given by

$$\frac{V_o}{V_i} = \frac{1}{1 + sC/g_m}$$

(1)

where $g_m$ is the transconductance of the OTAs. From (1), one can see that the time-constant of the integrator is equal to $C/g_m$, while its dc gain is equal to unity. The two biasing resistors $R_B$ provide the biasing currents to the two OTAs. Since the transconductance is proportional to the bias current ($I_B$), then controlling the bias voltage, which can be obtained from the output of a digital-to-analog converter (DAC), results in changing $g_m$, which, in turn, changes the time-constant of the integrator.
FIGURE 1 Proposed integrator with floating capacitor
For the circuit of Fig. 2 the transfer function of the integrator is given by

$$\frac{\nu_o}{\nu_i} = \frac{g_{m3}/g_{m2}}{1 + sC/g_{m2}}$$

(2)

From (2), one can see that the time-constant of this integrator is equal to $C/g_{m2}$ while its dc gain is equal to $g_{m3}/g_{m2}$. Thus, both the time-constant and the dc gain are digitally programmable. First, the time constant can be adjusted by changing $g_{m2}$ and then the dc gain can be adjusted by changing $g_{m3}$. It is interesting to note that this integrator uses only two OTAs and one capacitor. Thus, its implementation in CMOS technology is feasible.

For the circuit of Fig. 3, the transfer function of the differentiator is given by

$$\frac{\nu_o}{\nu_i} = \frac{1}{1 + g_{m4}/sC}$$

(3)

From (3), one can see that the time-constant of the differentiator is equal to $C/g_{m4}$ while its dc gain is unity. Thus, the time-constant can be adjusted by changing $g_{m4}$.

EXPERIMENTAL RESULTS

The circuits of Figs. 1–3 were tested experimentally. Digital programming is achieved by controlling the transconductances of the OTAs using the output of a digital-to-analog converter. The input of this DAC is obtained from a keyboard. The circuits were built using the CA3080 OTA, the LF356 operational amplifier, and the MC1408 DAC. Fig. 4 shows the pin connections of the DAC. Typical results are shown in Fig. 5. The time constant was successfully controlled from the keyboard.

CONCLUSION

In this paper, digitally programmable OTA-based integrators and differentiators have been presented. The time-constant of these circuits can be controlled by controlling the bias currents of the OTAs. By obtaining these bias currents from the output of a DAC, the time constants can be digitally programmed. The proposed circuits uses the minimum number of resistors for controlling the bias currents and, therefore, are very attractive for integration. The integrator of Fig. 2 uses a grounded capacitor. This is another attractive feature for integration.
FIGURE 2  Proposed integrator with grounded capacitor

From DAC
FIGURE 4 Connection of the DAC used to drive the biasing resistors of the OTAs.
upper trace: input 60 mV amplitude
lower trace: Output 50 mV/div
Frequency: 5 KHz
(a)

upper trace: input 20 mV amplitude
lower trace: output 0.5 V/division
Frequency: 700 Hz
(b)

FIGURE 5 Input and output waveforms obtained from the circuits of Figs. 2 and 3.
REFERENCES


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