Research Article

A Bandpass Filter Based on Half Mode Substrate Integrated Waveguide-to-Defected Ground Structure Cells

Yong Mao Huang, Zhenhai Shao, Zhaosheng He, Chang Jiang You, and Di Jiang

School of Communication and Information Engineering, University of Electronics Science and Technology of China, Chengdu 611731, China

Correspondence should be addressed to Zhenhai Shao; shao_zh@uestc.edu.cn

Received 22 March 2014; Accepted 14 August 2014

Academic Editor: Francisco Falcone

Copyright © 2015 Yong Mao Huang et al. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

A half mode substrate integrated waveguide-to-defected ground structure (HMSIW-DGS) cell and its embedded form are proposed to miniaturize a bandpass filter. Both cells can purchase wideband frequency response and low insertion loss, as well as simple and easy fabrication. By cascading two of them according to design requirement, an X-band bandpass filter is designed and measured to meet compact size, low insertion loss, good return loss, second harmonic suppression, and linear phase.

1. Introduction

In the past decade, substrate integrated waveguide (SIW) has attracted plenty of attention and has been utilized in different microwave and millimeter wave components design due to its merits of higher quality factor, low cost, high power handling capability, and easy integration [1–6]. And its related designs have been expended to on-chip scale [7]. The half mode substrate integrated waveguide (HMSIW), which not only keeps the good performance of SIW but reduces to half size or so of the SIW, has been developed and widely used in varied applications [8–10]. However, its physical size is not small enough compared with the microstrip circuitry, since its conventional rectangular cavity resonator is too large.

The defected ground structure (DGS), which is etched in the ground plane of planar circuit board, provides a promising approach to increase stopband reduction and reduce size of passive components [11–13]. In [14], a new size-reduced lowpass filter (LPF) design method was introduced, which utilized DGS and wide microstrip line to realize the shunt capacitors. Therefore, open stubs, junction elements, and very high-impedance lines, which are always adopted in conventional LPFs, are not required in the new DGS-LPF design in order to achieve compact size. However, all referred DGS above are etched into the bottom side of printed circuits board, which means that the circuits and components must be hung in the air so that they are quite inconvenient to be integrated in system. In order to keep out this drawback, one solution is to utilize DGS in coplanar waveguide (CPW) and SIW [15, 16], called “coplanar DGS,” in order to keep the signal transmission layer and the ground layer with DGS on the same plane.

As unique DGS shapes, the complementary split ring resonator (CSRR) [17, 18] and the complementary spiral resonator (CSR) [19], both of which were proposed as metamaterial resonators and exhibiting negative permittivity, have been utilized in SIW successfully. Both the CSRR and CSR resonators allow the implementation of a forward-wave passband propagating below the characteristic cutoff frequency of SIW. However, insertion losses of all experimental filters reported in [18, 19] are not low enough, which will be unacceptable for millimeter wave applications with critical power requirement.

In this paper, a novel HMSIW-DGS cell with a simple DGS shape and its embedded form, which can also be used as a cell, are proposed. By utilizing these two kinds of cells, a three-pole cascade bandpass filter is realized. According to its measured results, low insertion loss, good return loss, compact size, and suppressed second harmonic are achieved.
2. Analysis and Design

2.1. HMSIW-DGS Cells. Proposed HMSIW-DGS cell and its embedded form are shown in Figures 1(a) and 1(b), respectively. As depicted in Figures 1(a) and 1(b), since all slots are etched into the top metal cover of HMSIW, the device need not be hung in the air and can be integrated easily in the system.

To simplify the analysis, the bandpass transmission characteristic of proposed HMSIW-DGS cell can be seen as a combination of the inherent highpass characteristic provided by HMSIW and the bandgap function created by DGS. Whichever for SIW and HMSIW, as discussed in [1–3], their characteristic cutoff frequencies are mainly controlled by their widths, as labeled as “2a” and “a” in Figure 2. A SIW with 2a is equivalent, in terms of characteristic cutoff frequency, to a conventional rectangular waveguide with a width \( a_{EFF}^{SIW} \) when 2a and \( a_{EFF}^{SIW} \) satisfy the fitted equation [20]

\[
a_{EFF}^{SIW} = 2a - 1.08 \frac{(2R)^2}{D} + 0.1 \frac{(2R)^2}{2a},
\]

where \( R \) and \( D \) are the vias' radius and the spacing between two adjacent vias, respectively. This equivalent rectangular waveguide has the same height \( h \) and is filled with the same dielectric material as the SIW. Cutting the SIW with a width of 2a in half along its longitudinal symmetry plane, an HMSIW with a width of a is obtained [21], as shown in Figure 2. Similarly, its corresponding equivalent waveguide can be described as an open aperture in one side and an electric wall in the other side with a width as

\[
a_{EFF}^{HMSIW} = \frac{a_{EFF}^{SIW}}{2};
\]

owing to the fringing fields effect, this corresponding equivalent waveguide is not proper for calculating the characteristic cutoff frequency of the HMSIW. To solve this problem, a magnetic wall is used to replace the open aperture, so that an additional width, accounting for the effect of the fringing fields, should be considered to the width:

\[
a_{EFF,HMSIW} = a_{EFF,HMSIW}^{'} + \Delta a;
\]

using \( a_{EFF,HMSIW}^{'} \), it will be much easier to calculate the characteristic cutoff frequency of HMSIW.

From (1) to (3), it can be obtained that the characteristic cutoff frequency of an HMSIW with a is lower than the characteristic cutoff frequency of a SIW with 2a, due to the influence of \( \Delta a \). To verify this analysis, a comparison of the cutoff frequencies and transmission characteristics of SIW and HMSIW. The geometrical parameters are \( a = 7 \) mm, \( 2R = 0.5 \) mm, \( D = 0.8 \) mm, and \( h = 0.254 \) mm and relative permittivity \( \varepsilon_r = 2.2 \).

![Figure 2: Comparison of cutoff frequencies and transmission characteristics of SIW and HMSIW. The geometrical parameters are \( a = 7 \) mm, \( 2R = 0.5 \) mm, \( D = 0.8 \) mm, and \( h = 0.254 \) mm and relative permittivity \( \varepsilon_r = 2.2 \).](image-url)
DGS consists of a parallel inductance $L_{\text{DGS}}$ and a parallel capacitance $C_{\text{DGS}}$. Their values can be computed by

$$C_{\text{DGS}} = \frac{5f_c}{\pi (f_{bg}^2 - f_c^2)} \text{ pF},$$

$$L_{\text{DGS}} = \frac{25}{C_{\text{DGS}}(\pi f_{bg})^2} \text{ nH},$$

where $f_c$ and $f_{bg}$, both in gigahertz, are the 3 dB cutoff frequency and bandgap point frequency of the DGS [13].

To make the transmission behavior and working principles of proposed HMSIW-DGS cell clearer, some investigations on different parameters are discussed in detail. All investigations are carried out under the same conditions of relative permittivity $\varepsilon_r = 2.2$, $h = 0.254 \ mm$, $R = 0.25 \ mm$, and $D = 0.8 \ mm$, by using the same commercial FEM simulator mentioned above. The first investigation is about the influence of the DGS’s size, which is mainly determined by the longitudinal length of the slots “d” and the transverse length of the slots “l” in Figure 1(a). Figure 3 shows the transmission response of proposed cell with different “d” and “l”. According to the simulated results, as “d” or “l” gets longer, the upper cutoff frequency and bandgap point shift lower, which means that the larger the DGS is, the lower the upper cutoff frequency and bandgap point are. Furthermore, for combinations “d = 1.0 mm, l = 4.5 mm” and “d = 0.5 mm, l = 5.5 mm,” there is “2d + l = 6.5 mm”, that is, the slots of these two combinations are with the same area. However, the upper cutoff frequency and bandgap point of “d = 1.0 mm, l = 4.5 mm” are lower than those of “d = 0.5 mm, l = 5.5 mm”; that is, the longitudinal length of slots has stronger influence on the upper cutoff frequency and bandgap point than the transverse length of slots. In components design, through tuning “d” and “l,” the upper cutoff frequency and bandgap point can be controlled flexibly.

“b,” the spacing between two slots, as the second important parameter of proposed cell, is investigated, with its results shown in Figure 4. It can be obtained that, as the spacing decreases, the upper cutoff frequency shifts lower slowly, while the bandgap point shifts lower quite fast, which results in a sharper sideband rejection. And the change of the spacing has little influence on the insertion loss.

Figure 5 shows the simulated results of proposed HMSIW-DGS cell with different “c,” that is, the metallic spacing between the edge of DGS and the magnetic wall of HMSIW. From Figure 5(a), when “c = 0.0 mm,” which means that the edge of DGS has met the magnetic wall, the bandwidth of proposed cell is quite narrow, with very sharp rejection slope and wide stopband. Then, once “c” is not equal to zero, the bandwidth of proposed cell increases enormously and rapidly, with the sideband rejection being much weaker. As shown in Figure 5(b), the 3 dB bandwidth of “c = 0.0 mm” is about 500 MHz; however, as “c” increases to 0.5 mm, 1.0 mm, and 1.5 mm, the corresponding bandwidth is increasing to about 4.5 GHz, 5 GHz, and 5.5 GHz, respectively. It can also be obtained from Figure 5(a) that as “c” increases, the upper cutoff frequency of proposed cell shifts higher while the bandgap point shifts lower, which means wider bandwidth and sharper sideband rejection. However, the drawback is that the stopband rejection becomes weaker. Another important effect caused by the spacing between the edge of DGS and the magnetic wall is the passband insertion loss. As depicted in Figure 5(b), the insertion loss gets lower as “c” gets larger. For “c” larger than zero, the passband insertion losses are all less than 0.5 dB, much lower than those of “c = 0.5 mm,” which is over 1 dB. The reason for this phenomenon is that, for “c” larger than zero, a continuous metallic cover occurs along the magnetic wall, which means that there is a shortest direct metallic
path between the input and output ports of the cell. Such a shortest direct metallic path can reduce transmission loss caused by the coupling gaps and provide the best insertion loss performance.

For the embedded form of proposed HMSIW-DGS cell, when the lengths of DGS ("l1," "d1," "l2," and "d2"), the spacing between all couple slots ("b1," "b2"), and the spacing between the edge of DGS and the magnetic wall ("c") change, similar effects on the transmission characteristics as the single form can be obtained. Furthermore, the varying of outer slots can cause the cutoff frequency, bandgap point, and stopband rejection change heavily, while the varying of inner slots just brings a little influence on the transmission characteristics. Based on this property, the components design can be more flexible by using outer slots for the coarse tuning and inner slots for the fine tuning.

2.2. Filter Design. Based on the two proposed HMSIW-DGS cells, a three-cell cascaded bandpass filter is designed. There are two essential elements for a cascaded filter design. One is the basic transmission characteristics of the unit cell; the other is the intercoupling between the adjunct cells. For proposed HMSIW-DGS cells, the strength of the intercoupling is mainly controlled by the space between two adjunct cells, as labeled as "s" in Figure 6; the smaller the space is, the stronger the intercoupling is and the wider the separation between the two concave points of the reflection coefficient is. The geometrical configuration of proposed HMSIW-DGS filter, combining three cascaded cells in the middle with two asymmetrical tapered microstrip-HMSIW transitions at input/output (I/O) ports, and its optimized dimensions are demonstrated in Figure 7. The asymmetrical tapered microstrip-HMSIW transitions are utilized to achieve high external quality factor (Qe). Qe, as mentioned in [22], can be evaluated by the equation

\[
Q_e = \frac{2f_0}{BW_{3\text{dB}}},
\]

where \(f_0\) and \(BW_{3\text{dB}}\) are the central frequency and 3 dB roll-off bandwidth of the filter, respectively. In proposed HMSIW-DGS filter design, \(Q_e\) is mainly determined by length and width of the asymmetrical tapered microstrip-HMSIW transitions. For proposed HMSIW-DGS filter shown in Figure 7,
Figure 7: Geometrical configuration and dimensions of proposed HMSIW-DGS filter.

Figure 8: Photograph of fabricated HMSIW-DGS filter.

Figure 9: Comparison between measured and simulated scattering parameters of proposed HMSIW-DGS filter.

its three-dimensional (3D) full wave analyses, including parameters tuning and dimensions optimization, are carried out by using the same commercial FEM simulator mentioned before. And its simulated scattering parameters and group delay are shown in Figures 9 and 10, respectively.

3. Experimental Results

The proposed filter, with photograph shown in Figure 8, is fabricated on a RT/duriod 5880 substrate with relative permittivity of 2.2 ± 0.02, loss tangent of 0.001 (at 10 GHz), and a thickness of 0.254 mm by standard printed circuits board (PCB) process. This fabricated filter is measured by using an Agilent N5245A vector network analyzer and two 2.4 mm connectors. According to the measured scattering parameters in Figure 9, the proposed filter achieves a central frequency \( f_0 \) of 8.8 GHz, a fractional bandwidth (FBW) of 29%, insertion loss ranging from 0.91 dB to 1.14 dB, return loss around 30 dB, and second harmonic suppression over 35 dB. Its overall size, including two tapered microstrip-HMSIW transitions, is \( 27 \times 7.5 \text{ mm}^2 \), which is quite compact compared with the conventional SIW cavity filters. Figure 10 shows the comparison between simulated and measured group delay of the proposed filter. It can be obtained that the simulated group delay ripple in the passband is around 0.10 ns, while the measured ripple is about 0.15 ns. And the absolute group delay in the passband is less than 0.60 ns. Both the ripple and the absolute group delay show good phase linearity performance, which is quite suitable for wireless data transmission application, especially for the digital microwave communication with high order quadrature amplitude modulation (QAM) [5].

To inspect the performance of proposed filter clearer, some comparisons between proposed filter and previous SIW-DGS or HMSIW-DGS filters reported in the reference are summarized in Table 1. In order to make the comparisons more reasonable, the filters with similar dielectric material, such as relative permittivity of 2.22 and loss tangent of 0.001 (@10 GHz), are selected in references. According to the comparison, filters in [10, 16] are with higher insertion loss and no so good return loss. Although filter in [23] has good performance, it is inconvenient to do system integration.
Table 1: Comparison with filters presented in references.

<table>
<thead>
<tr>
<th>Reference filter</th>
<th>Order</th>
<th>Topology</th>
<th>FBW and $f_0$ (GHz)</th>
<th>Insertion loss (dB)</th>
<th>Return loss (dB)</th>
<th>Size $\lambda_g$</th>
<th>2nd harmonic suppression (dBc)</th>
</tr>
</thead>
<tbody>
<tr>
<td>[10]-1</td>
<td>3</td>
<td>HMSIW-slot</td>
<td>22% and 7.80</td>
<td>1.50</td>
<td>&gt;10</td>
<td>0.52</td>
<td>&gt;20</td>
</tr>
<tr>
<td>[10]-2</td>
<td>5</td>
<td>HMSIW-slot</td>
<td>40% and 8.79</td>
<td>1.20</td>
<td>&gt;10</td>
<td>0.37</td>
<td>&gt;28</td>
</tr>
<tr>
<td>[16]</td>
<td>3</td>
<td>SIW-CSRR</td>
<td>23% and 8.15</td>
<td>2.16</td>
<td></td>
<td>11.6</td>
<td>0.64</td>
</tr>
<tr>
<td>[23]</td>
<td>3</td>
<td>SIW-CSRR-DGS (bottom)</td>
<td>30% and 9.40</td>
<td>1.00</td>
<td>16</td>
<td>1.02</td>
<td>&gt;20</td>
</tr>
<tr>
<td>This work</td>
<td>3</td>
<td>HMSIW-DGS</td>
<td>29% and 8.80</td>
<td>0.91–1.14</td>
<td>30</td>
<td>0.40</td>
<td>&gt;35</td>
</tr>
</tbody>
</table>

* FBW and $f_0$ stand for the fractional bandwidth and central frequency of the filters, respectively.

** $\lambda_g$ is the guided wavelength of the central frequency of the passband.

because its DGS is etched into the bottom metal cover. It is obvious that the proposed filter can achieve lower insertion loss, better return loss, more compact size, and better second harmonic suppression.

4. Conclusion

An X-band three-cell cascaded bandpass filter based on the two proposed HMSIW-DGS cells is designed, fabricated, and measured. The measured results show good agreement with the simulated one. Compared with some reported filters using the same technology and operating at similar frequency, the proposed filter has better performance in low insertion loss, good return loss, compact size, and second harmonic suppression.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

Acknowledgments

This work was supported in part by Excellent Ph.D. Candidate Academic Foundation of UESTC under Grant YBXSZC20130154, in part by the Fundamental Research Funds for the Central Universities under Grant ZYGX2013J006, and in part by the Scientific Research Foundation of UESTC under Grant Y02002010101063.

References


