

Research Article

Prospects of Back Surface Field Effect in Ultra-Thin High-Efficiency CdS/CdTe Solar Cells from Numerical Modeling

Nowshad Amin,^{1,2,3} M. A. Matin,^{1,4} M. M. Aliyu,¹ M. A. Alghoul,²
M. R. Karim,³ and K. Sopian²

¹ Department of Electrical, Electronics and System Engineering, Faculty of Engineering and Built Environment, The National University of Malaysia (UKM), 43600 Bangi, Selangor, Malaysia

² Solar Energy Research Institute, Universiti Kebangsaan Malaysia (UKM), Bangi, Selangor 43600, Malaysia

³ Center of Excellence for Research in Engineering Materials (CEREM), College of Engineering, King Saud University, Riyadh 11421, Saudi Arabia

⁴ Department of Electrical & Electronics Engineering, Chittagong University of Engineering and Technology (CUET), Chittagong 4348, Bangladesh

Correspondence should be addressed to Nowshad Amin, nowshad@eng.ukm.my and M. A. Matin, imamatin@yahoo.com

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Polycrystalline CdTe shows greater promises for the development of cost-effective, efficient, and reliable thin film solar cells. Results of numerical analysis using AMPS-1D simulator in exploring the possibility of ultrathin, high efficiency, and stable CdS/CdTe cells are presented. The conventional baseline case structure of CdS/CdTe cell has been explored with reduced CdTe absorber and CdS window layer thickness, where 1 μm thin CdTe and 50 nm CdS layers showed reasonable efficiencies over 15%. The viability of 1 μm CdTe absorber layer together with possible back surface field (BSF) layers to reduce minority carrier recombination loss at the back contact in ultra thin CdS/CdTe cells was investigated. Higher bandgap material like ZnTe and low bandgap materials like Sb₂Te₃ and As₂Te₃ as BSF were inserted to reduce the holes barrier height in the proposed ultra thin CdS/CdTe cells. The proposed structure of SnO₂/Zn₂SnO₄/CdS/CdTe/As₂Te₃/Cu showed the highest conversion efficiency of 18.6% ($V_{oc} = 0.92$ V, $J_{sc} = 24.97$ mA/cm², and FF = 0.81). However, other proposed structures such as SnO₂/Zn₂SnO₄/CdS/CdTe/Sb₂Te₃/Mo and SnO₂/Zn₂SnO₄/CdS/CdTe/ZnTe/Al have also shown better stability at higher operating temperatures with acceptable efficiencies. Moreover, it was found that the cells normalized efficiency linearly decreased with the increased operating temperature with relatively lower gradient, which eventually indicates better stability of the proposed ultra thin CdS/CdTe cells.

1. Introduction

Over the period of recent decade, cadmium telluride (CdTe) has shown greater promise for efficient, low-cost photovoltaic (PV) solar cells. CdTe-based PV modules are currently entering mass production, but still there are potential scopes remaining for improvement of the cell performance, materials usage, and cell stability. The main technical issues of CdS/CdTe cells are related to the lesser material usage and stable back contact. The polycrystalline CdTe has a high absorption coefficient of over $5 \times 10^5/\text{cm}$, which means that all the potential photons with energy

greater than the bandgap can be absorbed within a very thin CdTe absorber layer. Moreover, CdTe has a direct optical bandgap of 1.45 eV which is very close to the optimum bandgap for solar cells. Hence, the lesser thickness required for an absorbing layer can lead to reduced cell material usage and lower cost of fabrication. Undoubtedly one of the main goals of today's PV research is using less semiconductor material by making the cells thinner. Thinning will not only save material usage, but will also lower the production cost, time, and the energy needed to produce the cells. All of these factors will lead to cheaper and affordable solar cells. Therefore, reduction of absorber and window layers

thickness to the extreme limit are the main focus of this work.

The CdTe thin film solar cells have shown long-term stable performance [1] and high efficiency under AM1.5 illumination for terrestrial usage. From optoelectronic and chemical properties, CdS is the best suited n-type hetero junction partner to CdTe for high efficiency and low cost PV. The maximum theoretical efficiency of CdS/CdTe cell at standard solar spectrum is about 29%. Tyan and Perez-Albuerne reported CdTe/CdS thin film solar cells with efficiency of 10% [2]. Ferekides et al. reported increased in efficiency of 15.8% [3]. Finally a group of NREL researchers reported a record efficiency of 16.5% [4]. The 16.5% efficient champion CdS/CdTe cell used modified cell structure of (CTO/ZTO/CdS/CdTe/Cu:HgTe:Cu_xTe) than the conventional structure (SnO₂/CdS/CdTe/metal) with 10 μm of CdTe and 0.1 μm CdS layer fabricated using three different technologies; close-spaced sublimation (CSS) for CdTe layer, chemical bath deposition (CBD) for CdS layer, and magnetron sputtering for all other layers. This champion cell efficiency (16.5%) is just a little over half of the theoretical limit (29%), but it was estimated that practical CdTe devices with 18-19% efficiencies might be feasible in the near future [5]. This work targets ultra thin CdS/CdTe cells with conversion efficiency over 18% by improving the open circuit voltage (V_{oc}), the short circuit current density (J_{sc}), and the fill factor (FF).

The performance of CdS/CdTe solar cells can further be increased by reducing CdS window layer with suitable buffer layer. By incorporating a very thin resistive buffer layer, the CdS layer thickness can be further reduced down to <50 nm, which significantly improves the blue response, CdS film morphology, and conversion efficiency of the CdS/CdTe devices [6]. It is possible to take advantage of the different properties of two TCOs by forming a buffer layer. High-efficiency CIGS and CdTe devices are generally fabricated with such buffer layer. The CdS window layer is expected to reduce with suitable buffer layer of zinc oxide (ZnO) or zinc stannate (Zn₂SnO₄).

The formation of a stable, low resistance, nonrectifying contact to p-CdTe thin film is one of the major and critical challenges associated with this technology in the fabrication of efficient and stable solar cells [7]. A stable backcontact that is not significantly rectifying and has a low resistance is essential for good performance and long-term stability of CdS/CdTe solar cells. Since CdTe is a p-type semiconductor with a high electron affinity ($\chi = 4.5$ eV) and high band-gap (1.45 eV), a high work function metal is required to make good ohmic contact to p-CdTe. Most metals, however, do not have sufficiently high work functions and therefore form Schottky-barrier contacts to CdTe absorber layer. When the fermi level at the metal/CdTe interface is pinned by surface states, a metal with a reasonably high work function might not make an ohmic contact. The presence of a backcontact barrier can significantly affect the current-voltage (I-V) characteristics of a CdTe cell, primarily by impeding hole transport. This mechanism is one of the causes of the current limiting effect referred to as "rollover". Typically, metals with a high work function ($\varphi_m \geq 5.9$ eV) are required to make

an ohmic contact to CdTe, but mostly they do not have such high work functions to make good ohmic contacts to p-CdTe, instead tend to form Schottky or blocking barriers [8]. A typical approach to overcome this obstacle is to either reduce the barrier height or moderate its width by heavily doping extra layer of back surface field (BSF) with appropriate material between the CdTe and final metal backcontact. The specific BSF materials chosen to investigate in this work are Zinc Telluride (ZnTe), Antimony Telluride (Sb₂Te₃), and Arsenic Telluride (As₂Te₃). The ZnTe, a p-type semiconductor with a direct band gap of 2.26 eV and formation of ohmic contacts to ZnTe is easier due to its lower work function and the ability to dope it highly p-type. The Sb₂Te₃ is a p-type semiconductor with a low optical band gap of 0.3 eV has been used as BSF of CdS/CdTe solar cells. The Sb₂Te₃ was proposed by Romeo et al. at the University of Parma (Italy) for CdS/CdTe solar cells [9]. Recently N. Romeo et al. proposed another potential As₂Te₃ material as BSF [10]. As₂Te₃ is a p-type semiconductor which has a forbidden gap of about 0.6 eV and exhibits resistivity of 10⁻³ Ωcm at room temperature. It melts at 360°C and can evaporate at temperatures higher than 250°C in vacuum; they have reported CdS/CdTe cell with efficiency of 15.8% using As₂Te₃ material.

Numerical modeling techniques can help in the understanding of solar cells, and should give the additional ideas to vary structures and cell parameters to improve the cell performance. Given the complex nature of CdS/CdTe thin film polycrystalline solar cells, the need for numerical modeling is apparent. Numerical simulations give insight into the mechanism of structures, thereby enabling the design of new structures with better efficiency and performance. There are scopes to increase the conversion efficiency of CdS/CdTe solar cells by improving the V_{oc} , J_{sc} , and FF through numerical modeling before involving with fabrication process of the cells. The strategies for improving CdS/CdTe cell output performance have been explored utilizing AMPS 1D (Analysis of Microelectronic and Photonic Structures) [11] simulator, and the obtained results are discussed in the following sections. This analysis has shown that SnO₂/Zn₂SnO₄ front contact with Sb₂Te₃/Mo and As₂Te₃/Cu as BSF are suitable material system for higher efficiency (>17.2%) and stable ultra thin CdS/CdTe cells. However, the performances of the cell with ZnTe BSF have shown better overall stability than other cells.

2. Experimental Modeling and Simulation

Numerical modeling is increasingly being used to obtain insight into the details of the physical operation of solar cells. Modeling and simulation were done using AMPS-1D simulator to explore the possibilities of thinner CdTe absorber and CdS window layers with high conversion efficiency. The AMPS-1D program has been developed to realistically simulate the electrical characteristics of the thin film heterojunction solar cells. It has been tested for thin film CdTe and CIGS solar cells [19]. The baseline case of CdTe cell [12] was utilized to approximate the highest-efficiency

TABLE 1: Material parameters used in simulation.

Parameter	n-SnO ₂	n-ZnO/n-Zn ₂ SnO ₄	n-CdS	p-CdTe	ZnTe/Sb ₂ Te ₃ /As ₂ Te ₃	Comments and references
Thickness, W (μm)	0.01–0.5	0.02–0.5	0.025–0.2	0.1–6.0	0.01–0.5	Variable values
Dielectric constant, ϵ/ϵ_0	9.0	9.0	10.0	9.4	14/55/20	[9, 10, 12, 13]
Electron mobility, μ_e (cm^2/Vs)	100	100/32	100	320	70/1094/500	[10, 12, 14, 15]
Hole mobility, μ_p (cm^2/Vs)	25	25/03	25	40	50/320/210	[10, 12, 14, 15]
Electron and hole concentration, n, p (cm^{-3})	10^{17}	10^{19}	10^{17}	5×10^{15}	7.5×10^{19}	[12, 16, 17]
Bandgap, E_g (eV)	3.6	3.0/3.35	2.42	1.45	2.25/0.3/0.6	[12, 16, 17]
Density of states at conduction band, N_C (cm^{-3})	2.2×10^{18}	2.0×10^{18}	2.2×10^{18}	8.0×10^{17}	7.5×10^{17}	[12, 16–18]
Density of states at valence band, N_V (cm^{-3})	1.8×10^{19}	1.5×10^{19}	1.8×10^{19}	1.8×10^{19}	1.5×10^{19}	[12, 16–18]
Electron affinity, χ (eV)	4.50	4.35/4.50	4.50	4.28	3.65/4.15/4.0	[12, 14–18]

CdS/CdTe solar cells at that time, and it was modified in this work to explore the possibility of efficient ultra thin cells with suitable BSF. Many researchers have carried out simulations with CdTe-based solar cells, and a list can be found in the references given in the review paper by Burgelman et al. [20]. Figure 1 illustrates the CdTe baseline case structure and modified (Glass/SnO₂/ZnO/CdS/CdTe/BSF/Metal) structure for higher conversion efficiency.

It is clear from Figure 1 that the modified structure has an extra layer (ZnO or Zn₂SnO₄) between SnO₂ and CdS layer to achieve thinner CdS window and SnO₂ layers for higher performances. Thus, the front contact consists of SnO₂ plus a buffer layer of ZnO or Zn₂SnO₄. The doping concentration (up to 10^{14} cm^{-3}) used in the baseline case was changed to (up to 10^{15} cm^{-3}) for today's achievable values for CdTe material. Proper BSF were inserted to overcome rollover effect, instability at higher operating temperature and to reduce minority carrier recombination loss at the back contact in ultra thin CdS/CdTe cells. Cheaper metals (Ag/Al/Mo) were used as final back contact material in the proposed cells for the low cost aspect. The number of parameters that can be varied in a particular solar cell model is larger than 50 [20]. Obviously, a problem with 50 variables is too ambiguous and tedious to solve reliably. It is therefore necessary to minimize the number of variable parameters by fixing many of them at reasonable values. It was a tough challenge to choose the appropriate parameters to be used for the different layers of the cells. Many of them depend on fabrication techniques and deposition methods and can thus vary even between devices fabricated in the same batch. Table 1 shows the significant material parameters used in this modeling, which were selected from various literatures as well as estimations based on theoretical and practically variable values.

A three-layer device model of the baseline case was the starting point of the analysis. Five layers that were emphasized in the modified cells are the front contact SnO₂ layer, a buffer layer (ZnO/Zn₂SnO₄), n-CdS layer, p-CdTe layer and BSF layer. In this modeling, all the layer thickness were varied: the CdTe layer from 100 nm up to 6 μm , CdS

window layer from 25 nm to 200 nm, ZnO or Zn₂SnO₄ buffer layer from 20 nm to 500 nm, and BSF layer from 10 nm to 500 nm while keeping all other cell parameters at fixed value as shown in Table 1. This modeling has shown the possibility of a highly efficient 18.6% ($V_{oc} = 0.92 \text{ V}$, $J_{sc} = 24.97 \text{ mA/cm}^2$ and $\text{FF} = 0.81$) CdS/CdTe solar cell with 1 μm of CdTe, 50 nm of CdS, 100 nm of Zn₂SnO₄, and 100 nm of As₂Te₃ layers.

3. Results and Discussion

3.1. CdTe Absorber Layer and CdS Window Layer Optimization. The conventional CdTe baseline structure [12] (SnO₂/CdS/CdTe) was the starting point of this analysis. The conversion efficiency of 15.0% ($V_{oc} = 0.86 \text{ V}$, $J_{sc} = 24.47 \text{ mA/cm}^2$, $\text{FF} = 0.71$) was found from the baseline case structure where CdTe was 4 μm , CdS was 100 nm and Ag as the final back contact metal. When the today's achievable CdTe doping concentration (5×10^{16}) and life time (1.6 ns) were adopted the cell conversion efficiency increased to 17.4% ($V_{oc} = 0.9 \text{ V}$, $J_{sc} = 24.5 \text{ mA/cm}^2$, $\text{FF} = 0.8$). This improvement was achieved mainly from increased V_{oc} and FF due to higher CdTe absorber doping concentration and life time than the baseline case cell. It is noteworthy that in most high efficiency CdS/CdTe solar cells, the CdTe absorber layer is purposely set at 5 μm and above to avoid pinhole formation and uniformity limitation. Theoretically the minimum thickness required is approximately 2 μm for CdTe layer to absorb 99% of the incident photons with energy greater than E_g [4]. However, further numerical analysis was done to reduce the thickness of CdTe and CdS layers aiming to conserve the materials usages and cost of cell production. The CdTe absorber thickness was varied from 100 nm to 6000 nm to explore thinner CdTe absorber layer using AMPS-1D simulator the simulated results are shown in Figure 2.

It is clear from Figure 2 that all the solar cell output parameters were unaffected around the CdTe thickness of 1 μm . Further reduction of CdTe thickness from 1 μm has shown that J_{sc} decreased slowly but the V_{oc} remained almost unaffected and FF increased below 600 nm. At the lower

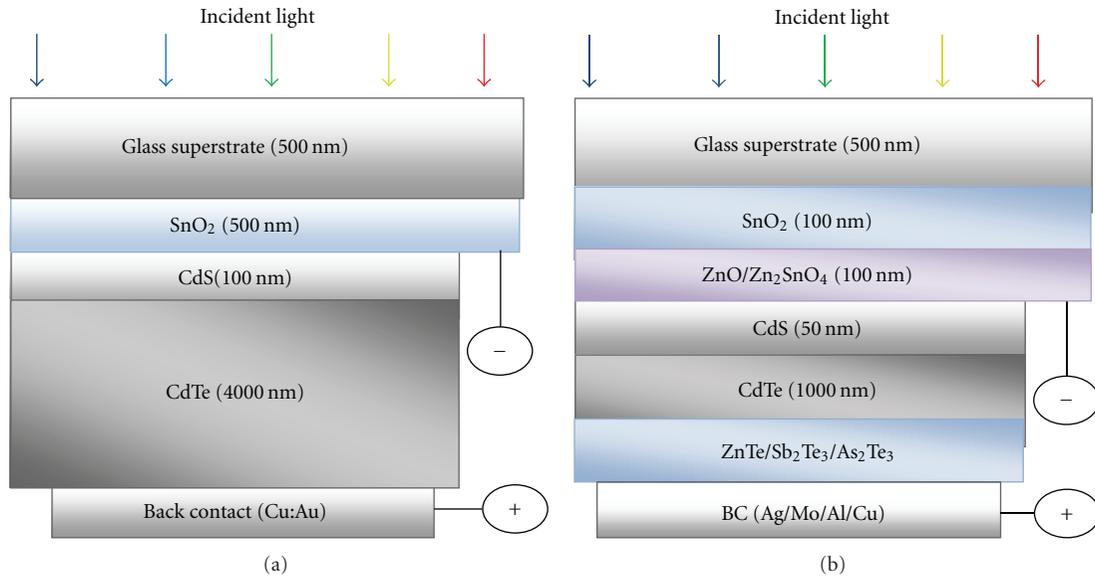


FIGURE 1: Structures of the CdTe solar cells: (a) conventional baseline case structure and (b) modified structure for higher performance.

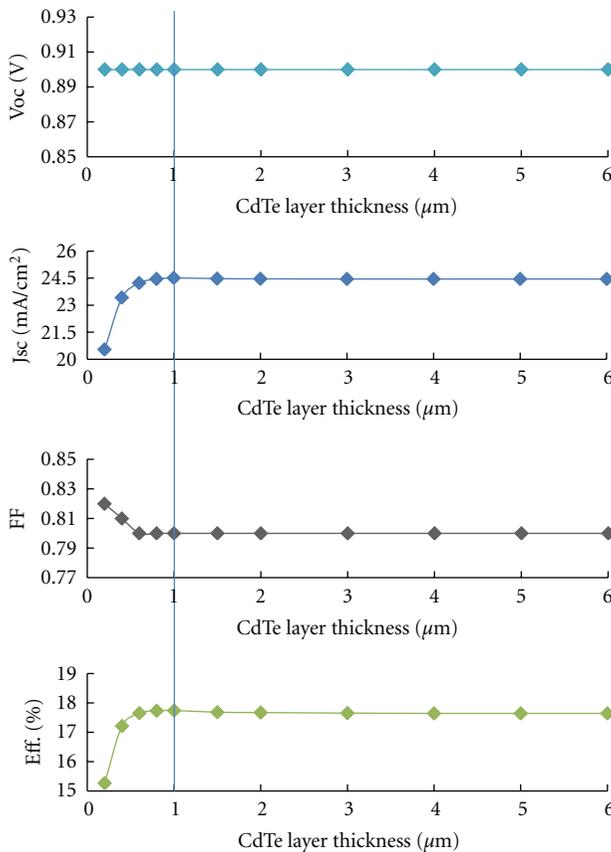


FIGURE 2: Effect of CdTe film thickness on cell parameters.

value of CdTe thickness ($<0.5 \mu\text{m}$) the J_{sc} decreased sharply because the minority carrier diffusion length is critically shorter but FF showed a little increased value due to

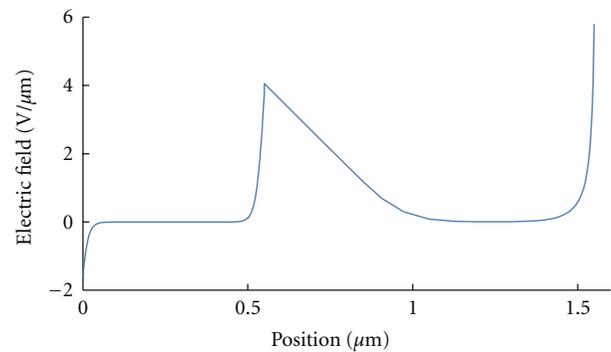


FIGURE 3: Electric field distribution in CdS/CdTe thin film solar cell.

the reduction of bulk resistance for CdTe material. As a combined effect, efficiency decreased with reduced CdTe thickness below $1 \mu\text{m}$, which indicated that selection of $1 \mu\text{m}$ CdTe absorber is safe.

It can be seen from the electric field distribution of Figure 3 for the $1 \mu\text{m}$ thick CdTe cell that the electric field diminishes before $1 \mu\text{m}$ of CdTe thickness. The charge depletion extends approximately $0.6 \mu\text{m}$ into the CdTe layer. If the thickness of the CdTe layer is restricted to smaller values ($<0.6 \mu\text{m}$), then the layer is said to be “fully depleted”. While collection in a fully depleted device is usually good due to the field strength at all positions but built-in potential and hence device voltage tend to be reduced. The $1 \mu\text{m}$ thick CdTe cell shows acceptable conversion efficiency of 17.4% ($V_{oc} = 0.90 \text{ V}$, $J_{sc} = 24.5 \text{ mA}/\text{cm}^2$, $\text{FF} = 0.8$). These results are in good agreements with related published works for CdTe cells [13, 14].

However, there are possibility of increasing J_{sc} as well as the cell conversion efficiency if the CdS window layer thickness (100 nm) can be reduced further with insertion of

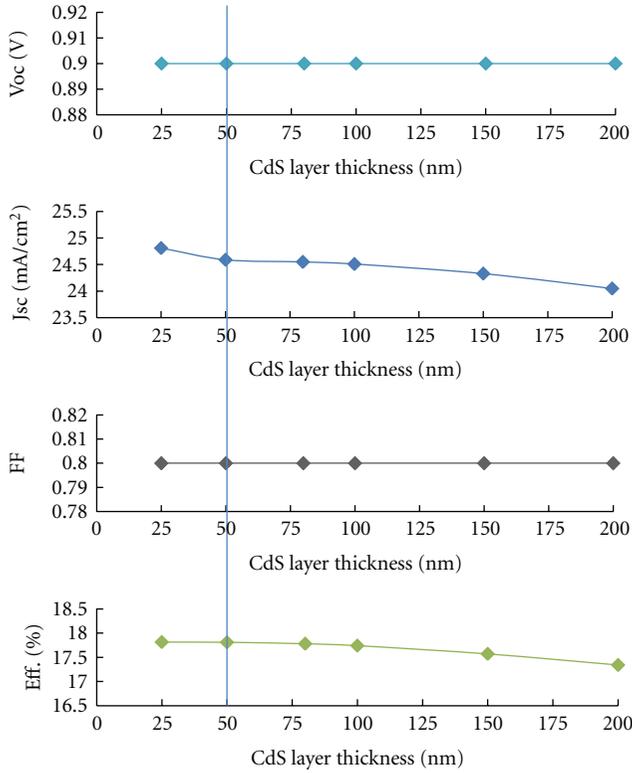


FIGURE 4: Effect of CdS window layer thickness on cell parameters.

a suitable buffer layer between SnO_2 and CdS layer. Moreover, the buffer layer might constitute a barrier against diffusion of unwanted species from the substrate and SnO_2 layer to CdS.

When the CdS film thickness has been reduced to explore thinner CdS layer, the absorption loss in the blue region due to thick CdS layer reduces, which improves mainly J_{sc} and cell conversion efficiency. Figure 4 shows the effects in details of CdS layer variation from 25 nm to 200 nm on the cell output parameters from AMPS 1D simulation, when utilizing all the parameters of Table 1 and $1 \mu\text{m}$ CdTe absorber layer.

Figure 4 shows that J_{sc} improved with reduced CdS layer but V_{oc} and FF are not so much affected. Thus, from the combined effect the conversion efficiency increases with CdS layer thickness reduction. The spectral response (SR) of CdS layer thickness variation from 25 nm to 200 nm is shown in Figure 5 from where the improvement of J_{sc} will be visibly noticeable.

It is evident that when the operating wavelength is below 510 nm the quantum efficiency (QE) was significantly affected with increasing CdS layer thickness. This affected mainly J_{sc} and finally the conversion efficiency of the cell. This result is in good agreement with other published works [14]. However, to date; it is impractical to fabricate cells with CdS film thickness below 50 nm. Considering the fabrication constraint, the CdS film thickness of 50 nm was selected with conversion efficiency of 17.8% ($V_{oc} = 0.9$ V, $J_{sc} = 24.6$ mA/cm², FF = 0.8). Thus, it is very clear that

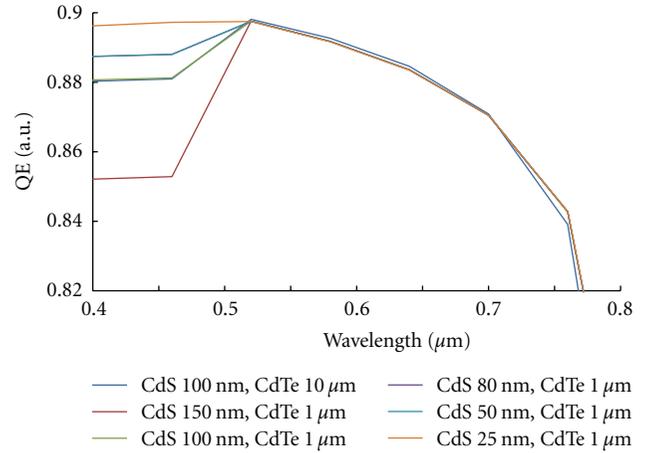


FIGURE 5: Effect of CdS window layer thickness on the spectral response (quantum efficiency).

the improved cell performance has been achieved mainly due to the improvement of J_{sc} .

3.2. Insertion of $\text{ZnO}/\text{Zn}_2\text{SnO}_4$ Buffer Layer. The CdS layer was reduced to 50 nm, for such a thin CdS window layer it was necessary to insert front contact buffer layer for further improvement of the cell performance. This 50 nm thick CdS layer might be attributed to the forward leakage current to front contact through pinholes in the CdS layer. In order to prevent this unwanted forward leakage current a high resistive buffer layer of suitable material (ZnO or Zn_2SnO_4) was inserted between SnO_2 and CdS window layer. Now the front contact of the proposed cells consist of a highly conducting layer of SnO_2 material as TCO for low resistance due to contact and lateral current collection and a much thinner high resistivity layer of a suitable buffer material (ZnO or Zn_2SnO_4). Further numerical analysis was done using AMPS-1D to investigate the cells performances with proposed buffer layers (ZnO or Zn_2SnO_4). The buffer layers were inserted in the designed CdTe cell with $1 \mu\text{m}$ CdTe, 50 nm CdS, 100 nm SnO_2 layers, and all other parameter as in Table 1. Moreover, I-V characteristics obtained from AMPS-1D simulation with buffer layers are shown in Figure 6.

ZnO insertion has shown almost the same result with Zn_2SnO_4 insertion as the material properties are very close to each other. With the insertion of ZnO and Zn_2SnO_4 the I-V curve had shown higher shunt and series resistances. Higher shunt resistance is desired for the better performance of cells, but higher series resistance is undesirable which might reduce the FF. The cell output characteristics are independent of the $\text{ZnO}/\text{Zn}_2\text{SnO}_4$ thickness from 20 nm up to 500 nm. Moreover, it was observed that the SR of the cells were unaffected by the $\text{ZnO}/\text{Zn}_2\text{SnO}_4$ thickness variation from 20 nm to 500 nm, meaning that a very thin buffer layer is adequate for the purpose. Considering the reality in fabrication, the $\text{ZnO}/\text{Zn}_2\text{SnO}_4$ buffer layer thickness was selected 100 nm with maximum cell conversion efficiency of 17.8%. It is noticeable here that with the insertion of buffer layer the efficiency of the cells did not improve but

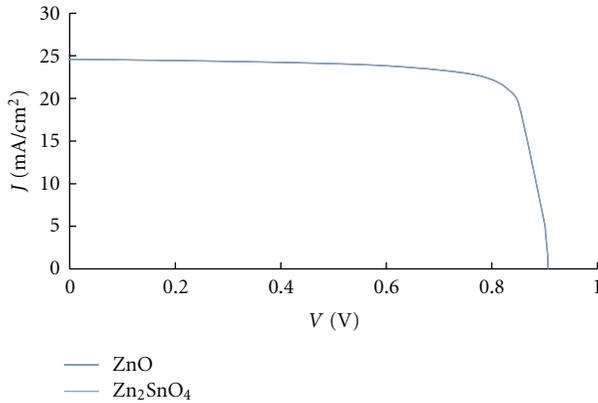


FIGURE 6: Effect of ZnO and Zn₂SnO₄ on the current-voltage characteristics of the solar cell.

the SnO₂ layer was reduced to 100 nm from 500 nm, which means that reduced material use can reduce the production cost. However, until now the proposed cells are with Ag as the final back contact material. There are scopes for further improvement of cell's performance with BSF layer and with other suitable back contact metals.

3.3. Insertion of BSF and Cells Stability. One of the major differences of thin cells compared to the thicker ones is that the absorber/back contact interface is now located closer to the p-CdTe and n-CdS junction, and the choice of the backcontact material therefore has a high impact on the cell performance. A stable backcontact that is not significantly rectifying is essential for good performance and long-term stability of CdS/CdTe cells. The formation of a low resistance, low barrier backcontact is one of the most challenging aspects for high performance CdTe-based solar cells.

Further numerical analysis has been done with the proposed ultra thin cell (SnO₂/Zn₂SnO₄/CdS/CdTe/Ag) aiming to explore the performances with three different BSF materials. The cells with 1 μm CdTe, 50 nm CdS, 100 nm SnO₂, and 100 nm BSF layers were simulated using AMPS-1D; the obtained results are summarized in Table 2.

It is evident from Table 2 that the proposed best cell without any BSF layer shows conversion efficiency of 17.8%, which is a high value for CdS/CdTe cells. When ZnTe is inserted as a BSF the conversion efficiency drops as the FF was strongly affected but the J_{sc} improved a bit due to reduced minority carrier recombination loss at the back contact. The reduction in FF might be attributed to the barrier developed for holes in the valance band and J_{sc} improved due to electrons reflected back from the conduction band, which will be clearer from the band diagram of the cells (Figure 7). When Sb₂Te₃ was inserted as BSF layer the FF and conversion efficiency improved better than that of ZnTe BSF because there is no barrier for holes at the valance band. The As₂Te₃ BSF produced best cell performances and highest conversion efficiency of 18.6%. The improvement in efficiency of this cell came from the improvement of all the cell output parameters like FF, V_{oc} , and J_{sc} .

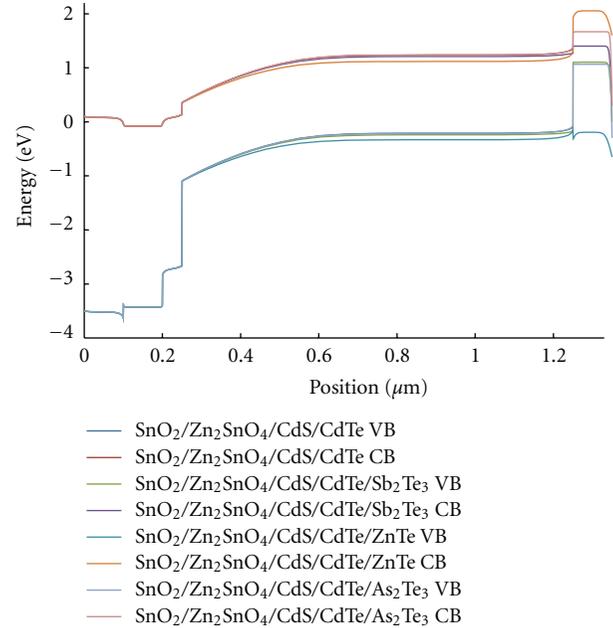


FIGURE 7: Band diagram of the proposed cells.

Figure 7 shows the band diagram of the proposed cells without BSF, with ZnTe/Al BSF, Sb₂Te₃/Mo BSF, and As₂Te₃/Al BSF at zero voltage biasing. The front contacts of the cells are the same (SnO₂/Zn₂SnO₄/CdS) with a band discontinuity of 0.22 eV at CdS-CdTe junction. Without any BSF layer it was found that there is a rectifying back contact with p-CdTe and Ag material which might be responsible for "rollover" of this cell. Smoother collections of holes are anticipated as there is no barrier in the conduction band for all the proposed cells except the cell with ZnTe BSF.

It is clear from the figure that the ZnTe has shown a barrier at the valance band of CdTe and ZnTe interface which might be responsible for its lower FF and conversion efficiency. All the BSF will effectively reflect back the electrons from the conduction band interface of CdTe-BSF towards the front contact as there is adequate barrier height in the conduction band of all the cells with BSF. For Sb₂Te₃ the barrier height is 0.2 eV, for As₂Te₃, the barrier height is 0.45 eV, and for ZnTe barrier height is 0.8 eV. Due to these barrier heights, cells with BSF have shown higher J_{sc} . It seems that 0.2 eV BSF is enough for the purpose of reflecting back the electrons towards front contact. Moreover, J - V characteristics of all the proposed cells are shown in Figure 8.

As can be seen in the figure, the structure with BSF shows higher J_{sc} than the cell without BSF, while the cell with ZnTe/Al shows higher J_{sc} but poor FF due to the barrier for holes in the valance band and higher series resistance added by the bulk resistance of the higher bandgap and lower dielectric property of the ZnTe material. The Sb₂Te₃/Mo shows moderate cell output parameters. In this analysis As₂Te₃/Cu has shown improved performances better than Sb₂Te₃/Mo and ZnTe/Al. Before any final conclusion on the BSF results, there is need to investigate the stability of the cells at higher operating temperatures.

TABLE 2: Output parameters of different cells with BSF.

Different cell structures	V_{oc} (V)	J_{sc} (mA/cm ²)	FF	Eff. (%)
Glass/SnO ₂ /Zn ₂ SnO ₄ /CdS/CdTe/Ag	0.90	24.60	0.80	17.8
Glass/SnO ₂ /Zn ₂ SnO ₄ /CdS/CdTe/ZnTe/Al	0.90	24.92	0.70	15.8
Glass/SnO ₂ /Zn ₂ SnO ₄ /CdS/CdTe/Sb ₂ Te ₃ /Mo	0.91	24.94	0.75	17.2
Glass/SnO ₂ /Zn ₂ SnO ₄ /CdS/CdTe/As ₂ Te ₃ /Cu	0.92	24.97	0.81	18.6

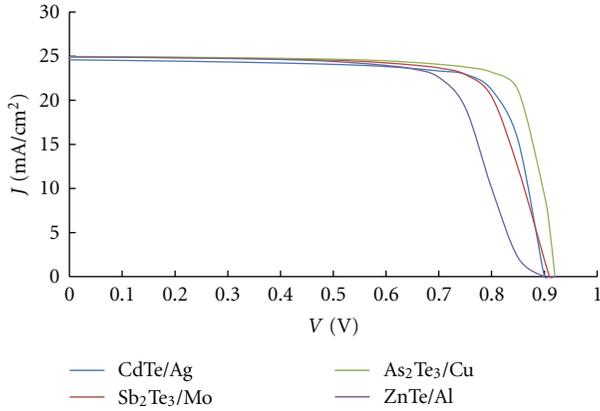
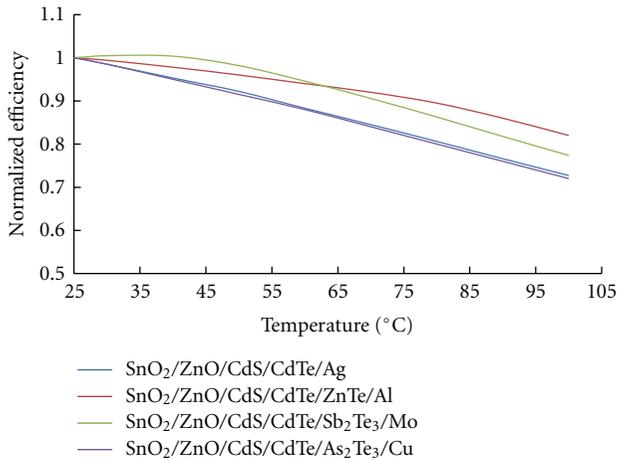
FIGURE 8: J - V characteristics of the proposed cells.

FIGURE 9: Effects of operating temperature on the proposed cell performance.

Operating temperature plays a very important role for the cell performance in the field. It is quite expected that at higher operating temperature, parameters such as the electron and hole mobility, carrier concentrations, density of states, and band gaps of the materials are affected, as also found in this analysis. An investigation was conducted utilizing AMPS-1D at operating temperature ranged from 25°C to 100°C for the proposed cells. The results obtained from AMPS-1D simulation are shown in Figure 9.

In the figure, it is evident that without BSF layer the cell conversion efficiency linearly decreased with the increase of operating temperature with a temperature coefficient (TC) of $-0.4\%/^{\circ}\text{C}$, which is in good agreement with related

works [15, 16]. Cells with Sb₂Te₃/Mo gives better stability as its efficiency remains almost unchanged in the operating temperature range from 25°C to 45°C, and then it decreases with a TC of $-0.4\%/^{\circ}\text{C}$, same as that obtained in cell without BSF. The stability of cell with As₂Te₃/Cu showed the same stability of the cell without BSF. Thus, the As₂Te₃ has no effects on the cell stability at higher operating temperature. The cell with ZnTe showed better stability with TC of $-0.3\%/^{\circ}\text{C}$. However, the performances of the cells with ZnTe have shown better overall stability than other proposed cells.

4. Conclusion

A highly efficient 18.6% ($V_{oc} = 0.92$ V, $J_{sc} = 24.97$ mA/cm² and FF = 0.81) ultra thin CdS/CdTe solar cell with 1 μm of CdTe, 50 nm of CdS, 100 nm SnO₂, 100 nm of Zn₂SnO₄, and 100 nm of As₂Te₃ has been obtained. The higher doping concentration and life time of CdTe absorber lead to higher cell performance. It was found from the CdTe baseline case cell (SnO₂/CdS/CdTe) that 1 μm thick CdTe absorber layer is possible with acceptable range of efficiency. Moreover, it was found that the reduced CdS layer thickness improves the spectral response of the cell at the blue region with suitable buffer layer of ZnO/Zn₂SnO₄. The Zn₂SnO₄ buffer layer insertion showed almost the same results with ZnO insertion. It has also been found that SnO₂/Zn₂SnO₄ front contacts with Sb₂Te₃/Mo and As₂Te₃/Cu as BSF are suitable contacts for higher efficiency ($>17.2\%$) and stable ultra thin CdS/CdTe cells. Moreover, the simulation result of cell operating temperature has shown the stability, to some extent, of the proposed cells at higher operating temperatures. Structures with ZnTe BSF have showed better stability in general, and in particular the structure SnO₂/Zn₂SnO₄/CdS/CdTe/ZnTe/Al achieved the best stability with a linear thermal coefficient of $-0.3\%/^{\circ}\text{C}$. From the consideration of both efficiency and stability, structures with As₂Te₃/Cu (SnO₂/Zn₂SnO₄/CdS/CdTe/As₂Te₃/Cu) proved to be comparable to other reported cells, and the proposed cells can be investigated using standard fabrication techniques of CdS/CdTe cells.

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