Research Article

Efficiency Enhancement of Nanoporous Silicon/Polycrystalline-Silicon Solar Cells by Application of Trenched Electrodes

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Trenched electrodes were proposed to enhance the short-circuit current and conversion efficiency of polycrystalline-silicon (poly-Si) solar cells with nanoporous silicon (NPS) surface layers. NPS films that served as textured surface layers were firstly prepared on heavily doped p^+^-type (100) poly-Si wafers by anodic etching process. Interdigitated trenches were formed in the NPS layers by a reactive-ion-etch (RIE) process and Cr/Al double-layered metal was then deposited to fill the trenches and construct trenched-electrode-contacts (TEC's). Cells with TEC structures (called “TEC cells”) obtained 5.5 times higher short-circuit current than that of cells with planar electrode contacts (called “non-TEC cells”). Most significantly, a TEC cell achieved 8 times higher conversion efficiency than that of a non-TEC cell. The enhanced short-circuit current and conversion efficiency in TEC cells were ascribed to the reduced overall series resistance of devices. In a TEC cell, trenched electrodes provided photocurrent flowing routes that directly access the poly-Si substrates without passing through the high resistive NPS layers. Therefore, the application of NPS surface layers with trenched electrodes is a novel approach to development of highly efficient poly-Si solar cells.

1. Introduction

Because of the high-cost and shortage of single-crystalline silicon (c-Si) wafers, polycrystalline-silicon (poly-Si) had recently become one of the main materials for mass production in solar cell manufacturing industries. Nevertheless, the poly-Si cells are less efficient in power conversion than the c-Si ones due to their lower silicon purity. Efficiency improvement had been an essential task in the development of poly-Si cells. Generally, the light-absorption spectra of poly-Si range primarily from visible light to near-infrared (NIR). Therefore, increasing the absorption efficiency in the solar irradiance spectra, especially the short-wavelength ranges, is one of the key approaches to promote the conversion efficiency of poly-Si cells.

Well-known for its photoluminescence and electroluminescence characteristics at room temperature [1–3], porous silicon (PS) has a variety of unique physical and chemical properties. Especially, it was regarded as a wide-band gap semiconductor due to the quantum-size effect [4]. PS has higher absorption efficiency for short-wavelength light than c-Si and poly-Si. Therefore, the combination of PS with poly-Si becomes a promising alternative in the development of low-cost and high-efficiency solar cells [5–7].

Among the various PS structures, nanoporous silicon (NPS) is great potential for solar cell applications owing to several advantageous characteristics, including the light absorption spectra close to the solar irradiance and low light-reflection coefficient from its effective textured structures [8, 9]. Unfortunately, the high resistivity of this material is apt to cause high series resistance of devices and result in poor conversion efficiency. In this work, trenched-electrode-contact (TEC) structures were proposed in the fabrication of NPS/poly-Si solar cells. Via TECs, photocurrent can flow without passing the highly resistive NPS layers. As a result,
the overall series resistance of devices decreases and thus the conversion efficiency of cells increases.

2. Materials and Methods

According to the related studies, uniform NPS layers were much easily prepared on heavily doped Si substrates [10]. Therefore, to prepare NPS layers, n⁺-type poly-Si surface layers were firstly formed on p-type poly-Si wafers by an iterative-diffusion doping technique using POCl₃/O₂ at 850°C. After five times of diffusion processes, an n⁺-poly-Si surface layer with resistivity of 0.027 Ω-cm can be obtained. Then NPS films were prepared on the heavily-doped n⁺-poly-Si layers by an anodic etching process. The anodization was carried out under etching parameters of HF:C₂H₅OH:H₂O = 18:1:1 at a current density of 5 mA/cm². Thereafter, interdigitated trenches were formed in the NPS layers by a reactive-ion-etch (RIE) process by using a metal mask. A gas mixture of CF₄/O₂ was used as the etchant gas. The flow rates employed were 25 sccm of CF₄ and 3 sccm of O₂ under a pressure of 100 mTorr. The power was held at 100 W and the etching time was 30 s. The estimated etch rate for NPS and poly-Si is about 10 nm/s and 0.5 nm/s, respectively. Because the etch rate of NPS is 20 times higher than that of poly-Si, the bottom of the trench will approximately reach the poly-Si layer at the etching end for a controlled etching time. Finally, by using the same metal mask, double-layered metals of Cr/Al were deposited to fill the trenches by e-beam evaporation and construct the TEC structures. Figure 1 illustrated the schematic structure of the developed NPS/poly-Si cell with a TEC. The shapes of Cr/Al interdigitated electrodes were also shown above the device. The cell's area is about 2 cm × 2 cm. The thickness of NPS and n⁺-poly-Si layer is about 200 nm and 5 μm, respectively. The TEC had metal fingers of 0.2 mm in width with a space of 0.5 mm between two fingers.

The surface morphology and TEC structures of the NPS/poly-Si cells were observed by field-emission scanning electron microscopy (FE-SEM) and an optical microscope (OM), respectively. A four-point probe (MODEL 280Si, Four Dimension Inc.) was used to measure the resistivity of poly-Si. Optoelectronic characteristics of devices were analyzed by a photoresponse measurement system with a TRIAX320 spectrometer. A Solar Simulator (Newport M-91190A) was used to measure the current-voltage characteristics and the conversion efficiency of devices.

3. Results and Discussion

Figure 2 showed the top and cross-sectional view of SEM images of the formed NPS layers on poly-Si substrates. It can be observed that the as prepared NPS layer was with uniformly distributed Si nanoparticles of about 8~10 nm in sizes. Figure 3 showed the local OM images of the formed trenched structures on the NPS/poly-Si before and after metal contacts. The dark-brown and light-brown areas denoted the NPS and poly-Si regions, respectively, as shown in Figure 3(a). We can observe that trenches were formed in the NPS surface layers after the RIE process. As shown in Figure 3(b), the trenches were filled with Al/Cr metals (the green areas) after e-beam evaporation, indicating the TEC structures were successfully constructed without covering the NPS regions.

Figure 4 illustrated the photoresponse spectra of poly-Si with and without a NPS surface layer. It was apparent that a NPS/poly-Si structure achieved higher and broader photoreponse than that of a poly-Si one without a NPS layer. Estimated from the Tauc’s plot, the bandgap energy ($E_g$) of the prepared NPS was about 2.0 eV which is larger than that of poly-Si (1.1 eV). Because the corresponding cut-off wavelength $\lambda_c$ (nm) = 1240/$E_g$ = 1240/2.0 = 620 nm, the enhanced short-wavelength responses within 400~700 nm were thought to be resulting from the light-absorption of NPS layers. Besides, the enhanced long-wavelength response above 700 nm was owing to the effective textured structures of NPS surface layers that reduced the optical reflection and thus increased light-absorption efficiency in the visible to NIR ranges.
Figure 2: (a) The top view and (b) the cross-sectional view of SEM images of the developed NPS/poly-Si structure.

Figure 3: The local images of the formed trench structures on NPS (a) before and (b) after metal contacts, observed by an optical microscope.

Figure 4: Photoresponse spectra of the NPS/poly-Si cell and the poly-Si cell.

Figure 5 showed the photocurrent and dark current of the NPS/poly-Si cells with and without TEC structures. The photocurrent was measured under illumination of 100 mW/cm² at AM1.5 using a solar simulator. Cells with TEC’s (TEC cells) got larger dark current and photocurrent than those of cells without TEC’s (non-TEC cells), indicating that the overall resistance of a TEC cell was lower than that of a non-TEC cell. The estimated average resistance is 60 Ω and 260 Ω for the TEC cell and the non-TEC cell, respectively. The reduced overall resistance of devices is ascribed to the TEC structures. Via TEC’s, the main current of cells can directly access devices, instead of flowing through the high resistive PS layers.

Current-voltage characteristics of NPS/poly-Si cells with and without TEC, measured by a solar simulator, were shown in Figure 6. From the curves in this figure, we can infer that a TEC cell had much lower series resistance than that of a non-TEC one, as a result of decreased contact resistance of devices by using TEC. The measured parameters of current-voltage characteristics were summarized in Table 1. It is highly important to note that a TEC cell achieved a short-current of 11.77 mA and conversion efficiency of 4.1%, which was about 5 times and 8 times larger than those of a non-TEC one, respectively.
Table 1: Parameters of current-voltage characteristics of NPS/poly-Si cells with and without TEC structures. The devices were measured by a solar simulator under irradiation of $P_L = 100$ mW/cm$^2$ at AM1.5, with an irradiated area of 0.5 cm$^2$.

<table>
<thead>
<tr>
<th>Devices</th>
<th>$I_{sc}$ (mA)</th>
<th>$V_{oc}$ (V)</th>
<th>FF</th>
<th>$\eta$%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Non-TEC Cell</td>
<td>2.42</td>
<td>0.41</td>
<td>0.26</td>
<td>0.52</td>
</tr>
<tr>
<td>TEC cell</td>
<td>11.77</td>
<td>0.48</td>
<td>0.36</td>
<td>4.10</td>
</tr>
</tbody>
</table>

4. Conclusions

TEC structures were successfully fabricated in NPS/poly-Si solar cells by means of a RIE process followed a metal-deposition step by e-beam evaporation via a single metal mask. Experimental results demonstrated that TEC structures greatly reduced the overall series resistance of a NPS/poly-Si cell and effectively increased the conversion efficiency of the cell. Therefore, combination of NPS with TEC has high potential in the development of low-cost poly-Si solar cells with high conversion efficiency.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

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References
