Editorial

Selected Papers from the International Workshop on Reconfigurable Communication-Centric Systems on Chips (ReCoSoC' 2010)

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The fifth edition of the Reconfigurable Communication-centric Systems-on-Chip workshop (ReCoSoC' 2010) was held in Karlsruhe, Germany, from May 17th to May 19th, 2010.

ReCoSoC is intended to be a periodic annual meeting to expose and discuss gathered expertise as well as state-of-the-art research around SoC-related topics through plenary invited papers and posters. ReCoSoC is a 3-day long event which endeavors to encourage scientific exchanges and collaborations.

ReCoSoC aims to provide a prospective view of tomorrow's challenges in the multibillion transistor era, taking into account the emerging techniques and architectures exploring the synergy between flexible on-chip communication and system reconfigurability.

This special issue includes the following Topics: embedded reconfigurability in all its forms; on-chip communication architectures; multiprocessor systems-on-chips; system and SoC design methods; asynchronous design techniques; low-power design methods; middleware and OS support for reconfiguration and communication; new paradigms of computation including bioinspired approaches.

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