



Journal of Electrical and Computer Engineering

Special Issue on
Advances in High Frequency Solid State Power Amplifiers

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The Power Amplifier (PA) is an essential component in the realization of many microwave and millimetre-wave systems. PAs applications span over a broad range of areas, among which communication, radar, and satellite systems represent just a few examples. Given such extremely diversified fields, PA specifications may greatly differ on operating, technological, and design requirements. As a consequence, a wide variety of PA realisations exist.

The request of high output power levels is the main driver in the selection of active devices adopted in PA realization. Moreover, to limit the power consumption, high efficiency levels are a primary goal, together with high linearity behaviour, especially for communication applications, where modulated signals have to be accounted for. Consequently, the PA design is typically the result of a trade-off, trying to fulfil several conflicting requirements such as linearity versus efficiency or high output power level versus low distortion.

Different design approaches could be adopted depending on the operating frequency and bandwidth, the available device technology, the application (fixed, mobile, or satellite communications, modulated CW or pulsed signal, etc.), and many other factors.

This special issue is intended to present and discuss the breakthrough technological developments in the field of high frequency and high power amplifier realization.

Potential topics include, but are not limited to:

- ▶ Technologies for solid state PA design
- ▶ Architectures for PA realizations
- ▶ Linearization techniques for PAs
- ▶ Stability issues in PA design
- ▶ Behavioural modelling techniques for PAs
- ▶ Measurement and characterization of PAs

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