Research Article

Design of Microwave Multibandpass Filters with Quasilumped Resonators

Dejan Miljanović,1,2 Milka Potrebić,2 and Dejan V. Tošić2

1m:tel Bosnia and Herzegovina, Kneza Lazara 4, 74000 Doboj, Bosnia and Herzegovina
2School of Electrical Engineering, University of Belgrade, Bulevar Kralja Aleksandra 73, P.O. Box 35-54, 11120 Belgrade, Serbia

Correspondence should be addressed to Milka Potrebić; milka_potrebi@etf.rs

Received 2 September 2014; Accepted 18 November 2014

Academic Editor: Masoud Hajarian

Copyright © 2015 Dejan Miljanović et al. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

Design of RF and microwave filters has always been the challenging engineering field. Modern filter design techniques involve the use of the three-dimensional electromagnetic (3D EM) solvers for predicting filter behavior, yielding the most accurate filter characteristics. However, the 3D EM simulations are time consuming. In this paper, we propose electric-circuit models, instead of 3D EM models, suitable for design of RF and microwave filters with quasilumped coupled resonators. Using the diakoptic approach, the 3D filter structure is decomposed into domains that are modeled by electric networks. The coupling between these domains is modeled by capacitors and coupled inductors. Furthermore, we relate the circuit-element values to the physical dimensions of the 3D filter structure. We propose the filter design procedure that is based on the circuit models and fast circuit-level simulations, yielding the element values from which the physical dimensions can be obtained. The obtained dimensions should be slightly refined for achieving the desired filter characteristics. The mathematical problems encountered in the procedure are solved by numerical and symbolic computations. The procedure is exemplified by designing a triple-bandpass filter and validated by measurements on the fabricated filter. The simulation and experimental results are in good agreement.

1. Introduction

Traditional approach in filter design has several distinct stages: specification, approximation, synthesis, implementation, and study of imperfections. Main filter characteristics, such as filter type, central frequency, bandwidth, insertion loss, and return loss, are given in the specification stage. Mathematical expressions of physically realizable frequency characteristics are formulated in the approximation stage. Development of an electric circuit with ideal elements, from the approximation stage, is carried out in the synthesis stage. The electric circuit is transformed into the physical device in the implementation stage for a desired technology. Study of imperfections is the final filter design stage when the physical structure is tuned or optimized to compensate for various parasitic effects [1, 2].

Particular implementation of the filter elements depends on the chosen technology. If the planar technology is used there are various approaches for filter implementation. These planar filters have been implemented as microstrip filters with open-loop resonators [3], microstrip interdigital filters [4], microstrip SIR (stepped impedance resonator) filters [5], dual-layer filters [6], dual-metal-plane patch-via-spiral filters [7], multilayer filters with apertures on the ground plane [8], and multilayer stacked LC resonator filters [9]. For the above mentioned filter design examples, a common approach is to find relations between geometrical characteristics of the element implementations and the corresponding element values, such as inductances, capacitances, resistances, coupling coefficients, and quality factors (Q-factor). These relations are typically generated by extensive 3D EM simulations but can be obtained experimentally as well. Practically, these relations are presented graphically as a set of curves describing, for example, quality factor as a function of coupling gaps [5], coupling as a function of interresonator spacing and quality factor as a function of tapped-line feed position [9], resonant frequency as a function of resonator dimensions [7], and electric, magnetic, and mixed coupling as a function of apertures.
in the ground plane [10]. Using these data, initial dimensions and mutual positions of element implementations are chosen.

The initial implementation guess, obtained from the set of curves, is refined iteratively in order to meet the filter specifications. The analysis of the actual filter structure and its parts is performed by 3D EM simulators, such as IE3D [11], yielding the most accurate filter characteristics. However, the 3D EM simulations are time consuming and require extensive computer resources, such as CPU speed and the operating memory size. Moreover, the filter parameter refinement often requires repeated 3D EM simulations which further extend the simulation time.

In this paper, we focus on electric-circuit models, instead of 3D EM models, suitable for design of RF and microwave multibandpass filters with quasilumped coupled resonators. The circuit models are composed of linear time-invariant passive lumped elements: capacitors, inductors, coupled inductors, and resistors. Using the diakoptic approach, the 3D filter structure is decomposed into domains that are modeled by linear time-invariant passive electric networks with finite number of elements. Electromagnetic coupling between the domains is modeled by capacitors (for electrical coupling) and coupled inductors (for magnetic coupling). Compared to the 3D EM simulators, the circuit-level simulators analyze the filter almost instantaneously. Therefore, repeated circuit simulations can be efficiently used to identify the key filter parameters which predominantly affect the filter characteristics. Consequently, these key parameters can be effectively used to fine-tune the filter performance and meet the specifications [12, 13].

In recent papers, electric-circuit models for filter design have been often used, as in [5, 7, 14]. They have been used to model filters and to analyze the influence of various filter elements on the filter characteristics. Prior to deploying a circuit model, 3D models of the filter have already been made and elements of the circuit model have been extracted from that 3D model. Circuit model is actually based on the 3D model that is to be implemented.

In this paper, the concept of circuit model is used as a primary tool to analyze and design a filter. A structure of the quasilumped coupled-resonator bandpass filter is proposed, for which the electric-circuit model is derived. The filter structure is decomposed and every filter 3D component is modeled by lumped electric elements. These elements are analytically described and relations between the electric-element values and the geometrical dimensions of the filter structure are established in software [15]. Using circuit models, three single bandpass filters are designed. After fine tuning, these filters are analyzed by 3D EM solvers. Finally, a triple-bandpass filter is designed using the three single bandpass filters. The filter is simulated using the 3D EM model and validated by measurements on the fabricated filter. The simulated results and experimental results are in good agreement.

2. Electric-Circuit Model of Bandpass Filter

In this section, we propose a novel microwave bandpass filter design based on coupled resonators. The resonators are implemented with quasilumped elements, using the planar multilayer technology. This approach has been inspired by filter design techniques reported in [7, 9, 16].

We have made a comprehensive analysis of one type of the multilayer quasilumped resonator and the corresponding feeders [17, 18]. From the design viewpoint, the filter implementation using these multilayer resonators is very flexible, but the 3D EM simulation is time consuming. In this work, one of our objectives is to reduce the simulation time as much as possible.

The multilayer quasilumped resonator is shown in Figure 1. The resonator is realized using double-sided microstrip. The mid layer is the common ground plane.

The resonator contains three elements: a spiral inductor, a via-hole, and a parallel-plate capacitor. The via connects the inner end of the spiral inductor at the top plane and the capacitor at the bottom plane. The outer end of the spiral inductor is open line. The via passes through the structure without electrical connection to the common ground plane.

The points 1 to 3 are marked on the resonator structure (Figure 1) in order to decompose the resonator into domains that are modeled by electrical networks. The points of the filter structure correspond to electric-circuit nodes. An electric-circuit model of the resonator is shown in Figure 2.
In this circuit model, the square-spiral inductor is modeled with π electric network between nodes 2 and 3, and the element parameters are inductance ($L_L$), resistance ($R_L$), and parasitic capacitances ($C_{L1}$ and $C_{L2}$) with their resistances ($R_{L1}$ and $R_{L2}$). The interspiral capacitance ($C_{IC}$) is capacitance between the turns of the spiral inductor.

The parameters of the cylindrical via-hole are inductance ($L_{via}$) and resistance ($R_{via}$) between nodes 1 and 3.

The parameters of the capacitor are capacitance ($C_C$) and resistance ($R_C$) between nodes 1 and 0. Neglecting the via inductance and all circuit losses modeled by resistors, the parallel resonant frequency of the unloaded resonator (Figure 2) is

$$f_0 = \frac{1}{2\pi \sqrt{L_L((C_C + C_{L1})C_{L2}/((C_C + C_{L1}) + C_{L2}) + C_{IC})}},$$

which is determined by the inductance $L_L$ and capacitances $C_{IC}, C_C, C_{L1},$ and $C_{L2}$.

This resonator is used as a main building block for the bandpass filter design. In this research, the second-order bandpass filter is designed, using coupled resonators, as shown in Figure 3. The nodes are marked on the filter resonators (1 to 4 for the first and 1' to 4' for the second resonator), as shown in Figure 3.

There are two possibilities of the interresonator coupling. In the first case, we have inductor-to-inductor coupling on one side of substrate and capacitor-to-capacitor coupling on the other side. This type of coupling is shown in Figure 3(a) and it will be denoted as LLCC coupling (meaning inductor $L$ to inductor $L$ and capacitor $C$ to capacitor $C$). In the second case, we have inductor-to-capacitor coupling on both sides of the layers. This type of coupling is shown in Figure 3(b) and will be denoted as LCLC coupling. These two possibilities require different models of the interresonator coupling. Two electric circuits are shown in Figure 4, corresponding to these two cases of interresonator coupling.

The LLCC coupling is modeled by capacitor (with capacitance $C_{CC}$) between two patch capacitors (nodes 1 and 1'). The capacitance between the end of two inductors (nodes 2 and 2') is modeled by capacitor $C_{LL}$. The mutual inductance ($M_{total} = k_mL_L$) is modeled with inductive coupling between the inductors. The sign of mutual inductance ($M_{total}$) can be positive or negative, depending on the mutual inductor orientation. Symmetric orientation of the inductors is also depicted in Figure 3(a). If we adopt the dot marking convention as in Figure 4(a), then $M_{total}$ is positive for the symmetric inductor orientation and negative for the asymmetric inductor orientation.

For the LCLC interresonator coupling, there is dominantly capacitive coupling between inductor and capacitor, and it is modeled by capacitance $C_{LC}$ (between nodes 1 and 2, 1' and 2'), as in Figure 4(b).

The feeder is realized as a microstrip line whose width corresponds to the characteristic impedance of 50 Ω ($Z_F = 50$ Ω). The feeder is capacitively coupled to the resonator and it is dominantly coupled to the outer end of the resonator inductor. This is modeled by capacitance $C_e$ between nodes 2 and 4 (2' and 4').

3. Analytic Closed-Form Expressions for Quasilumped Element Values

In order to analyze and design filter characteristics, using the electric-circuit model, it is necessary to make a correlation between the circuit-element values and the physical dimensions of the 3D filter structure. For that purpose, we use closed-form expressions for inductance, capacitance, and resistance and a software package for finding matrix parameters of the transmission lines [15]. In this research, the substrate used for filter design is RT/Duroid 5880 ($\epsilon_r = 2.2$, tanδ = 0.001, substrate thickness $h_L = h_C = 1.575$ mm, metallization thickness $t = 18$ μm).

The capacitance of a rectangular parallel-plate structure must be calculated considering the effect of fringing field. This is important when the size of the gap separating the electrodes (i.e., substrate thickness) is comparable to the dimensions of the capacitor plates. In that case, the capacitance cannot be calculated as for a simple model of
parallel-plate devices. The effective capacitance is greater in this case and should be calculated as [19]

\[ C = \varepsilon_0 \varepsilon_r \frac{d_Xd_Y}{d_{\text{sub}}} \left( 1 + \frac{d_{\text{sub}}}{\pi d_X} \left( 1 + \ln \left( \frac{2\pi d_X}{d_{\text{sub}}} \right) \right) \right) \times \left( 1 + \frac{d_{\text{sub}}}{\pi d_Y} \left( 1 + \ln \left( \frac{2\pi d_Y}{d_{\text{sub}}} \right) \right) \right) \]  

(2)

where \( d_X \) and \( d_Y \) are outer capacitor dimensions (in our case \( d_X = d_Y = d_{CC} \)) and substrate thickness \( h_{C} = d_{\text{sub}} \).

The capacitance of the patch capacitor with respect to the outer capacitor dimension \( d_{C} \) is shown in Figure 5.

The inductance of cylindrical via hole in microstrip can be calculated as a modified inductance of a cylindrical conductor and it will be calculated using the following formula [20]:

\[ L_{\text{via}} = \frac{\mu_0}{2\pi} \ln \left( \frac{l_{\text{via}} + \sqrt{r_{\text{via}}^2 + l_{\text{via}}^2}}{r_{\text{via}}} \right) + \frac{3}{2} \left( r_{\text{via}} - \sqrt{r_{\text{via}}^2 + l_{\text{via}}^2} \right) \]  

(3)

For the chosen substrate and intended via radius \( (r_{\text{via}}) \), the obtained value is \( L_{\text{via}} = 1.454 \) nH (length \( l_{\text{via}} = h_L + h_C = 3.15 \) mm and radius \( r_{\text{via}} = 0.15 \) mm). The via resistance is neglected.

The relation between the square-spiral inductance and the square-spiral physical dimensions is given by the following formula [12]:

\[ L = \frac{1.27 \mu_0 N^2}{2} \left( \frac{d_L + d_i}{2} \right) \times \left( \ln \left( \frac{2.07}{\rho} \right) + 0.18 \rho + 0.13 \rho^2 \right) K_g. \]  

(4)

The parameter \( N \) denotes the number of inductor turns, \( d_L \) is the outside diameter and \( d_i \) is the inside diameter of the inductor, \( \rho = (d_L - d_i)/(d_L + d_i) \) is the fill ratio, and \( K_g \) is a correction factor for the presence of a ground plane. \( K_g \) is calculated as follows:

\[ K_g = 0.57 - 0.145 \ln \left( \frac{u_s}{h_L} \right), \quad \frac{u_s}{h_L} > 0.05. \]  

(5)

Using formula (4) one can find the spiral line width \( (w_S) \), the turn spacing \( (s) \), and the outside diameter \( (d_L) \) for the required square-spiral inductance. The approximate maximum number of the inductor turns (for given \( d_L \) is
The parasitic capacitance of the inductor toward ground plane may be calculated using (2) with $d_x = d_y = d_e$. Although layout of the inductor is different from the layout of the capacitor, our research has shown that (2) can be used with great accuracy in the case of greater inductor fill ratio. This is certainly correct in the case of our filter design objectives, where one of the goals is miniaturization. In our model, the parasitic capacitance of the inductor toward ground plane is equally distributed to $C_{L1}$ and $C_{L2}$; that is, $C_{L1} = C_{L2} = C_{C}/2$.

The interspiral capacitance causes parallel resonances of the spiral inductor and it has influence on effective inductance at higher frequencies. The satisfactory estimation of the capacitance value is obtained with [21]

$$C_{IC} = \frac{\varepsilon_{reff} l_s t_s}{s},$$

where $\varepsilon_{reff}$ is the effective relative permeability for the spiral line of width $w_s$ and the given substrate. The parameter $l_s$ is the average spiral length.

The inductor resistance ($R_s$) is calculated by multiplying the spiral length with per unit length resistance of microstrip transmission line of width $w_s$. Per unit length resistance is frequency dependent, and it is obtained using a software package for calculating matrix parameters of the transmission lines [15]. This dependence is shown in Figure 7.

The mutual inductance between two square-spiral inductors can be calculated using approach presented in [12]. The method is based on formula for the mutual inductance for two adjacent sections of lengths $l_i$ and $l_j$, of the same width $w_s$ and mutual spacing $D_{ij}$ (Figure 8(a)):

$$M_{ij} [\text{nH}] = 2 \cdot 10^{-4} l_e \times \left[ \ln \left( \frac{l_e}{D_{ij}} \left( 1 + \frac{R}{D_{ij} l_e} \right)^{1/2} \right) \right]^{1/2} \left( 1 + \frac{D_{ij}^2}{l_e^2} \right)^{1/2} + \frac{D_{ij}}{l_e} \right],$$

where all dimensions are in microns. The parameter $l_e$ is the effective length of the two sections between which the mutual inductance is being calculated. As an approximation, $l_e$ can be considered as an average length of these two sections, $l_e = (l_i + l_j)/2$.

The method for calculating the total mutual inductance is shown in Figure 8(a). The total mutual inductance is calculated as a sum of mutual inductances of each pair of parallel sections, positioned opposite to each other. There is mutual inductance between each segment of the left spiral inductor (denoted from $i = 1$ to $2N$) and each opposite segment of the right spiral inductor (denoted from $j = 1$ to $2N$). In this case, we assume that $N$ is the number of complete turns. Mutual inductances of nonopposite segments are neglected. For the adopted current direction, positive and
negative mutual inductances are marked. The total mutual inductance can be calculated as follows:

\[
M_{\text{total}} = \sum_{i=1}^{N} \sum_{j=1}^{N} M_{i,j} + \sum_{i=N+1}^{N} \sum_{j=1}^{N} M_{i,j} - \sum_{i=1}^{N} \sum_{j=N+1}^{2N} M_{i,j} - \sum_{i=N+1}^{N} \sum_{j=1}^{N} M_{i,j} + 2 \sum_{i=N+1}^{N} \sum_{j=N+1}^{2N} M_{i,j} - 2 \sum_{i=1}^{N} \sum_{j=N+1}^{2N} M_{i,j} \]

(9)

where \(M_{i,j}\) is given by (8) and \(M_{i,j} = M_{j,i}\) for \(i = 1\) to \(N\), \(j = N + 1\) to \(2N\). In the case of the asymmetrical inductor orientation, the total mutual inductance has the opposite algebraic sign. Formula (9) is primarily used to obtain the maximum achievable value of the mutual inductance, because a greater mutual inductance value can cause appearance of transmission zeros. As an illustration, the coupling coefficient \(k_m = M_{\text{total}} / L_L\) is shown in Figure 8(b), with respect to the distance between spiral inductors \(d_{12}\), for several inductor sizes \(d_L\) and \(N = 4\).

The capacitances between the resonators and capacitance between the feeder and the resonator can be found using a software package for calculating matrix parameters of the coupled transmission lines [15]. This approach can be explained by the details shown in Figures 9 and 3.

The capacitance \(C_{LL}\) is equal to the mutual capacitance (sometimes referred to as the coupling capacitance) of a short
The capacitance $C_{IC}$ also influences resonant frequency, but it is less significant. The influence of the via becomes more pronounced at higher frequencies. The interspiral capacitance $C_{IC}$ also has significant influence on filter selectivity. Greater $C_{IC}$ increases filter selectivity but also decreases resonant frequency. A possible way to increase this value is to use greater number of spiral turns and smaller turn spacing $s$. As expected, $C_{LC}$, $C_{CC}$, $C_{LL}$, $k_m$, and $C_F$ determine filter bandwidth, insertion loss, and return loss. The above observations imply that the main resonator element for adjusting filter characteristics is the spiral inductor. Variation of the inductor parameters changes resonant frequency and selectivity simultaneously.

By analytically solving [22] the circuit model for the resonator (Figure 4(a) with $C_{CC} = C_{LL} = 0$ and $k_m = 0$, or Figure 4(b) with $C_{LC} = 0$), the following formula for the resonant frequency is obtained:

$$f_0 = \frac{\sqrt{(2\pi)^2 L_1^{-1} (C_C + C_{LL} + C_F)}}{(C_C + C_{LL} + C_{IC})(C_{LL} + C_F) + (C_C + C_{LL})C_{IC}}.$$  

The most important phenomenon is that filters using two different types of interresonator coupling have different overall characteristics. $LC$ interresonator coupling always generates transmission zeros, while $LLCC$ interresonator coupling does not. This effect for $LLCC$ and $LCLC$ interresonator coupling is shown in Figure 12. Using $LLCC$ interresonator coupling, there is a negative electric coupling $k_e$ between the capacitors (through $C_{CC}$) and positive or negative magnetic coupling $k_m$ between the inductors (through $M_{total}$). If these two couplings have the opposite signs and $k_m$ has sufficiently high value, transmission zeros can emerge. The opposite signs of $k_e$ and $k_m$ can be provided only with the symmetric inductor orientation in the resonators. Sufficiently high positive value of $k_m$ can be achieved using a different patch-via-spiral approach [7]. However, the resonator realization in [7] has grounded inductors which results in a greater
magnetic coupling $k_m$. Using the equivalent circuit proposed here, it can be found that, in the case of LLCC interresonator coupling, the value of $k_m > 0.15$ would be needed in order to have transmission zeros. Such a great value cannot be implemented using the proposed resonator layout.

In the case of LCLC interresonator coupling, the existence of zeros is caused by another phenomenon—the resonance of the inductor and certain capacitors. The resonator equivalent circuit proposed here has a complex inductor model. Therefore, the frequencies at which the transmission zeros occur can be found using the electric-circuit model and modern mathematical tools for expression simplification [22]. This yields to the following formulas for LCLC interresonator coupling (Figure 4(b)):

$$f_{\text{1,trans_zero}} = \frac{1}{2\pi\sqrt{L_C(C_{C} + C_{L1} + C_{IC} + C_{LC})}},$$

$$f_{\text{2,trans_zero}} = \frac{1}{2\pi\sqrt{L_{L1}C_{IC}}}.$$

According to formulas (11) it is evident that the resonance of the spiral inductor and the surrounding capacitors is causing the appearance of the transmission zeros.

5. Filter Design Using Electric-Circuit Model

In this section, we present the novel procedure for the bandpass filter design using the electric-circuit model (from Figure 4(b)) and relations between lumped-element values and their physical dimensions (Section 3). The procedure steps are as follows.

(1) In the first step, lumped-element values of the electric-circuit model are adjusted to meet the designed central frequency and the bandwidth. The most efficient way is to adopt initial dimensions of the resonator in terms of $d_L$ and $d_C$ ($d_L = d_C$) and the dimensions of the spiral inductor $w_s$ and $s$. In our case, with the goal of miniaturization and available milling technique [23], we will adopt dimensions of the square-spiral inductor $w_s = 0.15$ mm and $s = 0.1$ mm. This will give us approximate values of $C_C$, $C_{L1}$, $C_{L2}$, and $C_{IC}$. The only elements now left for adjusting are $L_L$ for the central frequency adjustment; $C_F$ for return loss; $C_{LC}$, $C_{LL}$, $C_{CC}$, and $k_m$ for insertion loss and bandwidth adjustment. We obtain values of $C_F$, $C_{LC}$, $C_{LL}$, $k_m$, and $C_{CC}$ after adjusting frequency response for the circuit model. Although this overall model gives the value of $L_L$, this inductance is notably affected by the influence of feeder capacitance $C_F$ and interresonator capacitances. The observed value of $L_L$ is actually the effective spiral inductance. Therefore, we propose to estimate the actual value of $L_L$ by observing unloaded resonator. The actual value of the inductance will give us actual value of the spiral inductors. This approach means removing interresonator coupling by setting $C_{LC} = C_{LL} = C_{CC} = 0$, $k_m = 0$, and reducing the influence of feeder by decreasing value of $C_F$, while observing $S_{11}$-parameter of the circuit model. $C_F$ should be decreased until it has no influence on resonant frequency (observing the $S_{11}$-parameter). Now, setting the value of $L_L$ to adjust the desired resonator central frequency will give us the actual value of the spiral inductance. Tuned this way, the spiral inductance is found to be the most accurate.

(2) In the second step, feasibility of the obtained lumped-element values is checked using formulas and design curves. If necessary, the first step is repeated and values are modified in order to find feasible solution. In a few iterations, the first variant should be easily achieved.

(3) The third step is realization of 3D EM filter model, performing full 3D EM simulation and comparing the obtained and specified frequency responses.

(4) In the final step, necessary modifications are performed on the 3D EM model and the final simulation and validation has to be done.

In the rest of the text, LCLC interresonator coupling will be considered only, because it provides realizable transmission zeros.

Using the presented approach, we have designed three bandpass filters, whose frequency responses are shown in Figure 13. These filters are designed for central frequencies of 1.6 GHz (Band-I), 3.5 GHz (Band-II), and 5.8 GHz (Band-III), with fractional bandwidths (FBW) of 4% (Band-I), 4.5% (Band-II), and 7.5% (Band-III). These bands are used for GPS navigation, WiMAX access, and WiFi, respectively.

With adopted resonator dimensions $d_L^I = d_C^I = 3.2$ mm, $d_L^{II} = d_C^{II} = 2.4$ mm, and $d_L^{III} = d_C^{III} = 2.2$ mm (superscripts I, II, and III will denote parameters for 1.6 GHz, 3.5 GHz, and 5.8 GHz bands, resp.) corresponding lumped-element values are given in Table 1. The via inductance and all circuit losses are neglected, except the square-spiral inductor resistance ($R_L$).

Using the values from Table 1 and formulas for finding physical dimensions of the resonator, the distance between the feeder and the resonator and the interresonator distance...
6. Triple-Bandpass Filter Design

By incorporating three single bandpass filters into one triple bandpass filter, we obtained the 3D EM model shown in Figure 16(a). The filter dimensions are shown in Figure 16(b). Due to additional interband influences, small adjustments have been made and the final dimensions have been achieved: \( d_1 = d_2 = d_3 = 0.35 \text{ mm}, \) \( d_4 = d_5 = 0.75 \text{ mm}, \) \( d_6 = d_7 = d_8 = 0.5 \text{ mm}, \) \( d_9 = d_10 = d_11 = 0.3 \text{ mm}, \) \( d_12 = 0.2 \text{ mm}, \) \( d_13 = 0.4 \text{ mm}, \) and \( d_14 = 0.62 \text{ mm}. \)

The block-diagram for the triple-bandpass filter is shown in Figure 17(a). Frequency responses of the triple-bandpass filter, obtained by 3D EM and circuit-level simulations, are shown in Figure 17(b).

It can be noticed that the parasitic resonance (marked with “P” in Figure 17(b)) of Band-I resonators (at 4.95 GHz) has disappeared. This is due to the vicinity of the two transmission zeros \( Z_2^1 \) and \( Z_2^3 \). Upper transmission zero of Band-II and lower transmission zero of Band-III have suppressed this effect of unwanted resonance. As it was earlier stated, this resonance is determined by the dimensions of the spiral inductor of Band-I. It is possible to adjust spiral dimensions (length and width) to satisfy required central frequency and to simultaneously tune the parasitic resonance to be canceled by the transmission zeros of the other bands.

By comparing the positions of the transmission zeros \( Z_2^1, Z_2^2, \) and \( Z_2^3 \) (Figures 15(a), 15(c), and 15(e)) with their positions for the triple-bandpass filter (Figure 17(b)), it is shown that they have been shifted toward the corresponding passbands. This happens for each of the three passbands, when a triple-bandpass filter is realized. The zero shifting occurs because each filter, far enough from its passband, behaves predominantly as a capacitor connected between its
input and output ports. This phenomenon is also observed by the circuit-level simulations.

In this final case, the circuit model is used for verification only, not for the design. The values of the circuit lumped elements for the three block-diagrams (in Figure 17(a)) are tuned to match the frequency response obtained by the 3D EM simulation of each filter. This tuning has provided the actual circuit-element values. The spiral inductance and the interspiral capacitance have been tuned to match transmission zeros and retain resonant frequency. The spiral inductances have been changed to $L^I_L = 28.3 \, \text{nH}$, $L^II_L = 9 \, \text{nH}$, and $L^III_L = 4.2 \, \text{nH}$ (initial values: $L^I_L = 30.3 \, \text{nH}$, $L^II_L = 11.4 \, \text{nH}$, and $L^III_L = 5.2 \, \text{nH}$) and the interspiral capacitance to $C^I_{IC} = 0.11 \, \text{pF}$, $C^II_{IC} = 0.1 \, \text{pF}$, and $C^III_{IC} = 0.07 \, \text{pF}$ (initial values: $C^I_{IC} = 0.1 \, \text{pF}$, $C^II_{IC} = 0.06 \, \text{pF}$, and $C^III_{IC} = 0.025 \, \text{pF}$). Additionally, capacitance $C_{IB}$ (interband capacitance) has been taken into account. This is capacitance between capacitors $C_{IC}$ of the first-band and the second-band resonators and also between the second-band and the third-band resonators. Its value is approximately $C_{IB} = 0.05 \, \text{pF}$. Three individual
Figure 15: Frequency responses of three bandpass filters generated by 3D EM and circuit-level simulations (after the second adjustment iteration): (a) Band-I, (c) Band-II, and (e) Band-III. 3D EM models of three bandpass filters: (b) Band-I, (d) Band-II, and (f) Band-III.

circuit models comprised into one circuit describe the overall filter characteristic. It should be noted that the circuit-level simulations, using the proposed circuit model, capture all relevant phenomena important for the filter design procedure.

Disagreement between the desired frequency response (obtained by using the circuit models) and the response obtained by 3D EM simulations is greater for higher frequencies, that is, for higher bands. Two reasons explain this behavior. The first one is that our model is based on the frequency independent lumped elements. The second one is that our implementation of feeder-to-resonator coupling and resonator-to-resonator coupling is based on the outer end of the spiral inductor.

7. Triple-Bandpass Filter Fabrication and Experimental Results

In the previous section, the procedure for the triple-bandpass filter design has been presented and satisfactory results have been obtained. Following the frequency response of the 3D EM simulations, the triple-bandpass filter is fabricated. The filter is fabricated on RT/Duroid 5880 substrate ($\varepsilon_r = 2.2$, $\tan\delta = 0.0027$).
The fabricated filter has a footprint of $0.053\lambda_g \times 0.062\lambda_g$, where $\lambda_g$ is the guided wavelength of a 50 $\Omega$ microstrip line at the central frequency of the first passband (about 7.2 mm x 8.4 mm, not including the feed lines).

The proposed size is among the best achieved results (smallest footprint) compared to different design approaches. In single layer techniques, various triple-bandpass filters are summarized in [25], with the best achieved result of 11.2 mm x 9.3 mm ($0.108\lambda_g \times 0.091\lambda_g$) which confirms the benefits in size reduction of the multilayer approach. Our achieved size is better compared to similar approaches with two-layer substrate $31\text{mm} \times 29\text{mm}$ ($0.37\lambda_g \times 0.34\lambda_g$) [26] and $31.4\text{mm} \times 29\text{mm}$ ($0.285\lambda_g \times 0.264\lambda_g$) [27]. Slightly better results are presented in [28] with triple-bandpass filter size of $2.9\text{mm} \times 2.4\text{mm}$ ($0.0527\lambda_g \times 0.0436\lambda_g$) but with the use of an additional substrate and conductive layer, with total of three substrates ($\varepsilon_r = 10.2$) and four conductive layers.

The filter is measured using Agilent N5227A PNA microwave network analyzer. The simulated and measured
responses of the proposed filter are in good agreement, as shown in Figure 19.

Transmission zeros are located at the frequencies of 1.5, 1.9, 3.3, 4.1, 5.5, and 7 GHz. Measured 3 dB bandwidths are 4%, 4.6%, and 7.5%, insertion losses are 2.56, 2.68, and 2.85 dB, and return losses are 15, 14, and 12 dB at frequencies of 1.6, 3.5, and 5.6 GHz, respectively. Unwanted-frequency suppression is greater than 30 dB between the bands and greater than 20 dB above the third passband. The most notable difference between the simulated responses and the measured ones is represented by the third passband ripple. This is attributed to the manufacturing process during which vias have been manually soldered to the spiral inductor and patch capacitor. More precise machining process (or tuning the interresonator spacing at the existing model) would certainly lead to the better matching of the obtained results.

8. Conclusions

In this paper, we have proposed the use of electric-circuit models for faster design of the multibandpass filters with quasilumped coupled resonators. In addition, we proposed a novel filter which is realized as a multilayer structure. Using the diakoptic approach, the 3D filter structure is decomposed into domains, and each domain is modeled by linear time-invariant passive electrical elements. The element characteristics have been analytically described, while relations between their electrical and physical parameters have been established.

The fast circuit-level simulators have been used to investigate the influence of each element on the filter characteristic. Two types of interresonator coupling have been investigated, \( LLCC \) and \( LCLC \). The limitations of the achievable capacitances, inductances, and mutual inductances have been found. This has yielded the constraints and guidelines in the design of a bandpass filter with the required characteristics.

The values of electric-circuit elements have been mapped to the physical dimensions of the filter elements. This procedure has been used to independently design three bandpass filters which could be combined into one triple-band bandpass filter. Using 3D EM simulations, the obtained triple-band filter structure has been analyzed and modified and the final filter model has been established. The triple-bandpass filter has been fabricated and measured and the results obtained by simulation and measurement are in good agreement. This triple-bandpass filter has also achieved very small size due to the multilayer implementation (7.2 mm × 8.4 mm, i.e., \( 0.053\lambda_p \times 0.062\lambda_p \)).

During the filter design, the number of time-consuming 3D EM simulations has been reduced significantly, compared to the traditional filter design approach. 3D EM simulations have been used at the last stage of the filter design with the element values obtained from the electric-circuit model. Only a few additional simulations have been needed at the end for fine tuning. Along with the very good results regarding the filter size reduction, we have shown that a significant time saving could be achieved, in the filter analysis and design, by means of modeling the 3D EM structure by an accurate electric circuit.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

Acknowledgment

This research is supported by the Ministry of Education, Science and Technological Development of the Republic of Serbia under Grant TR32005.

References


Submit your manuscripts at
http://www.hindawi.com