Research Article

A Collocation-Based Algorithm for Analyzing Bifurcations in Phase Locked Loops with Tanlock and Sawtooth Phase Detectors

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Analysis of bifurcation of second-order analog phase locked loop (PLL) with tanlock and sawtooth phase detectors is investigated. Both qualitative and quantitative analyses are carried out. Qualitatively, the basin boundaries of the attractors were constructed by plotting the stable and the unstable manifolds of the system. The basin boundaries show that the PLL under consideration for certain loop parameters has a separatrix cycle which terminates the limit cycle (out-of-lock state) and the loop pulls-in. This behavior is known in literature as homoclinic bifurcation and the value of the bifurcation parameter where this process occurs is called the pull-in range. Quantitatively, we propose a collocation-based algorithm to compute the separatrix cycle and the pull-in range. The separatrix cycle is approximated by a finite set of harmonics N with unknown amplitudes and by utilizing the fact that this limit cycle bifurcates from a separatrix cycle, a system of nonlinear algebraic equations is derived. For given values of filter parameters and gain, the algorithm numerically solves for the unknown amplitude of the harmonics and the value of the pull-in range simultaneously by evaluating the system at the collocation points. Results demonstrate that phase locked loop with sawtooth phase detector characteristics has the wider pull-in range followed by tanlock and sinusoidal, respectively.

1. Introduction

Phase locked loop is an electronic device with a voltage-controlled oscillator that is continuously adjusted to match the phase or frequency of a sinusoidal input signal. It is an essential part in many electronic circuits used for the demodulation of Amplitude Modulation (AM) and a BPSK (Binary Phase Shift Keying) signals. PLL is used as frequency synthesizers where a stable frequency at multiples of an input frequency can be obtained [1, 2]. PLL is used in modern wireless communication systems for synchronization, clock synthesis, and jitter reduction. In Code Division Multiple Access (CDMA) direct sequence spread spectrum systems (DS-SS), PLL is used to ensure exact code phase timing estimate. Such timing is influenced by clock offsets and by Doppler [3]. Moreover, phase locked loop frequency synthesizers are used in the design of transceivers in wireless communication systems [4]. A classical PLL consists of three components: phase detector, low pass filter, and voltage-controlled oscillator. Figure 1 shows the nonlinear model of the PLL under consideration where phase detector is modeled by the nonlinear function \( g(\varphi) \).

The closed loop phase error \( \varphi \) is given by \( \varphi \equiv \omega_i t - \theta_v \), where \( \omega_i \) is the frequency of the incoming signal and \( \theta_v \) is the phase of the VCO. Other voltage-controlled oscillator parameters are the VCO gain (\( k_v \)) and the free running frequency (\( \omega_o \)). The quantity \( \omega_{os} = \omega_i - \omega_o \) is known in the literature as the loop detuning frequency whereas the closed loop gain is denoted by \( G \). The role of the loop is to match the phase of the signal out of the VCO with the phase of the input signal. It is known that the phase error which is the solution of the autonomous, nonlinear differential equation describing the closed loop phase error loop can be in two different states. One is called the equilibrium state (phase-lock) where the phase error is constant and asymptotically stable and the other one is called the out-of-lock state (false lock) where the phase error is a limit cycle (periodic solution) [5]. Of interest here is the behavior of this limit cycle and how does this limit cycle affect the pull-in process. For second-order PLL, previous results showed that a limit cycle exists as a result of
bifurcation from a separatrix cycle (homoclinic bifurcation). The value where bifurcation occurs ($\omega_{\text{os}} = \omega_p$) is found to be the value of the pull-in range; i.e., for values of $\omega_{\text{os}} < \omega_p$, the loop pulls-in and the PLL is said to be phase locked, whereas for values of $\omega_{\text{os}} > \omega_p$, a limit cycle bifurcates and in this case, the PLL is said to be false-locked. This behavior will be discussed analytically later in this paper. Many studies have been conducted and results concerning nonlinear analysis and bifurcations in phase locked loops can be found in the literature [6–8]. For example, the work by Stensby [9] and Harb and Stensby [10] showed that second-order phase locked loop undergoes saddle-node bifurcation. Stensby and Harb [11] proposed a numerical technique based on Galerkin procedures to compute the pull-in range of a second-order, Type I PLL with sinusoidal phase detector. Later Harb and Ajlouni [12] used a collocation-based algorithm for determining the pull-in range in second-order PLL with sinusoidal phase detector. Harb [13] proposed an iterative algorithm to solve for the pull-in range for different phase detector characteristics. Higher order phase locked loops have been studied in the literature as well. For example, Harb and Harb [14] studied third-order phase locked loop where the loop undergoes Hopf bifurcation and by using the multiple scale method they computed the limit cycle which was born as a result of this bifurcation. Monteiro et al. [15] computed the capture range of third-order PLL based on the location of the equilibrium points. Piquira et al. [16] used bifurcation theory in determining the lock-in range for third-order phase locked loops. The effect of time delay on the pull-in range for phase locked loop has been studied in the literature [17]. The work by Buckwalter and York [18] studied the effect of time delay on the pull-in range of high frequency phase locked loop with sinusoidal phase detector. Harb [19] studied the effect of time delay in PLL with tanlock and sawtooth phase detector characteristics. Stensby [20] derived an exact formula for computing the pull-out frequency for a second-order Type II PLL with triangular phase detector. The idea of synchronization in coupled phase locked loop was investigated by Buckwalter et al. [21] using harmonic balance technique.

Digital phase locked loop with tanlock phase detector characteristics has gained a great attention in literature but less effort has been paid to analyze PLL with tanlock and sawtooth phase detectors [22–27]. In this paper, a collocation-based algorithm is used here to analyze bifurcations and computing the pull-in range of a second-order, Type II phase locked loop with tanlock and sawtooth phase detector characteristics. The effect of using different phase detector on the pull-in range will be evaluated and more attention will be paid to the qualitative analysis where the basins of attraction (equilibrium points and limit cycles) are identified. Moreover, the behavior of the attractors as one of the loop parameters changes will be analyzed and will be used in the formulation of the two parts of the algorithm. The first part approximates the limit cycle by a finite number of harmonics with unknown amplitudes. These amplitudes constitute 2N+1 unknowns that must be found. With frequency detuning, where the limit cycle exists as another unknown, the total number of unknowns will be 2N+2 to be determined. By evaluating the limit cycle at the collocation points, a set of (2N+1) algebraic equations is obtained. In order to solve for the unknowns simultaneously, one more equation is required which constitutes the other part of the algorithm and is obtained by evaluating the limit cycle at the saddle point. Given values of loop parameters and loop gain, the algorithm computes one period of the separatrix cycle and the value of detuning frequency ($\omega_{\text{os}} - \omega_p$) where the loop pulls-in.

The organization of the paper is as follows: Section 2 contains the mathematical model of the PLL under investigation. The qualitative behavior of the PLL is presented in Section 3. Section 4 discusses the proposed method and the simulation. Results and conclusions are presented in Section 5.

2. Second-Order, Type II Phase Locked Loop Model

In this section, the nonlinear differential equation that characterizes the dynamics of the PLL shown in Figure 1 is derived. The loop filter is given by the following transfer function:

$$F(s) = \frac{s + a}{s + b}$$

and the phase detector characteristics, $g(\phi)$, is periodic function with period of $2\pi$. The general form of the closed loop phase error satisfies [10]

$$\frac{d^2\phi}{dt^2} + \left(b + Gg'(\phi)\right) \frac{d\phi}{dt} + ag(\phi) = b\omega_{\text{os}}$$

where $G$ is the closed loop gain and $\omega_{\text{os}}$ is defined as the difference between the frequency of the incoming signal ($\omega_i$) and the VCO free running frequency ($\omega_0$). In this paper, a tanlock and sawtooth detectors are used. The general form of the tanlock phase detector is given by

$$g(\phi) = \frac{(1 + \gamma) \sin \phi}{1 + \gamma \cos \phi}$$

A sinusoidal phase detector is obtained by letting $\gamma = 0$, while, for $\gamma = 0.816$, the phase detector becomes sawtooth.

3. Qualitative Analysis of Second-Order Phase Locked Loop

In this section, the behavior of the solution of the nonlinear differential equation is explained qualitatively. A PLL with sinusoidal phase detector characteristic is assumed here for
the analysis. In this case, the nonlinear differential equation (2) becomes

\[
\frac{d^2 \varphi}{dt^2} + \left( b + G \cos(\varphi) \right) \frac{d\varphi}{dt} + aG \sin(\varphi) = b\omega_0
\]

(4)

The system has two equilibrium points which are given by

\[
(\varphi_1, \dot{\varphi}_1) = \left( \sin^{-1} \left( \frac{b\omega_0}{aG} \right), 0 \right)
\]

(5a)

\[
(\varphi_2, \dot{\varphi}_2) = \left( \pi - \sin^{-1} \left( \frac{b\omega_0}{aG} \right), 0 \right)
\]

(5b)

With all parameters strictly positive, the set of equilibrium points given by (5a) are stable while those given by (5b) are unstable (saddle points). The bifurcation diagram for the system is shown in Figure 2.

In the figure, the x-axis represents \( \varphi \) and the y-axis represents the parameter \( \omega_0 \). The diagram is plotted at fixed values of \( b = 0.1; a = 0.4; \) and \( G = 0.5 \) (the only varying parameter is \( \omega_0 \)). The blue line stands for stable equilibrium point (knot or focus) where the red line stands for the saddle point. The empty circle stands for the saddle point bifurcation. This bifurcation occurs at the point \( \omega_0 = aG/b = 2 \). Two equilibrium points occur after this bifurcation—the stable and the unstable (saddle) points. The square symbol stands for the homoclinic bifurcation (saddle–saddle connection). The homoclinic bifurcation damages the limit cycle—only equilibrium points exist in this system at the values of \( \omega_0 \) lower than the one corresponding to the ordinate of the square. Two different attractors coexist in the region between the circle and the square—a stable limit cycle and a stable equilibrium point. The basin boundaries of these attractors can be constructed by plotting the stable and the unstable manifolds of the system. To do that, all parameters of the system are fixed as before; the only variable parameter is \( \omega_0 \). Figures 3, 4, and 5 show the stable (blue) and the unstable (red) manifolds for different values of \( \omega_0 \). All initial conditions inside the red “tongue” (at \( \omega_0 = 1.8 \) and \( \omega_0 = 1.4 \)) are attracted to the stable fixed point; all initial conditions outside the “tongue” are attracted to the stable limit cycle. The parameter value \( \omega_0 = 1 \) represents the system state after the homoclinic (separatrix cycle) bifurcation; the limit cycle does not occur.
not exist anymore; the only attractor is the stable equilibrium point. Of course, all manifolds are periodic by 2π. Note that\( x = \varphi \) and \( y = \varphi' \) in the plots.

3.1. Equilibrium Points and Their Stability. In this section, the\( \varphi \) detector given by (3). Substitute (3) into (2) to get

\[
\frac{d^2 \varphi}{dt^2} + \left( b + G \left( \frac{(1 + y) \cos \varphi + y(1 + y)}{(1 + \gamma \cos \varphi)^2} \right) \right) \frac{d \varphi}{dt} + a_2 G \left( \frac{(1 + y) \sin \varphi}{1 + \gamma \cos \varphi} \right) = b \omega_{os}
\]  

Define \( x = \varphi \), \( y = \varphi' \).

Then (6) can be written as

\[
\begin{align*}
\dot{x} &= y \\
\dot{y} &= -b - G \left( \frac{(1 + y) \cos x + y(1 + y)}{(1 + \gamma \cos x)^2} \right) y \\
&\quad - a G \left( \frac{(1 + y) \sin x}{1 + \gamma \cos x} \right) + b \omega_{os}
\end{align*}
\]  

(7)

Of interest here are the points where the loop is in the phase locked state. This is found by letting \( \dot{x} = \dot{y} = 0 \) in (7). By doing so, equilibrium points exist at [13]

\[
\begin{align*}
x &= \pi \\
&\quad - \sin^{-1} \left( \frac{2c_1 - \sqrt{4c_1^2 - 4(c_1^2 - c_2^2)(1 + c_2^2)}}{2 + 2c_2^2} \right) \\
&\quad - 2\pi k \\
y &= 0,
\end{align*}
\]  

(8)

where \( c_1 = b_1 \omega_p / \left( a_1 (1 + \gamma) \right) \); \( c_2 = y c_1 \), \( \omega_p = \omega_{os} / G \), \( b_1 = b / G \), \( a_1 = a / G \).

The stability of the equilibrium points must be determined. In order to do that, one needs to find the eigenvalues of the Jacobian matrix. The Jacobian matrix is given by

\[
J = \begin{bmatrix}
0 & 1 \\
-Gg''(x)y - aGg'(x) & - \left( b + Gg'(x) \right)
\end{bmatrix}
\]  

(9)

where \( G \) is the derivative with respect to \( x \). The eigenvalues are the roots of the following characteristic equation:

\[
\lambda^2 + \left( b + Gg'(x) \right) \lambda + Gg''(x) y + aGg'(x) = 0.
\]  

(10)

It is shown that values of \( c_1 \) satisfy the following inequality [10]:

\[
\begin{align*}
(y^2 - 1) \left[ 1 + y^2 c_1^2 \right] &< -c_1^2 \left[ 1 + 2 \sqrt{1 - [1 - y^2]} \left[ 1 + y^2 c_1^2 \right] + 1 \right] \\
&\quad - (1 - y^2) \left( 1 + y^2 c_1^2 \right)
\end{align*}
\]  

(11)

The eigenvalues will be of opposite signs which imply the existence of a saddle point. This saddle point plays a major role for computing the separatrix cycle (saddle-saddle connection) and hence the pull-in range.

4. Computation of the Separatrix Cycle and the Pull-In Range

In this section, the value of detuning \( \omega_{os} \) for which a separatrix cycle exists is computed using the collocation method. We follow the same procedures described by Harb and Al-Ajlouni [12]. The computational process is as follows. Consider the nonlinear ordinary differential equation given by (7). This equation can be written as

\[
\begin{align*}
\frac{dy}{dx} &= -b - G \left( \frac{(1 + y) \cos x + y(1 + y)}{(1 + \gamma \cos x)^2} \right) y \\
&\quad - a G \left( \frac{(1 + y) \sin x}{1 + \gamma \cos x} \right) \omega_{os}
\end{align*}
\]  

(12)

or

\[
\begin{align*}
\frac{dy}{dx} &= -b - G \left( \frac{(1 + y) \cos x + y(1 + y)}{(1 + \gamma \cos x)^2} \right) y \\
&\quad + a G \left( \frac{(1 + y) \sin x}{1 + \gamma \cos x} \right) - \omega_{os} = 0
\end{align*}
\]  

(13)

The periodic limit cycle of (13) can be represented by a finite number of harmonics and is given by

\[
y = \alpha_0 + \sum_{k=1}^{N} \alpha_k \cos (kx) + \beta_k \sin (kx)
\]  

(14)

where \( N \) is the number of harmonics. The main task is to solve for the amplitudes of the harmonics and the value of \( \omega_{os} \) where bifurcation occurs for given loop gain (G). Note that the amplitude coefficients \( \alpha_0, \alpha_1, \ldots, \alpha_N, \beta_1, \ldots, \beta_N \) and \( \omega_{os} \) are unknowns to be determined. The first \( (2N + 1) \) algebraic equations are obtained by defining the residual R to be

\[
R = \frac{dy}{dx} + b - G \left( \frac{(1 + y) \cos x + y(1 + y)}{(1 + \gamma \cos x)^2} \right) y \\
&\quad + a G \left( \frac{(1 + y) \sin x}{1 + \gamma \cos x} \right) - \omega_{os} = 0
\]  

(15)
Next, we need to evaluate (15) at the collocation points which are the zeros of orthogonal polynomials such as Chebyshev polynomial or Legendre polynomial [28]. A Chebyshev polynomial of the first kind of degree \((2N+2)\) is used which is given by

\[
T_0 (x) = 1, \\
T_1 (x) = x, \\
T_n (x) = 2x T_n (x) - T_{n-1} (x)
\]  
(16)

Solving for \(R\) at the collocation points \(x_j\) (zeros of the derivative of Chebyshev polynomial) yields \((2N+1)\) algebraic equations with \((2N+2)\) unknowns which are given by

\[
R (x_j; \alpha_0, \alpha_1, \ldots, \alpha_N, \beta_1, \ldots, \beta_N, \omega_{os}) = 0 
\]
for \(0 \leq j \leq 2N\)  
(17)

As mentioned before, another equation is required to solve for the \(2N+2\) unknowns simultaneously. This equation is obtained from the qualitative analysis discussed in Section 3 where it is verified that the separatrix cycle must approach the saddle point. Note that (7) has a saddle point for a single value of \(\omega_{os}\) which is given by (8). This result leads to the \((2N+2)\)th which is given by

\[
y (x) \big|_{x = x_{saddle}} = \pi - \sin^{-1} \left( \frac{2c_1 - \sqrt{4c_1^2 - 4 (c_1^2 - c_2^2)(1 + c_2^2)}}{2 + 2c_2^2} \right) \]
(18)

\[
y (x) = 0
\]

Now, (17) and (18) have \((2N+2)\) unknowns and \((2N+2)\) equations. Solving these unknowns required initial estimates for the unknowns and a value for the loop gain (G). This is done by approximating the limit cycle with only DC component, \(\alpha_0\), and first harmonics, \(\alpha_1\) and \(\beta_1\), and assuming other unknowns to be zeros. The following example illustrates the procedures.

**Example 1.** In what follows, a numerical example is presented to test the algorithm outlined above for several values of \(N\). In this example, the loop filter parameters (\(a = 100, b = 10\)) with \(N = 1\) are used. In this case, the separatrix cycle is approximated by

\[
y \equiv \alpha_0 + \alpha_1 \cos (x) + \beta_1 \sin (x) \]
(19)

and \(R\) becomes

\[
R = \left[ \alpha_0 + \alpha_1 \cos (x_j) + \beta_1 \sin (x_j) \right] \\
\cdot \left[ -\alpha_1 \sin (x_j) + \beta_1 \sin (x_j) \right] \\
+ \left( b + G \left( \frac{(1 + \gamma) \cos x_j + (y + 1) y}{(1 + y) \cos x_j} \right) \right) \\
\cdot \left[ \alpha_0 + \alpha_1 \cos (x_j) + \beta_1 \sin (x_j) \right] \\
+ aG \left( \frac{(1 + \gamma) \sin x_j}{1 + y \cos x_j} \right) - b\omega_{os} = 0
\]
(20)

A Chebyshev polynomial of degree 4 is used here and is given by

\[
T_4 (x) = 8x^4 - 8x^2 + 1, \quad x \in [-1, 1].
\]
(21)

After normalization of \(T_4 (x)\) in the interval \(x \in [-\pi, \pi]\) (note that one period of the separatrix cycle is needed), (21) becomes

\[
G_4 (x) = \frac{8x^4}{\pi^4} - \frac{8x^2}{\pi^2} + 1, \quad x \in [-\pi, \pi].
\]
(22)

The values of collocation points are given by the zeros of the derivative of \(G_4(x)\). These values are substituted into (17) to get the following three algebraic equations:

\[
R \left( x = \frac{\pi}{\sqrt{2}}; \alpha_0, \alpha_1, \beta_1, \omega_{os} \right) = 0,
\]
(23)

\[
R \left( x = -\frac{\pi}{\sqrt{2}}; \alpha_0, \alpha_1, \beta_1, \omega_{os} \right) = 0,
\]
(23)

\[
R (x = 0; \alpha_0, \alpha_1, \beta_1, \omega_{os}) = 0.
\]
(23)

And the fourth equation is given by

\[
y (x) \big|_{x = x_{saddle}} = 0
\]
(24)

where \(x_{saddle}\) is given by (8). To solve for the above system, MATLAB was used for this task. For a value of \(\omega_{os}\), \(a\), \(b\), and gain \(G\), and using (23), initial guess is obtained for the coefficients \((\alpha_0, \alpha_1, \beta_1, \omega_{os})\). For a specified number of harmonics (\(N\)), these estimated coefficients are supplied to the algorithm while other coefficients \((\alpha_2, \alpha_3, \ldots, \alpha_N, \beta_2, \ldots, \beta_N)\) assumed to be all zeros. Figures 6, 7, and 8 show the phase plane plots (\(\phi \) versus \(db/dt\)) of one period of separatrix cycles computed for different values of gain \(G\) with \(N = 10\) for tanlock, sawtooth, and sinusoidal phase detectors, respectively. Fixe values of \(a = 100\) and \(b = 10\) throughout the simulation. Figure 9 shows values of \(G\) (gain) and \(\omega_{os}\) (pull-in detuning frequency) where a separatrix cycle exists for all phase detectors. Each point on this means that the PLL has a separatrix cycle. As \((G, \omega_{os})\) moves away from the graph, no separatrix cycle exists and the PLL may reach a stable equilibrium state (phase-lock) or out-of-lock state as discussed in Section 3 which means that the separatrix cycle is the boundary between the phase-lock and the out-of-lock states.
5. Conclusions

In this paper, new results on the nonlinear analysis of second-order phase locked loop with tanlock and sawtooth phase detector characteristics are reported. We have proved the existence of the separatrix cycle qualitatively which have not been addressed in the literature. The basin boundaries of the attractors were constructed by plotting the stable and the unstable manifolds of the system. The qualitative analysis showed that the PLL pulls in as a result of homoclinic bifurcations. The value of the bifurcation parameter (pull-in frequency) where this type of bifurcation occurs was computed via a collocation-based algorithm. Results showed that PLL with sawtooth phase detector characteristics has the wider pull-in range amongst all phase detectors considered in this study. These results should help system designer when evaluating other key performance parameters and the overall performance of the PLL.

Data Availability

The data used to support the findings of this study are included within the article.

Conflicts of Interest

The authors declare that they have no conflicts of interest.

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