Research Article

Supplementary High-Input Impedance Voltage-Mode Universal Biquadratic Filter Using DVCCs

Jitendra Mohan¹ and Sudhanshu Maheshwari²

¹ Department of Electronics and Communications, Jaypee Institute of Information Technology, Noida 201304, India
² Department of Electronics Engineering, Z. H. College of Engineering and Technology, Aligarh Muslim University, Aligarh 202002, India

Correspondence should be addressed to Jitendra Mohan, jitendramv2000@rediffmail.com

Received 9 April 2012; Accepted 12 July 2012

Academic Editor: Andrzej Dzielski

Copyright © 2012 J. Mohan and S. Maheshwari. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

To further extend the existing knowledge on voltage-mode universal biquadratic filter, in this paper, a new biquadratic filter circuit with single input and multiple outputs is proposed, employing three differential voltage current conveyors (DVCCs), three resistors, and two grounded capacitors. The proposed circuit realizes all the standard filter functions, that is, high-pass, band-pass, low-pass, notch, and all-pass filters simultaneously. The circuit enjoys the feature of high-input impedance, orthogonal control of resonance angular frequency ($\omega_o$), and quality factor ($Q$) via grounded resistor and the use of grounded capacitors which is ideal for IC implementation.

1. Introduction

Analog filters are the basic building blocks and widely used for continuous-time signal processing. Application of analog filters employing current-mode active elements extends over a large number of fields [1]. The filter circuits may be used in phase-locked loop frequency modulation stereo demodulators, touch-tone telephone tone decoder, and cross-over networks in a three-way high fidelity loudspeaker [2, 3]. In the literature several voltage-mode biquadratic filters are presented which uses different types of current conveyors [4–28]. The voltage-mode filters with high-input impedance are of great interest because they can be easily cascaded to synthesize higher-order filters [7, 10, 13, 17, 26, 28]. In the literature there are a number of voltage-mode universal biquadratic filters with single-input multiple-outputs (SIMO) [4–12, 18] that are available. However, these reported circuits suffer from one or more of the following drawbacks:

(i) excessive use of passive components [4, 6, 7];
(ii) low-input impedance [5, 6, 8, 9, 11, 18];
(iii) lack of orthogonal control over the resonance angular frequency ($\omega_o$) and quality factor ($Q$) [10, 12].

In this paper, a new voltage-mode universal biquadratic filter using three differential voltage current conveyors [29], two grounded capacitors, and three resistors is presented. The circuit realizes the entire standard filter functions, that is, high pass (HP), band pass (BP), low pass (LP), notch (NH), and all pass (AP) from the same circuit configuration. The circuit also possesses high-input impedance and provides orthogonal control of the $\omega_o$ and $Q$ via grounded resistor.

2. Differential Voltage Current Conveyor (DVCC)

The DVCC is first introduced long back as a modified current conveyor by Pal [30] and developed by Elwan and Soliman in 1997 [29]. It is a versatile building block, especially designed to handle differential and floating input signals [30, 31]. Using standard notation, the terminal relations of a DVCC
are shown in Figure 1 and described by the following matrix equation:

\[
\begin{bmatrix}
I_{Y_1} \\
I_{Y_2} \\
V_X \\
I_{Z+} \\
I_{Z-}
\end{bmatrix} =
\begin{bmatrix}
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
1 & -1 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 \\
0 & 0 & -1 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
V_{Y_1} \\
V_{Y_2} \\
I_X \\
V_{Z+} \\
V_{Z-}
\end{bmatrix}.
\] (1)

The difference of the \( Y_1 \) and \( Y_2 \) terminal voltages is conveyed to the \( X \) terminal; the current input at the \( X \) terminal is conveyed to the \( Z_+ \), with the same polarity and \( Z_- \) terminal with inverse polarity. The DVCC is characterized by high-input impedance at the \( Y_1 \) and \( Y_2 \) terminals, high-output impedance at the \( Z_+ \) and \( Z_- \) terminals, and low impedance at the \( X \) terminal.

3. Proposed Circuit

The proposed voltage-mode universal biquadratic filter is shown in Figure 2, employing three DVCCs, three resistors, and two grounded capacitors. The routine analysis of the proposed circuit in Figure 2 using (1) yields the filter transfer functions as

\[
\begin{align*}
\frac{V_{OUT1}}{V_{IN}} &= \frac{1}{s^2C_1C_2R_1R_2 + sC_1R_3 + 1} \\
\frac{V_{OUT2}}{V_{IN}} &= \frac{s^2C_1C_2R_1R_2}{s^2C_1C_2R_1R_2 + sC_1R_3 + 1} \\
\frac{V_{OUT3}}{V_{IN}} &= \frac{sC_1R_1}{s^2C_1C_2R_1R_2 + sC_1R_3 + 1} \\
\frac{V_{OUT4}}{V_{IN}} &= \frac{s^2C_1C_2R_1R_2 + 1}{s^2C_1C_2R_1R_2 + sC_1R_3 + 1} \\
\frac{V_{OUT5}}{V_{IN}} &= \frac{s^2C_1C_2R_1R_2 - sC_1R_4 + 1}{s^2C_1C_2R_1R_2 + sC_1R_3 + 1}.
\end{align*}
\] (2)

It can be seen from (2) that an LP response is obtained from \( V_{OUT1} \); HP response is obtained from \( V_{OUT2} \); BP response is obtained from \( V_{OUT3} \); NH response is obtained from \( V_{OUT4} \); if \( R_3 = R_1 \), AP response is obtained from \( V_{OUT5} \). In all the cases, the parameters \( \omega_0 \) and \( Q \) of the filter can be found as

\[
\begin{align*}
\omega_0 &= \left( \frac{1}{C_1C_2R_1R_2} \right)^{1/2}, \\
Q &= \frac{1}{R_3} \sqrt{\frac{R_1R_2C_2}{C_1}}.
\end{align*}
\] (3)
From (3), the Q of the proposed filter can be controlled independently of \( \omega_b \) by varying \( R_3 \). Since the input voltage signal is connected directly to the \( Y_1 \) port of the DVCC (1) and the input current to the \( Y_1 \) port is zero (\( I_{Y_1} = 0 \)), the circuit has the feature of high-input impedance. Thus the proposed circuit is capable to realize all five standard filter functions simultaneously and without changing the circuit topology, unlike the previously proposed circuit in [26]. The circuit reported in [26] uses two floating resistors but the proposed circuit employs only one floating resistor. The circuit needs no component-matching conditions except for the all-pass filter realization.

Note that the proposed circuit uses two grounded capacitors at the \( Z \)-terminal and three resistors at the \( X \)-terminal of the DVCCs. The design offers the feature of a direct incorporation of the shunt parasitic capacitances at the \( Z \)-terminals and the series parasitic resistances at the \( X \)-terminal, as a part of the main capacitors (\( C_1 \) and \( C_2 \)) and resistors (\( R_1 \), \( R_2 \), and \( R_3 \)).

4. Nonideal Analysis

Taking the nonidealities of the DVCC into account, the relationship of the terminal voltages and currents of the DVCC can be rewritten as

\[
\begin{bmatrix}
I_{Y_1} \\
I_{Y_2} \\
V_X \\
I_{Z+} \\
I_{Z-}
\end{bmatrix} =
\begin{bmatrix}
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
\beta_{k1} & -\beta_{k2} & 0 & 0 & 0 \\
0 & 0 & \alpha_{k1} & 0 & 0 \\
0 & 0 & -\alpha_{k2} & 0 & 0
\end{bmatrix}
\begin{bmatrix}
V_{Y_1} \\
V_{Y_2} \\
I_X \\
V_{Z+} \\
V_{Z-}
\end{bmatrix},
\] (4)

where \( \beta_{k1}(s) \), \( \beta_{k2}(s) \) represent the frequency transfers of the internal voltage followers and \( \alpha_{k1}(s) \), \( \alpha_{k2}(s) \) represent the frequency transfers of the internal current followers of the \( k \)-th DVCC, respectively. They can be approximated by first-order low-pass functions, which can be considered to have a unity value for frequencies [30]. If this circuit is working at frequencies much less than the corner frequencies of \( \beta_{k1}(s) \), \( \beta_{k2}(s) \), \( \alpha_{k1}(s) \), and, \( \alpha_{k2}(s) \), namely, then \( \beta_{k1}(s) = \beta_{k1} = 1 - \varepsilon_{kv1} \) and \( \varepsilon_{kv1}(|\varepsilon_{kv1}| \ll 1) \) denotes the voltage tracking error from the \( Y_1 \) terminal to the \( X \) terminal of the \( k \)-th DVCC; \( \beta_{k2}(s) = \beta_{k2} = 1 - \varepsilon_{kv2} \) and \( \varepsilon_{kv2}(|\varepsilon_{kv2}| \ll 1) \) denotes the voltage tracking error from the \( Y_2 \) terminal to the \( X \) terminal of the
Taking the tracking errors of the nonideal DVCC into account, the denominator of (2) becomes

\[
D(s) = z C_1 C_2 R_2 \alpha_{22} (R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}) + s C_1 R_3 \beta_{22} \beta_{31} \alpha_{11} \alpha_{22} + \beta_{12} \beta_{22} \beta_{32}.
\]

The parameters \( \omega_0 \) and \( Q \) from (5) can be rewritten as

\[
\omega_0 = \left( \frac{\beta_{12} \beta_{22} \beta_{32}}{C_1 C_2 R_2 \alpha_{22} (R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21})} \right)^{1/2},
\]

\[
Q = \frac{1}{R_3 \beta_{31} \alpha_{11}} \left( \frac{C_2 R_3 \beta_{12} \beta_{32} (R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21})}{C_1 \beta_{22} \alpha_{22}} \right)^{1/2}.
\]

The active and passive sensitivities of the proposed SIMO voltage-mode filter are derived from (6). These are as follows:

\[
S_{\omega_0}^{A_{C_1, C_2, R_2}} = -\frac{1}{2},
\]

\[
S_{R_1}^{A_{C_1, C_2, R_2}} = -\frac{1}{2} \left( \frac{R_1 \beta_{12}}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}} \right),
\]

\[
S_{\beta_{22}}^{A_{C_1, C_2, R_2}} = -\frac{1}{2},
\]

\[
S_{\beta_{31}}^{A_{C_1, C_2, R_2}} = \frac{1}{2} \left( \frac{R_3 \alpha_{11} - R_3 \alpha_{11} \beta_{21}}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}} \right),
\]

\[
S_{R_3 \alpha_{11}}^{A_{C_1, C_2, R_2}} = -\frac{1}{2} \left( \frac{R_3 \alpha_{11} (1 - \beta_{21})}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}} \right),
\]

\[
S_{\beta_{12}}^{A_{C_1, C_2, R_2}} = -\beta_{22} \left( \frac{R_3 \alpha_{11} (1 - \beta_{21}) + R_1 \beta_{12}}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}} \right),
\]

\[
S_{\beta_{32}}^{A_{C_1, C_2, R_2}} = -1,
\]

\[
S_{R_1 \beta_{12}}^{A_{C_1, C_2, R_2}} = \frac{1}{2} \frac{R_3 \alpha_{11} + 2 R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}},
\]

\[
S_{R_2 \beta_{32}}^{A_{C_1, C_2, R_2}} = -\frac{1}{2} \left( \frac{R_3 \alpha_{11} (1 - \beta_{21}) + R_1 \beta_{12}}{R_3 \alpha_{11} + R_1 \beta_{12} - R_3 \alpha_{11} \beta_{21}} \right).
\]

From the results it is evident that the sensitivities are low and within unity in absolute value. Thus the proposed circuit can be classified as insensitive as all the active and passive sensitivities are less than or equal to unity [28].

5. Simulation Results

The performance of the proposed voltage-mode universal biquadratic filter in Figure 2 is verified using the PSPICE simulation program. The MOS transistors are simulated using 0.35 μm CMOS process parameters given in Table 1. The aspect ratios of the transistors used in the simulation are given in Table 2. The supply voltages and biasing voltage are given by \( V^+ = 2 V \), \( V^- = -1.9 V \), \( V_{b1} = -1.23 V \), and \( V_{b2} = -1.15 V \), respectively. The proposed circuit is designed for 3.18 MHz and \( Q = 1 \) by choosing \( R_1 = R_2 = R_3 = 5 k\Omega \) and \( C_1 = C_2 = 10 pF \). Figure 3 shows the simulated gain responses for the LP, HP, and BP filters and agrees well with the designed values. Figures 4 and 5 show the simulated gain and phase responses for the NH and AP filters. Next, Q tuning of BP filter at constant frequency of 3.18 MHz is shown in Figure 6. The circuit of Figure 2 is designed for \( Q \) values of 1, 5, and 10 with \( R_1 \) values as 5 kΩ, 1 kΩ, and 500 Ω, respectively, with \( C_1 = C_2 = 10 pF \) and \( R_1 = R_2 = 5 k\Omega \).
To test the input dynamic range of the proposed filter, the simulation of the HP filter as example has been repeated for a sinusoidal input signal at 3.18 MHz. Figure 7 shows the input dynamic range of the HP filter which extends up to amplitude 200 mV (peak to peak) without significant distortion. In Figure 8, the total harmonic distortion (THD) of the $V_{OUT2}$ output voltage is given at 3.18 MHz operation frequency. Figure 9 shows the INOISE and ONoise simulated amplitude-frequency responses of the proposed LP filter at $V_{OUT1}$.

6. Conclusion

The voltage-mode universal biquadratic filter realizes all the standard filter functions by employing three DVCCs and five passive components. It offers high-input impedance, which permit the use of circuits in cascade without requiring any impedance-matching device. Moreover, the proposed circuit enjoys the following features: orthogonal control of $\omega_0$ and $Q$, low active and passive sensitivity, and the direct incorporation of parasitic resistance and parasitic capacitance at the X-terminal and Z-terminal of the respective DVCCs, as a part of main resistor and capacitor. So, the new circuit will enhance the existing knowledge on the voltage-mode universal biquadratic filter. PSPICE simulations with TSMC 0.35 μm process confirm the theoretical prediction.

References


