Research Article

New Power Sharing Control for Inverter-Dominated Microgrid Based on Impedance Match Concept

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Power flow control is one of the most important issues for operating the inverter-dominated autonomous microgrid. A technical challenge is how to achieve the accurate active/reactive power sharing of inverters. P-F and Q-V droop control schemes have been widely used for power sharing in the past decades. But they suffer from the poor power sharing in the presence of unequal line impedance. In order to solve the problem, a comprehensive analysis of the power droop control is presented, and a new droop control based on the impedance match concept is proposed in this paper. In addition, the design guidelines of control coefficients and virtual impedance are provided. Finally, the performance evaluation is carried out, and the evaluation results verify the effectiveness of the proposed method.

1. Introduction

The environmental concerns and electric utility deregulation promote the development of distributed generation (DG) in a rapid pace. The high penetration of DG brings about a concept of the microgrid. It is defined as a cluster of DG units (such as wind turbines and photovoltaics), storage devices, and loads, which can operate in the grid-connected mode or autonomous mode [1], and this paper focuses on the latter.

For inverter-based autonomous microgrid, the droop control is widely used to regulate the power flow according to the local information with no need of communication [2–6]. Generally speaking, an ideal droop control should provide the fast and accurate power sharing without affecting the voltage and frequency at the point of common coupling (PCC). In practice, however, the widely used droop control, which stems from the original philosophy of the synchronous generator control in power systems, fails to meet the above-mentioned requirements. In the past decades, many attempts have been made to improve the performance of the conventional droop control. A significant contribution from Josep Guerrero is the virtual impedance concept, which demonstrates that the inverter output impedance depends on not only the system parameters but also the inverter control scheme [7]. After that, the virtual impedance based droop control has gained more and more attention. For the accurate power sharing, the output impedance should be fixed as inductive, resistive, or complex impedances. In [8], a virtual inductance is designed for the inductive output impedance even with high $R/X$ ratio. On the other hand, the resistive output impedance is used [2], which ensures that the system is more damped and achieves better power sharing. In [9], the virtual complex impedance is designed to minimize the circulating current for the efficient power sharing.

On the other hand, an improved droop method has been reported in [10], where the basic idea is to transform the actual power to the virtual power according to $R/X$ ratio of the line impedance, and then the conventional droop control can be used. Another interesting solution reported in [11] is the virtual frequency and voltage frame droop control. Different from the former control, it can directly control the actual real and reactive power, but the frame transformation angle for each inverter should be the same (e.g., 45°). Note that two aforementioned methods can get better performance from the viewpoint of power decoupling. However, power sharing is still a question, especially in the presence of unequal line impedance.
In this paper, a comprehensive analysis is provided to clarify why the conventional droop control can achieve the accurate active power sharing but fail to get better reactive power sharing. And then a simple and accurate power sharing scheme is presented to solve the problem. Finally, the performance evaluation is carried out to verify the feasibility of the proposed method.

2. Analysis and Design of Droop Control

Figure 1 illustrates the schematic diagram of inverter-based microgrid [11]. It is comprised of the energy sources with optional energy storages and dc/ac inverters. The inverters can provide an interface for the flexible functions such as power flow control and power quality improvement. The inverter output may either feed the local loads independently in autonomous mode or in conjunction with the electric utility by static switch (STS) in grid-connected mode. This paper will focus on the former mode.

2.1. Power Droop Theory. The power droop control has a long history of use for the synchronous generator control in power system. Recently, it has been used for parallel-inverter control, especially in inverter-dominated microgrid [12]. The following will provide a brief review of the conventional power droop scheme.

The inverter output active and reactive power can be expressed according to Figure 1 as follows:

\[ P_i = \frac{R_i E_i^2 - R_i E_i V \cos \phi_i + X_i E_i V \sin \phi_i}{R_i^2 + X_i^2}, \]
\[ Q_i = \frac{X_i E_i^2 - X_i E_i V \cos \phi_i - R_i E_i V \sin \phi_i}{R_i^2 + X_i^2}, \] (1)

where \( E_i \) and \( V \) are the amplitudes of the \( i \)th inverter output voltage, and PCC voltage respectively. \( \phi_i \) is the power angle. \( R_i \) and \( X_i \) are the resistance and inductance of the line impedance.

In order to clarify the basic principle of the power droop control, the sensitivity analysis is carried out as follows:

\[ \frac{\partial P_i}{\partial \phi_i} = \frac{R_i E_i V \sin \phi_i + X_i E_i V \cos \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial P_i}{\partial E_i} = \frac{2R_i E_i - R_i V \cos \phi_i + X_i V \sin \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial Q_i}{\partial \phi_i} = \frac{X_i E_i V \sin \phi_i - R_i E_i V \cos \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial Q_i}{\partial E_i} = \frac{2X_i E_i - X_i V \cos \phi_i - R_i V \sin \phi_i}{R_i^2 + X_i^2}. \] (2)

Note that the power angle \( \phi_i \) is relatively small in practice; that is, \( \sin \phi_i \approx 0 \) and \( \cos \phi_i \approx 1 \). Equation (2) can be simplified as

\[ \frac{\partial P_i}{\partial \phi_i} = \frac{R_i E_i V \sin \phi_i + X_i E_i V \cos \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial P_i}{\partial E_i} = \frac{2R_i E_i - R_i V \cos \phi_i + X_i V \sin \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial Q_i}{\partial \phi_i} = \frac{X_i E_i V \sin \phi_i - R_i E_i V \cos \phi_i}{R_i^2 + X_i^2}, \]
\[ \frac{\partial Q_i}{\partial E_i} = \frac{2X_i E_i - X_i V \cos \phi_i - R_i V \sin \phi_i}{R_i^2 + X_i^2}. \] (3)

For power system applications, the line impedance is mainly inductive; that is, \( R_i \approx 0 \). So (3) can be rewritten as follows:

\[ \frac{\partial P_i}{\partial \phi_i} = \frac{E_i V}{X_i}, \quad \frac{\partial P_i}{\partial E_i} = \frac{V \phi_i}{X_i}, \]
\[ \frac{\partial Q_i}{\partial \phi_i} = \frac{E_i V \phi_i}{X_i}, \quad \frac{\partial Q_i}{\partial E_i} = \frac{2E_i - V}{X_i}. \] (4)

Considering that the power angle \( \phi_i \) is much smaller than PCC voltage \( V \), \( (\partial P_i)/(\partial \phi_i) \gg (\partial P_i)/(\partial E_i) \), and \( (\partial Q_i)/(\partial \phi_i) \gg (\partial Q_i)/(\partial E_i) \). In other words, the active power \( P_i \) is more dependent on the power angle (frequency) variation, while the reactive power \( Q_i \) is more sensitive to the output voltage magnitude variation. That is why \( P \)-\( F \) and \( Q \)-\( V \) droop control schemes are widely used in power systems:

\[ \omega_i = \omega_i^* + m_i (P_i^* - P_i), \]
\[ E_i = E_i^* + n_i (Q_i^* - Q_i). \] (5)

2.2. Proposed Droop Theory. The conventional droop control works well under the assumption that the line impedance is mainly inductive. However, it is not always the case, especially in low voltage microgrid, where the \( R/X \) ratio of the transmission line is relatively high. In this case, the conventional power droop control might suffer from the poor power decoupling and power sharing. Therefore, many interesting solutions have been presented for alleviating the abovementioned problems at the expense of complicated algorithms.

On the other hand, let us recall the conventional droop control to find out the inherent reason for the poor power
For three-phase balanced ac system, the steady-state power is constant; that is, \( s = j \omega = 0 \). Therefore, (7) can be simplified as

\[
\begin{align*}
P_i &= P_i^* + \frac{1}{m_i} \omega_i^* , \\
Q_i &= \frac{n_i}{(R_i^2 + X_i^2)} (Q_i^*) + \frac{n_i}{(R_i^2 + X_i^2)} (Q_i^*) + \frac{1}{m_i} \omega_i^* ,
\end{align*}
\]

Equation (8) indicates that the active power of \( i \)th inverter is dependent on the power reference \( P_i^* \), angular frequency reference \( \omega_i^* \), and droop coefficient \( m_i \). An accurate active power sharing can be easily achieved on the condition that the parameters of \( P_i^* \), \( \omega_i^* \), and \( m_i \) are equally set, respectively.

For example, the accurate sharing of active power among two inverters can be achieved on the condition that \( P_1^* = P_2^* \), \( \omega_1^* = \omega_2^* \), and \( m_1 = m_2 \), which can be well defined in practice.

It can also be easily understood from (5) that \( P_1 = P_2 \) is valid under the aforementioned three conditions along with \( \omega_1 = \omega_2 \), which is always true because the frequency remains the same at any node along the transmission line.

On the other hand, (9) implies that the reactive power of \( i \)th inverter is dependent on not only the well-defined parameters of \( Q_i^* \), \( E_i^* \), and \( n_i \) but also the PCC voltage \( V \) and the line impedance \( R_i + jX_i \). That is why the reactive power sharing is difficult to achieve in practice, which is mainly due to unequal line impedances.

In order to solve the above-mentioned problem, a new droop control based on impedance match concept is proposed in this paper. As stated earlier, the overall impedance in the droop control of inverters depends on not only the line impedance \( Z_{l \omega} \theta_{o_s} \) but also the inverter output impedance \( Z_{l \omega} \theta_{o} \), as illustrated in Figure 2.

Note that the inaccuracy of reactive power sharing is mainly caused by the unequal impedance. Therefore, the inverter output impedance should be carefully designed to ensure that the overall impedance of each inverter is equal; that is to say, \( Z_1 \omega \theta_{o_1} + Z_2 \omega \theta_{o_2} = Z_2 \omega \theta_{o_2} + Z_2 \omega \theta_{o_2} \). In this way, the reactive power will be accurately shared. It can also be easily understood from (9) and Figure 2. With overall impedance, \( Q^* \), \( E^* \) and \( n \) of each inverter equal, \( Q_1 = Q_2 \) will be valid.
3. System Loop Control Design

In this section, the design guideline of the system loop control is presented to achieve the high performance in both steady state and transient state. Figure 3 illustrates the block diagram of the system loop control, which consists of inner current/voltage control loop and external power droop control loop.

3.1. Current Loop Design. The block diagram of the current control loop is depicted in Figure 4, where $L$ is filter inductor and $K_{PWM}$ is the PWM gain.

The closed-loop transfer function of the current control loop can be derived from Figure 4 as follows:

$$T_i(s) = \frac{I_{\alpha\beta}(s)}{I^{*}_{\alpha\beta}(s)} = \frac{k_{ip} K_{PWM}}{Ls + k_{ip} K_{PWM}}.$$  \hspace{1cm} (10)

In general, the bandwidth of the current control loop should be high enough to achieve the fast dynamic response \cite{13, 14}. So the bandwidth (4 kHz, as shown in Figure 5) is selected by one-fifth of the switching frequency (20 kHz), which means $k_{pi} = 0.6$.

3.2. Voltage Loop Design. Figure 5 shows the block diagram of the voltage control loop, where $T_v(s)$ is the transfer function of the current control loop and $C$ is the filter capacitor.

The closed-loop transfer function of the voltage control loop can be derived from Figure 6 as follows:

$$T_v(s) = \frac{U_{\alpha\beta}(s)}{I^{*}_{\alpha\beta}(s)} = \frac{k_{ip} K_{PWM}}{LCs^4 + k_{ip} CK_{PWM} s^3 + D_2 s^2 + D_1 s + D_0}.$$  \hspace{1cm} (11)
where
\[
D_2 = k_p k_i p_{PWM} + L C \omega_0^2,
\]
\[
D_1 = k_i p_{PWM} \left( C \omega_0^2 + k_i \right),
\]
\[
D_0 = k_p k_i p_{PWM} \omega_0^2.
\]

The bandwidth of the voltage control loop should be carefully designed to avoid the dynamic interaction with current control loop. In this paper, the bandwidth (800 Hz, as shown in Figure 7) is selected by one-fifth of the current control loop (4 kHz), which means \( k_p \approx 0.05 \) and \( k_i \approx 3.2 \).

### 3.3. Output Impedance Design

As stated earlier, the output impedance has a potential impact on the accuracy of reactive power sharing. Therefore, this section will provide the analysis and design of the output impedance.

The overall block diagram of the voltage/current control loop is illustrated in Figure 8, where \( R \) is the equivalent series resistor of the inductor. \( Z_v(s) \) represents the virtual impedance.

The overall output impedance can be derived from Figure 8 as
\[
Z_o(s) = -\frac{U_{a \beta}(s)}{I_{a \beta}(s)} = -\left( s^2 + \omega_0^2 \right) (Ls + R) + Z_v(s) k_i p_{PWM} D(s)
\]
\[
\times \left[ \left( L C s^2 + (k_i p_{PWM} + R) C s + 1 \right) \left( s^2 + \omega_0^2 \right) + k_i p_{PWM} D(s) \right]^{-1},
\]
where \( D(s) = k_p s^2 + k_i s + k_p^2 \omega_0^2. \)

When the virtual impedance is disabled \((Z_v = 0)\), the output impedance can be simplified as follows:
\[
Z_o(s) = -\left( s^2 + \omega_0^2 \right) (Ls + R)
\]
\[
\times \left[ \left( L C s^2 + (k_i p_{PWM} + R) C s + 1 \right) \left( s^2 + \omega_0^2 \right) + k_i p_{PWM} D(s) \right]^{-1}.
\]

From (14), it can be concluded that the output impedance at the fundamental frequency is zero, as shown in Figure 9. And it is in contrast with [7], where the PI control coefficients and filter parameters have a significant effect on the output impedance.

On the other hand, when the virtual impedance is enabled \((Z_v \neq 0)\), the overall output impedance at the fundamental frequency can be derived from (13) as follows:
\[
Z_o(s) = -Z_v(s).
\]

The virtual impedance should be designed to be inductive or resistive according to the impedance match concept, as discussed in the previous section.

### 4. Performance Evaluation

In order to verify the effectiveness of the proposed control scheme, the time-domain simulations in MATLAB/Simulink are carried out according to Figure 1. The system parameters are given as follows. The dc and ac bus voltages are 250 V and 120 V/50 Hz, respectively. The LC filter parameters are 3 mH and 9.9 uF. The switching frequency is 20 kHz. The control parameters are given in Section 3. The line impedances are set to be unequal \((Z_1 = 0.5 + j0.314 \text{ and } Z_2 = 1 + j0.628)\).
Figure 9: Bode diagram of output impedance ($Z_v = 0$).

Figure 10: Simulation results of power sharing with conventional and proposed control. (a) Reactive power. (b) Active power.

Figure 11: Simulation results of current sharing with conventional and proposed control. (a) Inverter_1. (b) Inverter_2.

5. Conclusion

This paper has presented a comprehensive analysis and design of power sharing control for the inverter-dominated microgrid. From the theoretical analysis and simulation results, several conclusions can be reached as follow.

1. Conventional droop control can achieve the accurate active power sharing, even in the presence of high $R/X$ ratio and unequal line impedance.

2. Conventional droop control fails to get better reactive power sharing, especially in case of unequal line impedance.

3. Reactive power sharing mainly depends on not only the line impedance but also the output impedance of each inverter.

4. Accurate reactive power sharing can be achieved on the condition that the overall impedance is matched for each inverter in microgrid, even in the presence of high $R/X$ ratio and unequal line impedance.
Disclosure

The paper is the authors' original work.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

Authors' Contribution

Each author had participated sufficiently in the submission and will take public responsibility for the contents. Publication is approved by all authors.

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