

Review Article

Harmonic Mitigation Techniques Applied to Power Distribution Networks

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A growing number of harmonic mitigation techniques are now available including active and passive methods, and the selection of the best-suited technique for a particular case can be a complicated decision-making process. The performance of some of these techniques is largely dependent on system conditions, while others require extensive system analysis to prevent resonance problems and capacitor failure. A classification of the various available harmonic mitigation techniques is presented in this paper aimed at presenting a review of harmonic mitigation methods to researchers, designers, and engineers dealing with power distribution systems.

1. Introduction

The nonlinear characteristics of many industrial and commercial loads such as power converters, fluorescent lamps, computers, light dimmers, and variable speed motor drives (VSDs) used in conjunction with industrial pumps, fans, and compressors and also in air-conditioning equipment have made the harmonic distortion a common occurrence in electrical power networks. Harmonic currents injected by some of these loads are usually too small to cause a significant distortion in distribution networks. However, when operating in large numbers, the cumulative effect has the capability of causing serious harmonic distortion levels. These do not usually upset the end-user electronic equipment as much as they overload neutral conductors and transformers and, in general, cause additional losses and reduced power factor [1–5]. Large industrial converters and variable speed drives on the other hand are capable of generating significant levels of distortion at the point of common coupling (PCC), where other users are connected to the network [6, 7].

Because of the strict requirement of power quality at the input AC mains, various harmonic standards and engineering recommendations such as IEC 1000-3-2, IEEE 519 (USA), AS 2279, D.A.CH.CZ, EN 61000-3-2/EN 61000-3-12, and ER G5/4 (UK) are employed to limit the level of distortion at the PCC. To comply with these harmonic standards, installations utilizing power electronic and nonlinear loads often use one

of the growing numbers of harmonic mitigation techniques [8]. Because of the number and variety of available methods, the selection of the best-suited technique for a particular application is not always an easy or straightforward process. Many options are available, including active and passive methods. Some of the most technically advanced solutions offer guaranteed results and have little or no adverse effect on the isolated power system, while the performance of other simple methods may be largely dependent on system conditions. This paper presents a comprehensive survey on harmonic mitigation techniques in which a large number of technical publications have been reviewed and used to classify harmonic mitigation techniques into three categories: passive techniques, active techniques, and hybrid harmonic reduction techniques using a combination of active and passive methods. A brief description of the electrical characteristics of each method is presented with the aim of providing the designer and site engineer with a more informed choice regarding their available options when dealing with the effects and consequences of the presence of these harmonics in the distribution network.

2. Passive Harmonic Mitigation Techniques

Many passive techniques are available to reduce the level of harmonic pollution in an electrical network, including the

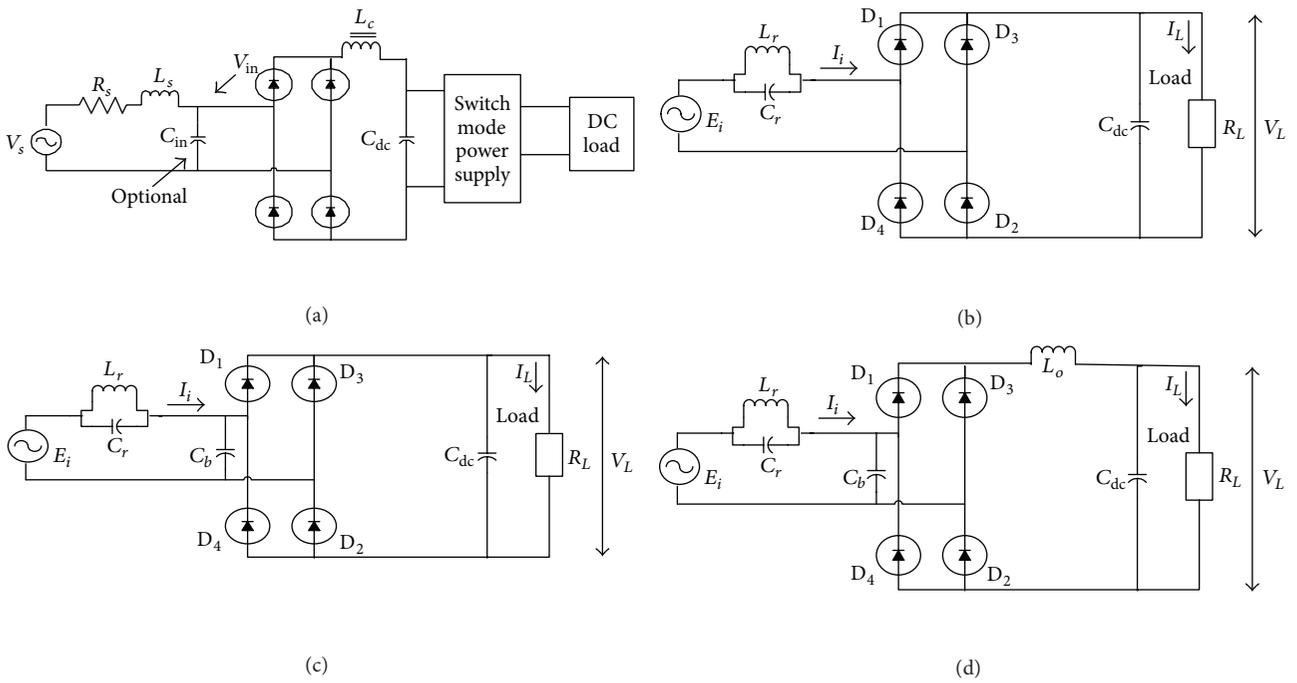


FIGURE 1: (a) The Series inductor filters for current shaping, (b) The Ziogas inductor capacitor filter, (c) The Yanchao improvement on Ziogas filter, and (d) The Hussein improvement on Yanchao filter.

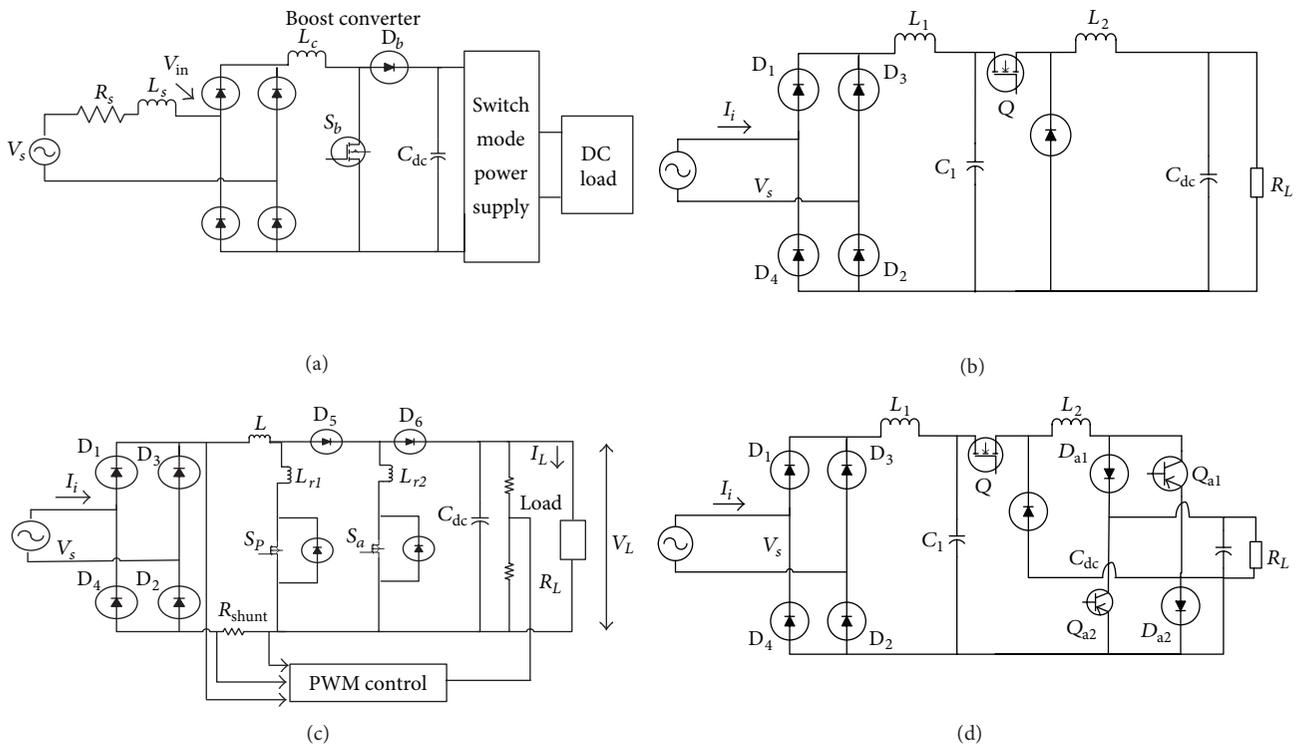


FIGURE 2: (a) Boost converter current shaping circuit, (b) buck converter current shaping circuit, (c) improve boost converter current shaping circuit, and (d) improve buck converter current shaping circuit.

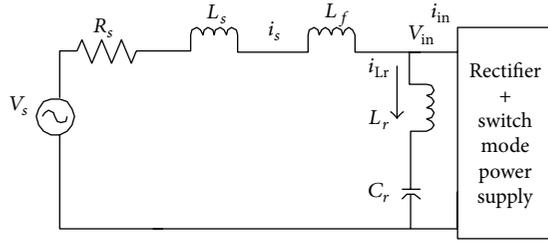


FIGURE 3: A parallel-connected resonant filter.

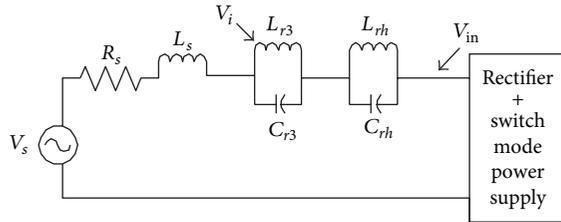


FIGURE 4: Double-tuned series-connected resonant filter.

connection of series line reactors, tuned harmonic filters, and the use of higher pulse number converter circuits such as 12-pulse, 18-pulse, and 24-pulse rectifiers. In these methods, the undesirable harmonic currents may be prevented from flowing into the system by either installing a high series impedance to block their flow or diverting the flow of harmonic currents by means of a low-impedance parallel path [9].

Harmonic mitigation techniques used for supply power factor correction and harmonics mitigation in two ways to qualify the products performance. One is to put a limit on the PF for loads above a specified minimum power. Utility companies often place limits on acceptable power factors for loads (e.g., <0.8 leading and >0.75 lagging). A second way to measure or specify a product is to define absolute maximum limits for current harmonic distortion. This is usually expressed as limits for odd harmonics (e.g., 1st, 3rd, 5th, 7th, etc.). This approach does not need any qualifying minimum percentage load and is more relevant to the electric utility.

Harmonic regulations or guidelines are currently applied to keep current and voltage harmonic levels in check. As an example, the current distortion limits in Japan illustrated in Tables 1 and 2 represent the maximum and minimum values of total harmonic distortion (THD) in voltage and the most dominant fifth harmonic voltage in a typical power system [10].

Certain techniques, such as the use of tuned filters, require extensive system analysis to prevent resonance problems and capacitor failures, while others, such as the use of 12-pulse or 24-pulse converters, can be applied with virtually no system analysis.

2.1. Effect of Source Reactance. Typical AC current waveforms in single-phase and three-phase rectifiers are far from a

TABLE 1: Voltage THD and fifth harmonic voltage in a high-voltage power transmission system.

	Over 154 kV		154–22 kV	
	THD	5th harmonic	THD	5th harmonic
Max.	2.8%	2.8%	3.3%	3.2%
Min.	1.1%	1.0%	1.4%	1.3%

TABLE 2: Voltage THD and fifth harmonic voltage in a 6.6 kV power distribution system.

	6.6 kV			
	Residential		Commercial	
	THD	5th harmonic	THD	5th harmonic
Max.	3.5%	3.4%	4.6%	4.3%
Min.	3.0%	2.9%	2.1%	1.2%

sinusoid. The power factor is also very poor because of the high harmonic contents of the line current waveform. In rectifier with a small source reactance, the input current is highly discontinuous, and, as a consequence, the power is drawn from the utility source at a very poor power factor.

The magnitude of harmonic currents in some nonlinear loads depends greatly on the total effective input reactance, comprised of the source reactance plus any added line reactance. For example, given a 6-pulse diode rectifier feeding a DC bus capacitor and operating with discontinuous DC current, the level of the resultant input current harmonic spectrum is largely dependent on the value of AC source reactance and an added series line reactance; the lower the reactance, the higher the harmonic content [1–3].

Other nonlinear loads, such as a 6-pulse diode rectifier feeding a highly inductive DC load and operating with continuous DC current, act as harmonic current sources. In such cases, the amount of voltage distortion at the PCC is dependant on the total supply impedance, including the effects of any power factor correction capacitors, with higher impedances producing higher distortion levels [7, 11].

2.2. Series Line Reactors. The use of series AC line reactors is a common and economical means of increasing the source impedance relative to an individual load, for example, the input rectifier used as part of a motor drive system. The harmonic mitigation performance of series reactors is a function of the load; however, their effective impedance reduces proportionality as the current through them is decreased [12].

2.3. Tuned Harmonic Filters. Passive harmonic filters (PHF) involve the series or parallel connection of a tuned LC and high-pass filter circuit to form a low-impedance path for a specific harmonic frequency. The filter is connected in parallel or series with the nonlinear load to divert the tuned frequency harmonic current away from the power supply. Unlike series line reactors, harmonic filters do not attenuate all harmonic frequencies but eliminate a single harmonic frequency from the supply current waveform. Eliminating harmonics at their source has been shown to be the most

effective method to reduce harmonic losses in the isolated power system. However, the increased first cost entailed presents a barrier to this approach. If the parallel-connected filter is connected further upstream in the power network, higher day-to-day costs will accumulate due to I^2R losses in the conductors and other plant items that carry the harmonic currents. Conversely, for series-connected filter at the load, there are increased losses in the filter itself. These losses are simply the result of the higher series impedance, which blocks the flow of harmonics but increases the line loss as a result of the flow of the remaining components of the load current [12, 13]. The quality factor of the filter inductor Q affects the actual value of the low-impedance path for each filter. Usually, a value of Q ranges between 20 and 100 [14]. Many types of harmonic filters are commonly employed, including the following:

2.3.1. Series Induction Filters. Harmonic currents produced by switched-mode power supplies and other DC-to-DC converter circuits can be significantly lowered by the connection of a series inductor that can be added on either the AC or DC power circuit [15–17], as shown in Figure 1. So many improvements on these filters have been made.

Ziogas passive filter for single-phase rectifiers has some reduction in Total Harmonics Distortion THD and improvement in PF in comparison with conventional rectifier. Also, Yanchoa waveshaping filter used to reduce THD and increase power factor. Connecting author filter at the output terminal of the rectifier will improve power factor and reduce input current THD of the supply.

2.3.2. DC-DC Converter Current Shaping. Like the series induction filter, this circuit (Figure 2) can greatly reduce current distortion produced by switched-mode power supplies and other DC converter circuits by modulating the duty cycle of switch S_b to control the shape of input supply current to track a desired sine waveshape [5, 18–20]. So many improvements on these filters have been made.

2.3.3. Parallel-Connected Resonant Filter. Passive LC filters tuned to eliminate a particular harmonic are often used to reduce the level of low-frequency harmonic components like the 5th and 7th produced by three-phase rectifier and inverter circuits. The filter is usually connected across the line as shown in Figure 3. If more than one harmonic is to be eliminated, then a shunt filter must be installed for each harmonic. Care must be taken to ensure that the peak impedances of such an arrangement are tuned to frequencies between the required harmonic frequencies to avoid causing high levels of voltage distortion at the supply's PCC because of the presence of an LC resonance circuit [7, 12].

2.3.4. Series-Connected Resonant Filter. This work on a similar in principle to the parallel version, but with the tuned LC circuits connected in series with the supply. The series filter can be tuned to a single harmonic frequency, or it may be multituned to a number of harmonic frequencies. The multituned arrangement connects multiple tuned filters

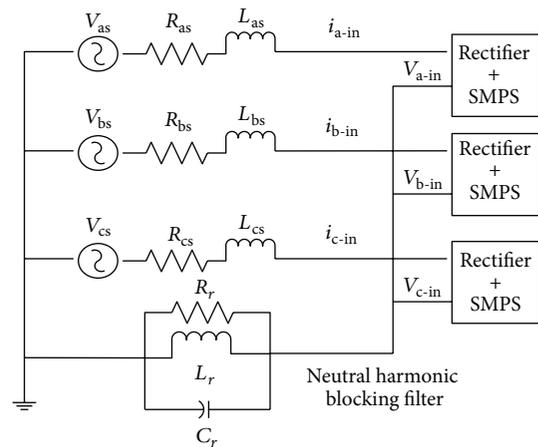


FIGURE 5: A neutral current blocking filters.

in series as shown in Figure 4 showing a third harmonic tuned LC circuit, L_{r3} and C_{r3} , and a high-frequency tuned LC circuit, L_{rh} and C_{rh} , to eliminate high-order harmonics [5, 7, 12].

2.3.5. Neutral Current Filter. This filter is connected in the neutral conductor between the site transformer and the three-phase load to block all triple frequency harmonics, as shown in Figure 5. Because these triple zero-sequence harmonics are in phase with each other, they all flow through the neutral conductor, and it is more economical to block them in the neutral instead of individual phases [5, 12].

2.3.6. Zigzag Grounding Filter. By integrating phase shifting into a single or multiphase transformer with an extremely low zero-sequence impedance, substantial reduction of triple, 5th, and 7th harmonics can be achieved. This method provides an alternative to protect the transformer neutral conductor from triple harmonics by canceling these harmonics near the load. In this method, an autotransformer connected in parallel with the supply can provide a zero-sequence current path to trap and cancel triple harmonics as shown in Figure 6 [16].

2.4. Higher Pulse Converters. Three phases, 6-pulse static power converters, such as those found in VSD, generate low-frequency current harmonics. Predominantly, these are the 5th, 7th, 11th, and 13th with other higher orders harmonics also present but at lower levels. With a 6-pulse converter circuit, harmonics of the order $6k \pm 1$, where $k = 1, 2, 3, 4$, and so forth, will be present in the supply current waveform. In high-power applications, AC-DC converters based on the concept of multipulse, namely, 12, 18, or 24 pulses, are used to reduce the harmonics in AC supply currents. They are referred to as a multipulse converters. They use either a diode bridge or thyristor bridge and a special arrangement of phase-shifting magnetic circuit such as transformers and inductors to produce the required supply current waveforms [9, 21–27].

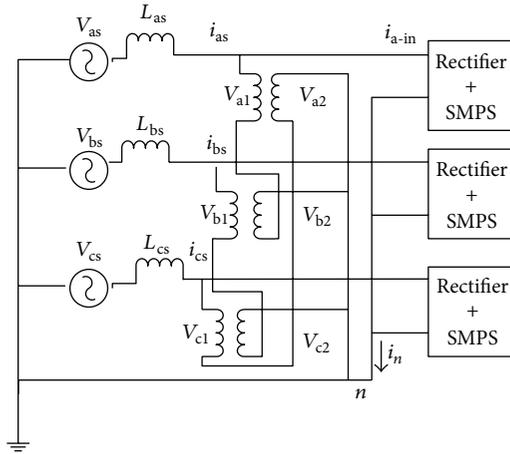


FIGURE 6: Zigzag autotransformer connected to three-phase nonlinear loads.

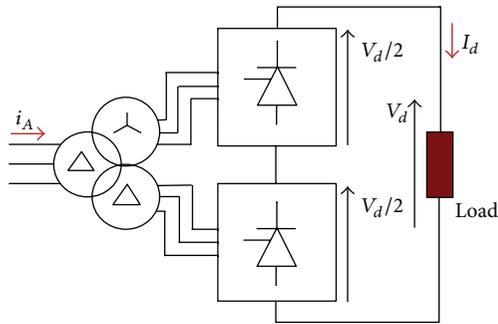


FIGURE 7: Series 12-pulse rectifier connection.

2.4.1. *12-Pulse Rectification.* In large converter installations, where harmonics generated by a three-phase converter can reach unacceptable levels, it is possible to connect two 6-pulse converters in series with star/delta phase-shifting transformers to generate a 12-pulse waveform and reduce the harmonics on the supply and load sides, as shown in Figure 7. This could be beneficial despite the considerable extra cost of the transformers. Twelve-pulse rectifier is frequently specified by consulting engineers for heating, ventilating, and air-conditioning applications because of their theoretical ability to reduce harmonic current distortion.

Instead of connecting the two converter bridges in series, they could also be connected in parallel to give 12-pulse operation. A parallel 12-pulse arrangement is shown in Figure 8. Parallel connections require special care to ensure adequate balance between the currents drawn by each bridge. Secondary leakage reactance must be carefully matched, and extra reactors are needed on the DC side to absorb the instantaneous differences between the two DC voltage waveforms, [9, 22, 28].

When using a 12-pulse system, the 5th and 7th harmonics disappear from line current waveforms leaving the 11th as the first to appear. Only harmonics of the order $12k \pm 1$, where $k = 1, 2, 3, 4$, and so forth, will be present in the supply current

waveform, thus resulting in a high power factor, low THD at input AC mains, and ripple-free DC output of high quality.

2.4.2. *18-Pulse Rectification.* Eighteen-pulse converter circuits, shown in Figure 9, use a transformer with three sets of secondary windings that are phase-shifted by 20 degrees with respect to each other. Only harmonics of the order $18k \pm 1$, where $k = 1, 2, 3, 4$, and so forth, will be present in the supply current waveform [9, 29].

2.4.3. *24-Pulse Rectification.* Connecting two 12-pulse circuits with a 15° phase shift produces a 24-pulse system. Figure 10 shows one such system in which the two 12-pulse circuits are connected in parallel to produce the required 24-pulse system. The 11th and 13th harmonics now disappear from the supply current waveform leaving the 23rd as the first to appear. Only harmonics of the order $24k \pm 1$, where $k = 1, 2, 3, 4$, and so forth, will be present in a 24-pulse system [9, 30].

3. Active Harmonic Mitigation Techniques

When using active harmonic reduction techniques, the improving in the power quality came from injecting equal-but-opposite current or voltage distortion into the network, thereby canceling the original distortion. Active harmonic filters (AHFs) utilize fast-switching insulated gate bipolar transistors (IGBTs) to produce an output current of the required shape such that when injected into the AC lines, it cancels the original load-generated harmonics. The heart of the AHF is the controller part. The control strategies applied to the AHF play a very important role on the improvement of the performance and stability of the filter. AHF is designed with two types of control scheme. The first performs fast Fourier transforms to calculate the amplitude and phase angle of each harmonic order. The power devices are directed to produce a current of equal amplitude but opposite phase angle for specific harmonic orders. The second method of control is often referred to as full spectrum cancellation in which the full current waveform is used by the controller of the filter, which removes the fundamental frequency component and directs the filter to inject the inverse of the remaining waveform [31–38].

Typically, these filters are sized based on how much harmonic current the filter can produce, normally in amperage increments of 50 Amps. The proper amperage of AHF can be chosen after determining the amount of harmonic cancellation current.

Essentially, the filter consists of a VSD with a special electronic controller which injects the harmonic current onto the system 180 out of phase to the system or drive harmonics. This results in harmonics cancellation. For example, if the VSD created 50 A of 5th harmonic current, and the AHF produced 40 A of 5th harmonic current, the amount of 5th harmonic current exported to the utility grid would be 10 A. The AHF may be classified as a single-phase or three-phase filters.

Also, it could be classified as parallel or series AHF according to the circuit configuration.

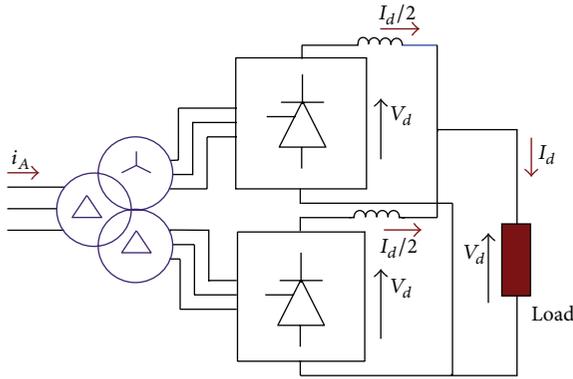


FIGURE 8: Parallel twelve-pulse rectifier connection.

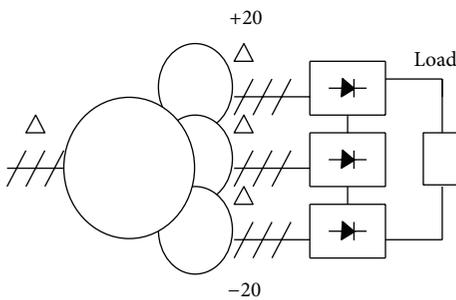


FIGURE 9: 18-pulse rectifier connection.

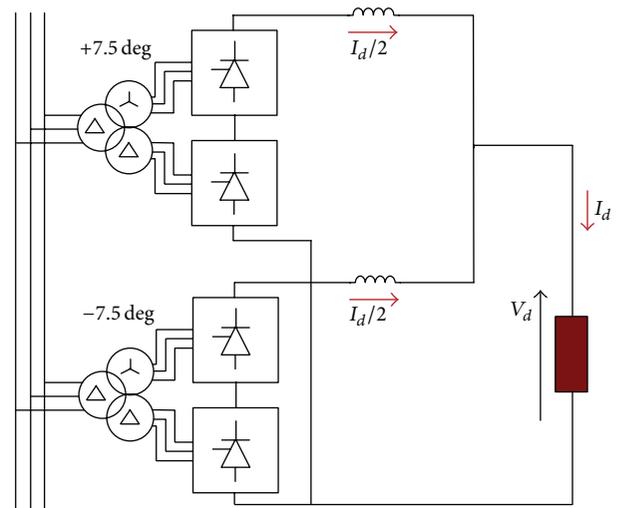


FIGURE 10: 24-pulse rectifier connection.

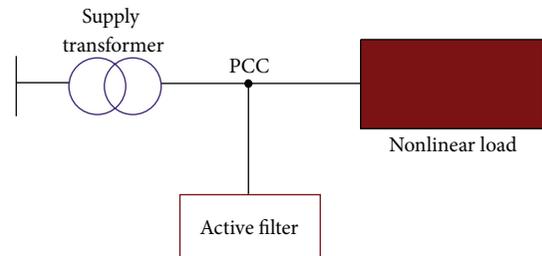


FIGURE 11: Parallel active filter.

3.1. Parallel Active Filters. This is the most widely used type of AHF (more preferable than series AHF in terms of form and function). As the name implies, it is connected in parallel to the main power circuit as shown in Figure 11. The filter is operated to cancel out the load harmonic currents leaving the supply current free from any harmonic distortion. Parallel filters have the advantage of carrying the load harmonic current components only and not the full load current of the circuit [39–44].

AHF can be controlled on the basis of the following methods:

- (i) the controller detects the instantaneous load current i_L ,
- (ii) the AHF extracts the harmonic current i_{Lh} from the detected load current i_L by means of digital signal processing,
- (iii) the AHF draws the compensating current i_{AF} ($= -i_{Lh}$) from the utility supply voltage v_s so as to cancel out the harmonic current i_{Lh} [45].

3.2. Series Active Filters. The main circuit configuration for this type of AHF is shown in Figure 12. The idea here is to eliminate voltage harmonic distortions and improve the quality of the voltage applied to the load. This is achieved by producing a sinusoidal pulse width modulated (PWM) voltage waveform across the connection transformer, which

is added to the supply voltage to counter the distortion across the supply impedance and present a sinusoidal voltage across the load. Series AHF has to carry the full load current increasing their current ratings and I^2R losses compared with parallel filters, especially across the secondary side of the coupling transformer [43].

Unlike the shunt AHF, the series AHF is controlled on the basis of the following methods:

- (i) the controller detects the instantaneous supply current i_s ,
- (ii) the AHF extracts the harmonic current i_s from the detected supply current by means of digital signal processing,
- (iii) the active filter applies the compensating voltage v_{AF} ($= -Ki_{Sh}$) across the primary of the transformer. This will result in a significant reduction in the supply harmonic current (i_{Sh}), when the feedback gain K is set to be high enough [45].

An AHF with both series and parallel (shunt) connected sections, as shown in Figures 11 and 12, respectively, can be used to compensate for both voltage and current harmonics simultaneously [34–36]. In all cases, the critical requirement of any AHF circuit is to calculate the required compensation current accurately and in real time.

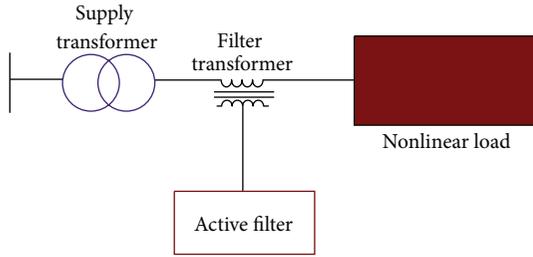


FIGURE 12: Series active filter.

4. Hybrid Harmonic Mitigation Techniques

Hybrid connections of AHF and PHF are also employed to reduce harmonics distortion levels in the network. The PHF with fixed compensation characteristics is ineffective to filter the current harmonics. AHF overcomes the drawbacks of the PHF by using the switching-mode power converter to perform the harmonic current elimination. However, the AHF construction cost in an industry is too high. The AHF power rating of power converter is very large. These bound the applications of AHF used in the power system. Hybrid harmonic filter (HHF) topologies have been developed [46–51] to solve the problems of reactive power and harmonic currents effectively. Using low cost PHF in the HHF, the power rating of active converter is reduced compared with that of AHF. HHF retains the advantages of AHF and does not have the drawbacks of PHF and AHF. Figure 13 shows a number of possible hybrid combinations. Figure 13(a) is a combination of shunt AHF and shunts PHF. Using a combination of PHF will make a significant reduction in the rating of the AHF. As a result, no harmonic resonance occurs, and no harmonic current flows in the supply. In [50], author claimed that in HHF the AHF can improve the filter performance and suppress the harmonic resonance of existing PHF. Figure 13(b) shows a combination of AHF series with the supply and a shunt PHE. The author of reference [46] found that this topology is not suitable for low-frequency interharmonic compensation because the AHF introduces a high compensation voltage which can interfere with downstream phase-controlled nonlinear loads.

Figure 13(c) shows an AHF in series with a shunt PHF. In all cases, it is required that the filters in a hybrid combination share compensation properly in the frequency domain [51]. A lot of improvements and researches have been made on the control strategies of hybrid harmonic filters.

The AHF and PHF are used to generate the equivalent voltage which is related to the mains harmonic current using different methods (i.e., impedance variation method) as shown in Figure 13(c). The mains harmonic current is suppressed by increasing the ratio of effective source impedance to the harmonic components. To achieve a constant DC bus voltage of the AHF, a PI voltage controller is employed. A hysteresis voltage comparator is employed to track the output voltage to perform the equivalent impedance of active converter [48, 49]. HHF is cost effective and becomes more practical in industry applications.

AHF controller mainly is divided into two parts, that is, reference current generation and PWM current controller. The PWM current controller is principally used for providing gating pulse to the AHF. In reference to current generation scheme, reference current is generated by using the distorted waveform. Many control schemes are there for reference current generation, such as p - q theory, deadbeat controller, neuro, adaptive control, wavelet control, fuzzy, delta-sigma modulation, sliding mode control, vector control, repetitive control, and SFX control for improving the steady state and dynamic performance of AHFs [52–59].

4.1. p - q Method. Instantaneous reactive power theory has been published in 1984. Based on this theory, the so-called “ p - q method” was applied successfully in the control of AHF. Zero-sequence component is neglected in this method, and because of that the p - q method is not accurate when the three-phase system is distorted or unbalanced.

4.2. d - q Method. Based on the park transformation, the d - q method came. The three-phase load current can be decomposed in positive-sequence, negative sequence and zero-sequence component. The current in the d - q frame i_d and i_q can be transformed from the positive sequence and negative sequence using a PLL (phase locked loop). The division of the AC and DC components can be obtained across a low-pass PHF. The reference current signal can be achieved by the AC component in d - q frame through a countertransformation.

4.3. Direct Testing and Calculating Method (DTC). Separation of the harmonic and reactive components from the load current is the aim of current reference generator. The main characteristic of this method is the direct derivation of the compensating component from the load current, without the use of any reference frame transformation. In fact, this method presents a low-frequency oscillation problem in the AHF DC bus voltage.

4.4. Synchronous Reference Frame Method (SRF). Real currents are transformed into a synchronous reference frame in this method. The reference frame is synchronized with the AC mains voltage and is rotating at the same frequency. In this method, the reference currents are derived directly from the real load currents without considering the source voltages, which represent the most important characteristics of this method. The generation of the reference signals is not affected by distortion or voltage unbalance, therefore increasing the compensation robustness and performance.

4.5. Current Hysteresis Control. The basic principle of this control method is that the switching signals are derived from the comparison of the current error signal with a fixed width hysteresis band. This current control technique exhibits some unsatisfactory features due to simple, extreme robustness, fast dynamic, good stability, and automatic current limited characteristics.

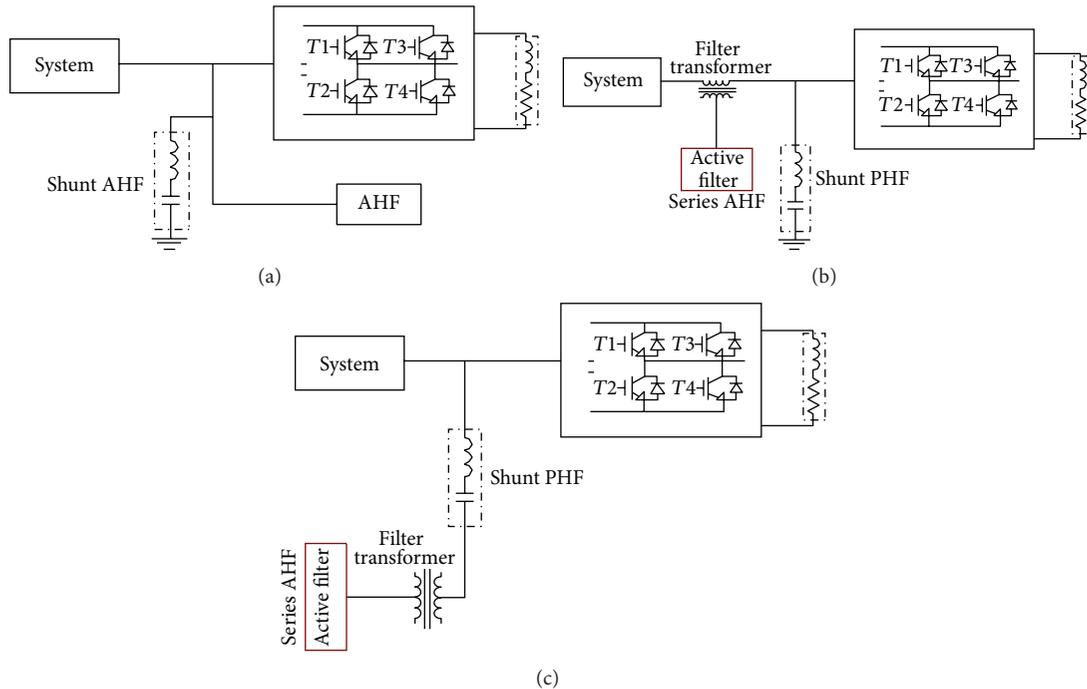


FIGURE 13: Hybrid connections of active and passive filters.

4.6. Triangle-Comparison PWM Control. This control method is also called linear current control. The conventional triangle-comparison PWM control principle is that the modulation signal achieved by a current regulator from the current error signal is intersected with the triangle wave. After that, pulse signals obtained are to control the switches of the converters. With analog PWM circuit, this control method has simple implementation with fast speed of response. Because the modulation frequency equals the triangle frequency, the current loop gain crossover frequency must be kept below the modulation frequency.

4.7. Space Vector Modulation (SVM). The aim of this method is to find the appropriate switching combinations and their duty ratios according to certain modulation scheme. The SVM operates in a complex plane divided in the six sectors separated by a combination of conducting or nonconducting switches in the power circuit. The reference vector is used to locate two adjacent switching-state vectors and compute the time for which each one is active. SVM is of low speed of response caused by the inherent calculation delay, due to the strong antijamming and the good reliability of digital control technique. In order to solve the drawback, the improvement of adopting deadbeat control and a certain oversize of the system reactive components is advised.

Currently, the research trends of the AHF control strategies are mainly towards the optimizing and practical application of the control strategies. At the end, the comparative criteria for PHF, AHF, and HHF could be summarized based on the following:

- (i) cost of the equipment and installation,
- (ii) harmonic indices (ex. i_h , THD_i , THD_v , TDD, and PWHD),
- (iii) life time and failure rate,
- (iv) maintenance and engineering.

5. Conclusions

Electrical system reliability and normal operation of electrical equipment rely heavily upon a clean distortion free power supply. Designers and engineers wishing to reduce the level of harmonic pollution on a power distribution network where nonlinear harmonic generating loads are connected have several harmonic mitigation techniques available. Because of the number and variety of available methods, selection of the best-suited technique for a particular application is not always an easy or straightforward process. A broad categorization of different harmonic mitigation techniques (passive, active, and hybrid) has been carried out to give a general viewpoint on this wide-ranging and rapidly developing topic. PHF is traditionally used to absorb harmonic currents because of low cost and simple robust structure. However, they provide fixed compensation and create system resonance. AHF provides multiple functions such as harmonic reduction, isolation, damping and termination, load balancing, PF correction, and voltage regulation. The HHF is more attractive in harmonic filtering than the pure filters from both viability and economical points of view, particularly for high-power applications. It is hoped that the discussion and classification of harmonic mitigation

techniques presented in this paper will provide some useful information to help make the selection of an appropriate harmonic reduction method for a given application on an easier task.

References

- [1] A. Mansoor, W. M. Grady, A. H. Chowdhury, and M. J. Samotyj, "An investigation of harmonics attenuation and diversity among distributed single-phase power electronic loads," *IEEE Transactions on Power Delivery*, vol. 10, no. 1, pp. 467–473, 1995.
- [2] A. Mansoor, W. M. Grady, R. S. Thallam, M. T. Doyle, S. D. Krein, and M. J. Samotyj, "Effect of supply voltage harmonics on the input current of single-phase diode bridge rectifier loads," *IEEE Transactions on Power Delivery*, vol. 10, no. 3, pp. 1416–1422, 1995.
- [3] G. Carpinelli, F. Iacovone, P. Varilone, and P. Verde, "Single phase voltage source converters: analytical modelling for harmonic analysis in continuous and discontinuous current conditions," *International Journal of Power and Energy Systems*, vol. 23, no. 1, pp. 37–48, 2003.
- [4] E. F. El-Saadany and M. M. A. Salama, "Reduction of the net harmonic current produced by single-phase non-linear loads due to attenuation and diversity effects," *International Journal of Electrical Power and Energy Systems*, vol. 20, no. 4, pp. 259–268, 1998.
- [5] T. Key and J. S. Lai, "Analysis of harmonic mitigation methods for building wiring systems," *IEEE Transactions on Power Systems*, vol. 13, no. 3, pp. 890–897, 1998.
- [6] M. H. Rashid and A. I. Maswood, "A novel method of harmonic assessment generated by three-phase AC-DC converters under unbalanced supply conditions," *IEEE Transactions on Industry Applications*, vol. 24, no. 4, pp. 590–597, 1988.
- [7] R. E. Owen, M. F. McGranhan, and J. R. Vivirito, "Distribution system harmonics: controls for large power converters," *IEEE Transactions on Power Apparatus and Systems*, vol. 101, no. 3, pp. 644–652, 1982.
- [8] T. Hoevenaars, K. LeDoux, and M. Colosino, "Interpreting IEEE STD 519 and meeting its harmonic limits in VFD applications," in *Proceedings of the 50th Annual Technical Conference of the Petroleum and Chemical Industry Committee*, pp. 145–150, Houston, Tex, USA, September 2003.
- [9] B. Singh, B. N. Singh, A. Chandra, K. Al-Haddad, A. Pandey, and D. P. Kothari, "A review of three-phase improved power quality AC-DC converters," *IEEE Transactions on Industrial Electronics*, vol. 51, no. 3, pp. 641–660, 2004.
- [10] "Investigation into execution of harmonic guidelines for household and office electric applications," Japanese IEE of Japan SC77A Domestic Committee Report, 2002.
- [11] R. L. Smith and R. P. Stratford, "Power system harmonics effects from adjustable-speed drives," *IEEE Transactions on Industry Applications*, vol. 20, no. 4, pp. 973–977, 1984.
- [12] J. C. Das, *Power System Analysis, Short-Circuit Load Flow and Harmonics*, Marcel Dekker, New York, NY, USA, 2002.
- [13] D. Alexa, A. Sirbu, and D. M. Dobrea, "An analysis of three-phase rectifiers with near-sinusoidal input currents," *IEEE Transactions on Industrial Electronics*, vol. 51, no. 4, pp. 884–891, 2004.
- [14] E. B. Makram, E. V. Subramaniam, A. A. Girgis, and R. Catoe, "Harmonic filter design using actual recorded data," *IEEE Transactions on Industry Applications*, vol. 29, no. 6, pp. 1176–1183, 1993.
- [15] T. S. Key and J. S. Lai, "Comparison of standards and power supply design options for limiting harmonic distortion in power systems," *IEEE Transactions on Industry Applications*, vol. 29, no. 4, pp. 688–695, 1993.
- [16] A. R. Prasad, P. D. Ziogas, and S. Manias, "A novel passive waveshaping method for single-phase diode rectifiers," *IEEE Transactions on Industrial Electronics*, vol. 37, no. 6, pp. 521–530, 1990.
- [17] J. Yanchao and F. Wang, "Single-phase diode rectifier with novel passive filter," *IEE Proceeding Circuits Devices Systems*, vol. 145, no. 4, pp. 254–259, 1998.
- [18] K. Hirachi, T. Iwade, and K. Shibayama, "Improvement of control strategy on a step-down type high power factor converter," *National Conversion Record of Industrial Electronic Engineering Japan*, pp. 4.70–4.71, 1995.
- [19] R. Ltoh, K. Lshizaka, H. Oishi, and H. Okada, "Single-phase buck rectifier employing voltage reversal circuit for sinusoidal input current waveshaping," *IEE Proceedings: Electric Power Applications*, vol. 146, no. 6, pp. 707–712, 1999.
- [20] C. A. Canesin and I. Barbi, "A novel single-phase ZCS-PWM high-power-factor boost rectifier," *IEEE Transactions on Power Electronics*, vol. 14, no. 4, pp. 629–635, 1999.
- [21] P. J. A. Ling and C. J. Eldridge, "Designing modern electrical systems with transformers that inherently reduce harmonic distortion in a PC-rich environment," in *Proceedings of the Power Quality Conference*, pp. 166–178, 1994.
- [22] J. C. Read, "The calculation of rectifier and inverter performance characteristics," *Journal of the Institute of Electrical Engineers*, vol. 92, no. 2, pp. 495–509, 1945.
- [23] E. J. Cham and T. R. Specht, "The ANSI 49 rectifier with phase shift," *IEEE Transactions on Industry Applications*, vol. 20, no. 3, pp. 615–624, 1984.
- [24] R. Hammond, L. Johnson, A. Shimp, and D. Harder, "Magnetic solutions to line current harmonic reduction," in *Proceedings of the Europe by International Power Conversion Conference (PCIM '94)*, pp. 354–364, San Diego, Calif, USA, 1994.
- [25] S. Kim, P. N. Enjeti, P. Packebush, and I. J. Pitel, "A new approach to improve power factor and reduce harmonics in a three-phase diode rectifier type utility interface," *IEEE Transactions on Industry Applications*, vol. 30, no. 6, pp. 1557–1564, 1994.
- [26] S. Choi, P. N. Enjeti, and I. J. Pitel, "Polyphase transformer arrangements with reduced kVA capacities for harmonic current reduction in rectifier-type utility interface," *IEEE Transactions on Power Electronics*, vol. 11, no. 5, pp. 680–690, 1996.
- [27] B. M. Bird, J. F. Marsh, and P. R. McLellan, "Harmonic reduction in multiplex converters by triple-frequency current injection," *Proceedings of the Institution of Electrical Engineers*, vol. 116, no. 10, pp. 1730–1734, 1969.
- [28] Y. S. Tzeny, "Harmonic analysis of parallel-connected 12-pulse uncontrolled rectifier without an interphase transformer," *IEE Proceeding Electrical Power Applications*, vol. 145, no. 3, pp. 253–260, 1998.
- [29] M. Hink Karl, "18 -Pulse Drives and Voltage Unbalance," <http://mtecorp.com/18pulse.html>.
- [30] T. H. Chen and M. Y. Huang, "Network modelling of 24-pulse rectifier transformers for rigorous simulation of rail transit power systems," *Electric Power Systems Research*, vol. 50, no. 1, pp. 23–33, 1999.
- [31] E. F. El-Saadany, R. Elshatshat, M. M. A. Salama, M. Kazerani, and A. Y. Chikhani, "Reactance one-port compensator and

- modular active filter for voltage and current harmonic reduction in nonlinear distribution systems: a comparative study," *Electric Power Systems Research*, vol. 52, no. 3, pp. 197–209, 1999.
- [32] J. R. Johnson, "Proper use of active harmonic filters to benefit pulp and paper mills," *IEEE Transactions on Industry Applications*, vol. 38, no. 3, pp. 719–725, 2002.
- [33] D. Li, Q. Chen, Z. Jia, and J. Ke, "A novel active power filter with fundamental magnetic flux compensation," *IEEE Transactions on Power Delivery*, vol. 19, no. 2, pp. 799–805, 2004.
- [34] W. M. Grady, M. J. Samotyi, and A. H. Noyola, "Survey of active line conditioning methodologies," *IEEE Transactions on Power Delivery*, vol. 5, no. 3, pp. 1536–1541, 1990.
- [35] M. Takeda, K. Ikeda, and Y. Tominaga, "Harmonic current compensation with active filter," in *Proceedings of the IEEE/IAS Annual Meeting*, pp. 808–815, 1987.
- [36] Z. Du, L. M. Tolbert, and J. N. Chiasson, "Active harmonic elimination for multilevel converters," *IEEE Transactions on Power Electronics*, vol. 21, no. 2, pp. 459–469, 2006.
- [37] S. Bhattacharya, T. M. Frank, D. M. Divan, and B. Banerjee, "Active filter system implementation," *IEEE Industry Applications Magazine*, vol. 4, no. 5, pp. 47–63, 1998.
- [38] I. Takahashi, S. G. Li, and Y. Omura, "Low price and high power active filter," in *Proceedings of the IEEE/IAS Annual Meeting*, pp. 95–98, 1991.
- [39] G. W. Chang and T. C. Shee, "A novel reference compensation current strategy for shunt active power filter control," *IEEE Transactions on Power Delivery*, vol. 19, no. 4, pp. 1751–1758, 2004.
- [40] H. Akagi, "Control strategy and site selection of a shunt active filter for damping of harmonic propagation in power distribution systems," *IEEE Transactions on Power Delivery*, vol. 12, no. 1, pp. 354–362, 1997.
- [41] A. Cavallini and G. C. Montanari, "Compensation strategies for shunt active-filter control," *IEEE Transactions on Power Electronics*, vol. 9, no. 6, pp. 587–593, 1994.
- [42] H. Akagi, H. Fujita, and K. Wada, "A shunt active filter based on voltage detection for harmonic termination of a radial power distribution line," *IEEE Transactions on Industry Applications*, vol. 35, no. 3, pp. 638–645, 1999.
- [43] H. Fujita and H. Akagi, "A practical approach to harmonic compensation in power systems—series connection of passive and active filters," *IEEE Transactions on Industry Applications*, vol. 27, no. 6, pp. 1020–1025, 1991.
- [44] F. Z. Peng, H. Akagi, and A. Nabae, "A new approach to harmonic compensation in power systems—a combined system of shunt passive and series active filters," *IEEE Transactions on Industry Applications*, vol. 26, no. 6, pp. 983–990, 1990.
- [45] H. Akagi, "Active harmonic filters," *Proceedings of the IEEE*, vol. 93, no. 12, pp. 2128–2141, 2005.
- [46] D. Basic, V. S. Ramsden, and P. K. Muttik, "Hybrid filter control system with adaptive filters for selective elimination of harmonics and interharmonics," *IEEE Proceedings: Electric Power Applications*, vol. 147, no. 4, pp. 295–303, 2000.
- [47] S. Senini and P. J. Wolfs, "Hybrid active filter for harmonically unbalanced three phase three wire railway traction loads," *IEEE Transactions on Power Electronics*, vol. 15, no. 4, pp. 702–710, 2000.
- [48] P. Salmerón, J. C. Montano, J. R. Vázquez, J. Prieto, and A. Pérez, "Compensation in nonsinusoidal, unbalanced three-phase four-wire systems with active power-line conditioner," *IEEE Transactions on Power Delivery*, vol. 19, no. 4, pp. 1968–1974, 2004.
- [49] H. Akagi, "New trends in active filters for power conditioning," *IEEE Transactions on Industry Applications*, vol. 32, no. 6, pp. 1312–1322, 1996.
- [50] H. L. Jou, J. C. Wu, and K. D. Wu, "Parallel operation of passive power filter and hybrid power filter for harmonic suppression," *IEEE Proceedings: Generation, Transmission and Distribution*, vol. 148, no. 1, pp. 8–14, 2001.
- [51] X. Zha and Y. Chen, "The iterative learning control strategy for hybrid active filter to dampen harmonic resonance in industrial power system," in *Proceedings of the IEEE International Symposium on Industrial Electronics*, vol. 2, pp. 848–853, 2003.
- [52] B. Singh, K. Al-Haddad, and A. Chandra, "A review of active filters for power quality improvement," *IEEE Transactions on Industrial Electronics*, vol. 46, no. 5, pp. 960–971, 1999.
- [53] B. R. Lin, B. R. Yang, and H. R. Tsai, "Analysis and operation of hybrid active filter for harmonic elimination," *Electric Power Systems Research*, vol. 62, no. 3, pp. 191–200, 2002.
- [54] D. Chen and S. Xie, "Review of the control strategies applied to active power filters," in *Proceedings of the IEEE International Conference on Electric Utility Deregulation, Restructuring and Power Technologies (DRPT '04)*, pp. 666–670, April 2004.
- [55] K. Zhou, Z. Lv, A. Luo, and L. Liu, "Control strategy of shunt hybrid active power filter in distribution network containing distributed power," in *Proceedings of the China International Conference on Electricity Distribution (CICED '10)*, pp. 1–10, September 2010.
- [56] D. Detjen, J. Jacobs, R. W. De Doncker, and H. G. Mall, "A new hybrid filter to dampen resonances and compensate harmonic currents in industrial power systems with power factor correction equipment," *IEEE Transactions on Power Electronics*, vol. 16, no. 6, pp. 821–827, 2001.
- [57] M. Aredes, J. Häfner, and K. Heumann, "Three-phase four-wire shunt active filter control strategies," *IEEE Transactions on Power Electronics*, vol. 12, no. 2, pp. 311–318, 1997.
- [58] M. Aredes, L. F. C. Monteiro, and J. M. Miguel, "Control strategies for series and shunt active filters," in *Proceedings of the IEEE Bologna Power Tech Conference*, pp. 1–6, June 2003.
- [59] S. Khalid and A. Tripathi, "comparison of sinusoidal current control strategy & synchronous rotating frame strategy for total harmonic reduction for power electronic converters in aircraft system under different load conditions," *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering*, vol. 1, no. 4, pp. 305–313, 2012.

