

Research Article

Prospects of 4H-SiC Double Drift Region IMPATT Device as a Photo-Sensitive High-Power Source at 0.7 Terahertz Frequency Regime

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The dynamic performance of wide-bandgap 4H-SiC based double drift region ($p^{++}pnm^{++}$) IMPATT diode is simulated for the first time at terahertz frequency (0.7 Terahertz) region. The simulation experiment establishes the potential of SiC based IMPATT diode as a high power ($2.5 \times 10^{11} \text{ Wm}^{-2}$) terahertz source. The parasitic series resistance in the device is found to reduce the RF power output by 10.7%. The effects of external radiation on the simulated diode are also studied. It is found that (i) the negative conductance and (ii) the negative resistance of the diode decrease, while, the frequency of operation and the quality factor shift upward under photoillumination. Holes in 4H-SiC based IMPATT are found to dominate the modulation activities. The inequality in the magnitude of electron and hole ionization rates in the semiconductors may be correlated with these findings.

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1. INTRODUCTION

Terahertz (THz) science and technology are rapidly developing all over the world. There is strong interest in the exploitation of the THz frequency range in virtually all fields of basic natural science (physics, chemistry, and biology) as well as medicine [1]. Terahertz applications and technologies for operation in the range between 300 GHz to 10 THz (1 millimeter to 30 micrometers) are attracting increased interest from military and security fields. Scientists all over the world are searching for high-power solid-state sources that can be employed as efficient THz source. Impact avalanche transit time (IMPATT) diodes are the most powerful solid-state sources at mm and sub-mm-wave frequencies and are widely used in various civilian and space communication systems as well as in high-power radars, missile seekers, and so forth. The conventional IMPATT diodes fabricated on gallium arsenide (GaAs) and silicon (Si) are found to be reliable, but these are limited by power and operating frequencies due to the fundamental

limitations of the material parameters. To meet the gradual demand of high-power THz solid-state sources, extensive research is being carried out for development of high-power (Watt level) IMPATT devices that can be operated at high-frequency region. One approach is to employ power combining technique to increase the output power of the IMPATT devices, but it is practically difficult to combine large number of devices. The other option is to develop IMPATT devices from wide bandgap (WBG) semiconductors having high-critical electric field (E_C) and high-thermal conductivity (K) [2]. High value of E_C as well as high-saturation drift velocity of charge carriers ($v_{sn,sp}$), is essential criterion for selecting suitable base semiconductor material for IMPATT fabrication, since the RF power density (P_{RF}) of an IMPATT device is proportional to $E_C^2 \cdot v_s^2$. Moreover, to achieve good thermal stability in THz devices, the base material should have high value of thermal conductivity (K) also. Considering all these facts, WBG semiconductor silicon carbide (SiC) is an automatic choice for designing of high-power IMPATT, because this material offers at least

(i) $10 \times E_c$, (ii) $1.5 \times v_s$, and (iii) $3 \times K$, in comparison to those of conventional Si, GaAs, and indium phosphide (InP). That means, SiC IMPATTs are likely to produce $400 \times P_{RF}$ than its counterparts. The expected excellent performances of WBG SiC-based devices can be assessed by figures of merit (FOM). Keyes' FOM considers the speed of transistors and their thermal limitation, and Johnson's FOM considers the high-frequency and high-power capability of devices. Taking Keyes' and Johnson's FOM for Si as unity, the Keyes' and Johnson's FOM for GaAs are 0.45 and 7.1, respectively, while those of 4H-SiC are 5.1 and 278 [2]. Following the FOMs for high-frequency and high-temperature operation SiC is found to be superior to both Si and GaAs. The availability of 4H-SiC polytypes in bulk wafer form [3, 4] has helped SiC to emerge as relatively mature wide bandgap semiconductor technologies. So, in the light of the maturity of the fabrication technology and the unique material parameters, WBG semiconductors, especially, SiC appear to be the best choice, overall, for the next decade of device development particularly at THz region. At low-frequency region, the superiority of 4H-SiC-based IMPATT over the traditional IMPATTs is already reported [5]. But the prospects of SiC as high-power THz source are still not explored. The authors in this paper have reported for the first time the potential of 4H-SiC as a suitable base material for THz IMPATT device. Presently, two SiC polytypes are popular in SiC research: 6H-SiC and 4H-SiC. Although both the polytypes have similar properties, 4H-SiC is preferred over 6H-SiC because the carrier mobilities in 4H-SiC are isotropic, that is, identical along the two planes (parallel and perpendicular to c -axis) of the hexagonal semiconductor, whereas in 6H-SiC, carriers exhibit anisotropic mobility. Moreover, it is already reported that the high-frequency performance of 4H-SiC IMPATT is far better than its 6H-SiC counterpart in terms of V_B as well as P_{RF} [6]. Thus, the possibility of generating high power from an IMPATT has been investigated by studying the DC and small-signal properties of WBG 4H-SiC-based flat profile DDR (double drift region, $p^{++} p n n^{++}$) IMPATT diode simulated for operation at THz regime.

Parasitic positive series resistance (R_S) is a crucial parameter that limits power dissipation and causes burn out problem in high frequency IMPATTs. Apart from the contribution from substrate, the p-n junction diode parameters, especially, the width of the depletion layer, doping density, and so forth, also contribute to R_S . Moreover, the contact resistance of the device contributes significantly to the total parasitic series resistance. The authors have determined the value of R_S of the simulated IMPATT device considering contribution from the substrates, undepleted epitaxial layer and ohmic contacts. The authors have evaluated the values of R_S for the designed diode through a generalized simulation technique, and its effect on exploitable power level of the device has also been simulated.

Optical control of the dynamic properties of THz IMPATT device can have tremendous application in advanced radars and space communication systems. Basic process involved is that, when a photon (due to optical /other radiation) of energy $h\nu$ greater than the bandgap of the

semiconductor is absorbed at the edges of the reversed biased p-n junction of an IMPATT diode, creation of additional electron-hole pairs takes place within the active region of the diode. These photogenerated carriers give rise to photocurrent and thereby enhance the existing thermal leakage current in the IMPATT diode. The enhanced leakage current alters the avalanche phase delay in the diode, which subsequently modifies the phase and magnitude of terminal current in the device oscillator circuit. The previous experimental [7] as well as theoretical [5, 6] studies on optically illuminated IMPATT diodes indicated that the photogenerated carriers reduce the efficiency and power output of the devices, but increase the tuning range of the devices in the mm-wave frequency band. Moreover, a recent report on the effect of ionizing radiation on Si IMPATT diode characteristics establishes that control of IMPATT performance by any external radiation is an emerging technique that is currently being explored for important application possibilities [8]. These interesting results for photoilluminated IMPATTs have prompted the authors to investigate theoretically the role of external radiation in modulating the dynamic properties of the 4H-SiC DDR THz IMPATT. A modified simulation scheme has been used for this purpose.

2. THEORY OF SIMULATION TECHNIQUE

The simulation method consists of three parts: (i) dc analysis, (ii) small-signal analysis, and (iii) simulation technique for studying illumination effect. The IMPATT diode is basically a p-n junction diode that operates when it is reverse-biased to avalanche breakdown condition. A one-dimensional model of the p-n junction has been considered in the present analysis. The following assumptions have been made in the simulation of dc and small-signal behavior of 4H-SiC DDR IMPATT diodes: (i) the electron and hole velocities have been taken to be saturated and independent of the electric field throughout the space charge layer, (ii) the effect of carrier space-charge has been considered, and (iii) the effect of carrier diffusion has been neglected.

4H-SiC DDR diode is first designed and optimized through a generalized double iterative simulation technique used for analysis of IMPATT action [9]. The method involves iteration over the magnitude of field maximum (E_m) and its location in the depletion layer. The electric field and carrier current profiles are obtained through simultaneous solution of Poisson and current continuity equations. The experimental values of material parameters, namely, realistic field dependence of ionization rates, saturated drift velocities, and mobility of charge carriers in 4H-SiC [10] are incorporated in the present analysis. The junction temperature is assumed to be 300 K. The device dimensions, doping, and current densities of the diode (shown in Table 1) are optimized for operation at around 700 GHz after several computer runs.

The small-signal analysis of the IMPATT diode is carried out through a double iterative simulation technique [9], used to solve two second-order differential equations involving diode resistance (Z_R) and reactance (Z_X). The small-signal admittance characteristics (negative conductance ($-G$))

TABLE 1: Design parameters of 4H-SiC impatt at thz frequency.

DDR diode type	Doping conc. (n region) (10^{24} m^{-3})	Doping conc. (p region) (10^{23} m^{-3})	Width of the n region (nm)	Width of the p region (nm)	Current density (10^{10} A m^{-2})
4H-SiC	3.0	3.0	80.0	80.0	1.37

versus susceptance (B) plots), device negative resistance, and device quality factor ($Q = B/G$) of the optimized SiC DDR diode are determined by this technique after satisfying the appropriate boundary conditions [11]. The total integrated negative resistance (Z_R) and reactance (Z_X) of the diodes at a particular frequency ω can be determined from the numerical integration of the resistivity (R) and the reactivity (X) profiles over the depletion layer (depletion layer width is w) as follows:

$$Z_R = \int_0^w R dx, \quad (1)$$

$$Z_X = \int_0^w X dx.$$

Diode impedance is expressed as $Z(x, \omega) = R(x, \omega) + jX(x, \omega)$.

Moreover, the total diode impedance $Z_{\text{total}}(\omega)$ is obtained by

$$Z_{\text{total}}(\omega) = \int_0^w Z(x, \omega) dx = Z_R + jZ_X. \quad (2)$$

The diode total negative conductance (G) and susceptance (B) have been calculated from the following expressions:

$$G = -\frac{Z_R}{(Z_R)^2 + (Z_X)^2}, \quad B = \frac{Z_X}{(Z_R)^2 + (Z_X)^2}. \quad (3)$$

G and B are function of RF voltage (V_{RF}) and frequency such that the steady-state condition for oscillation is given by [12]

$$g(\omega) = -G(\omega) - \{B(\omega)\}^2 R_S(\omega), \quad (4)$$

where g is load conductance. G , B , and g are normalized to the area of the diode. The relation provides minimum uncertainty in g at low-power oscillation threshold. The authors have evaluated R_S from the admittance characteristics using the realistic analysis of Gummel and Blue [13], and Alderstein et al. [12] without any drastic assumption. Under the small-signal condition, V_{RF} (amplitude of the RF swing) has been taken as $V_B/2$, assuming 50% modulation of the breakdown voltage V_B . For such a small value of V_{RF} , R_S has been calculated by considering the value of g nearly equal to the diode conductance (G) at resonance. The maximum RF power density (P_{RF}) from the device is obtained from the expression:

$$P_{\text{RF}} = \frac{V_{\text{RF}}^2 G_P}{2}. \quad (5)$$

The diode negative conductance at the optimum frequency ($-G_p$) is normalized to the area of the diode. The space step for the present simulation technique is set

as $\sim 10^{-10}$ m. The validity of this simulation method was reported earlier for subterahertz and THz IMPATT diodes [11, 14].

The leakage current (J_s), entering the depletion region of the reversed biased p-n junction of an IMPATT diode, is normally due to thermally generated electrons and holes [$J_s = J_{ns(\text{th})} + J_{ps(\text{th})}$], and it is so small that electron and hole current multiplication factor,

$$M_{n,p} = \frac{J_0}{[J_{ns(\text{th})} \text{ or } J_{ps(\text{th})}]}, \quad [J_0 = \text{bias current density}] \quad (6)$$

can be considered to be infinitely large. Thus, the enhancement of the leakage current under optical illumination of the devices is manifested as the lowering of M_n , p . In a DDR IMPATT structure, the composition of photocurrent may be altered by shining a laser beam selectively on the p^{++} or n^{++} side of the device through fabricated optical windows of appropriate diameter, keeping the diode mounted in a microwave cavity. Thus, the electron saturation current and also the hole saturation current might be enhanced separately, which would produce changes in the small-signal behavior of SiC-based device. The top mounted (TM) and flip chip (FC) structures, shown in Figure 1, illustrate the optical illumination schemes for generation of electron and hole dominated photocurrents, respectively. In order to assess the role of leakage current in controlling the dynamic properties of 4H-SiC IMPATT oscillators, simulation studies are carried out by the authors on the effect of M_n (keeping M_p very high $\sim 10^6$) and M_p (keeping M_n very high $\sim 10^6$) on (i) the small signal negative conductance, (ii) the RF power, and (iii) device negative resistance (Z_{RP}) of flat profile 4H-SiC DDR IMPATT. The details of mathematical calculations based on modified boundary conditions due to enhancement of leakage current are described elsewhere [11].

3. RESULTS AND DISCUSSIONS

The optimized design parameters of the unilluminated SiC DDR IMPATT diode for which M_n and M_p are both large ($= 10^6$) are mentioned in the last section. The DC and high-frequency properties of the simulated diode are reported in Table 2 and will be discussed first. Table 2 shows that the THz IMPATT diode based on SiC breaks down at 55.3 V. The simulated diode is capable of generating a maximum P_{RF} of $2.8 \times 10^{11} \text{ Wm}^{-2}$ with efficiency 10.5 %.

Table 3 shows the value of R_S at 0.7 THz for the simulated diode. The small-signal values of negative conductance (G), susceptance (B), and expected values of load conductance (g , at low-power oscillation threshold) are also reported in Table 3. The value of R_S for 4H-SiC IMPATT is found

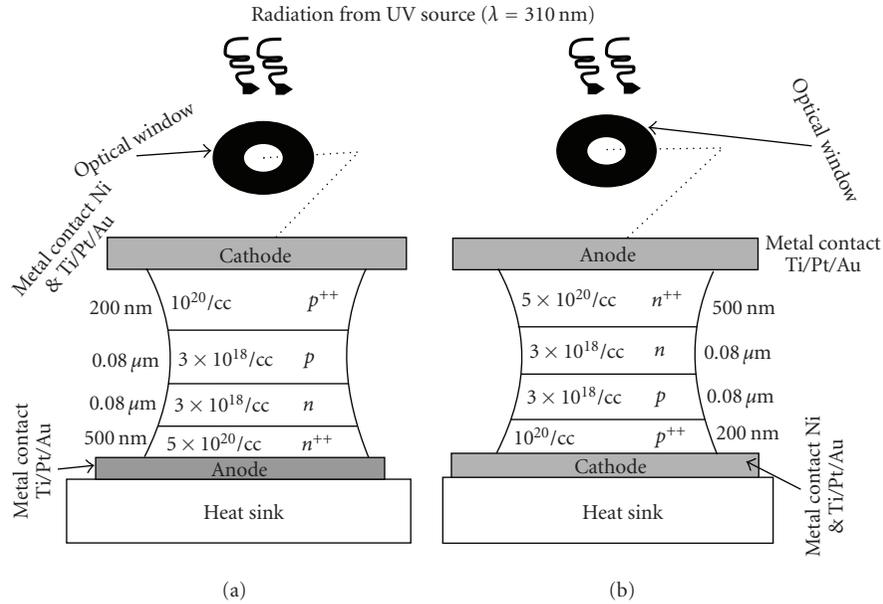


FIGURE 1: Schematic diagram of (a) top mounted and (b) flip chip DDR IMPATT diode under optical-illumination.

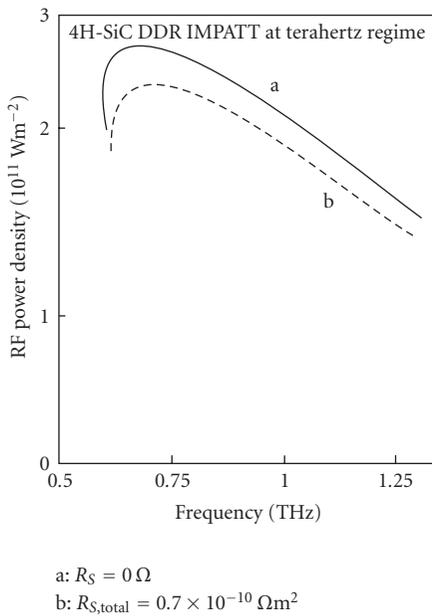


FIGURE 2: Effect of series resistance on RF power density of 4H-SiC Terahertz IMPATT diode.

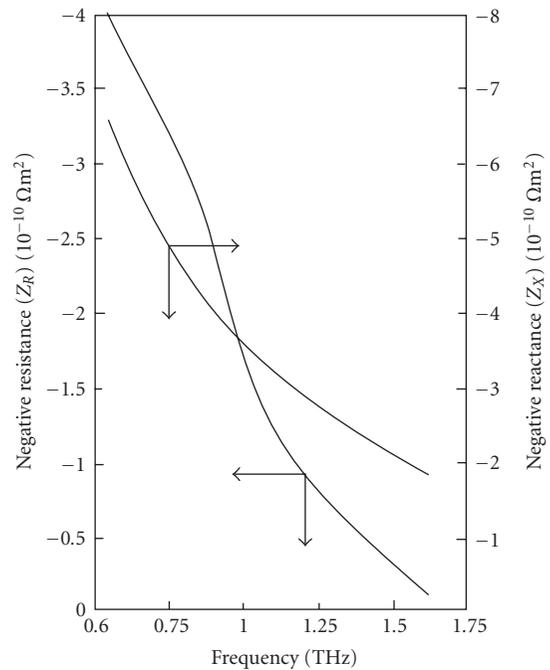


FIGURE 3: Impedance plots of 4H-SiC IMPATT diode at Terahertz region.

to be $0.49 \times 10^{-10} \Omega\text{m}^2$. Ohmic contact resistance may put a severe restriction on the high frequency (THz level) performance of the wide bandgap IMPATT devices and thus the presence of contact resistance should be included in the realistic consideration of parasitic positive series resistance. A very recent study showed that ohmic contact to n-SiC are formed by using pure Ni-based layer with a thin underlying Si layer. By this technique, a stable and low n-SiC contact resistivity of $\sim 10^{-7} \Omega \text{ cm}^2$ can be realized in practice [15].

Very low specific contact resistance for p-SiC has not been achieved in reality. Using alloy composition such as Ni/Al to p-SiC, a contact resistivity $\sim 10^{-6} \Omega \text{ cm}^2$ can be realized in practice [16]. It is noteworthy to mention that, in order to get appreciable power (Watt level) from a THz source, low specific contact resistance ($\sim 10^{-7} \Omega \text{ cm}^2$) should be achieved, since at THz region intrinsic diode negative resistance is usually very small. It may be predicted that, further increasing the doping concentration of p-SiC semiconductor

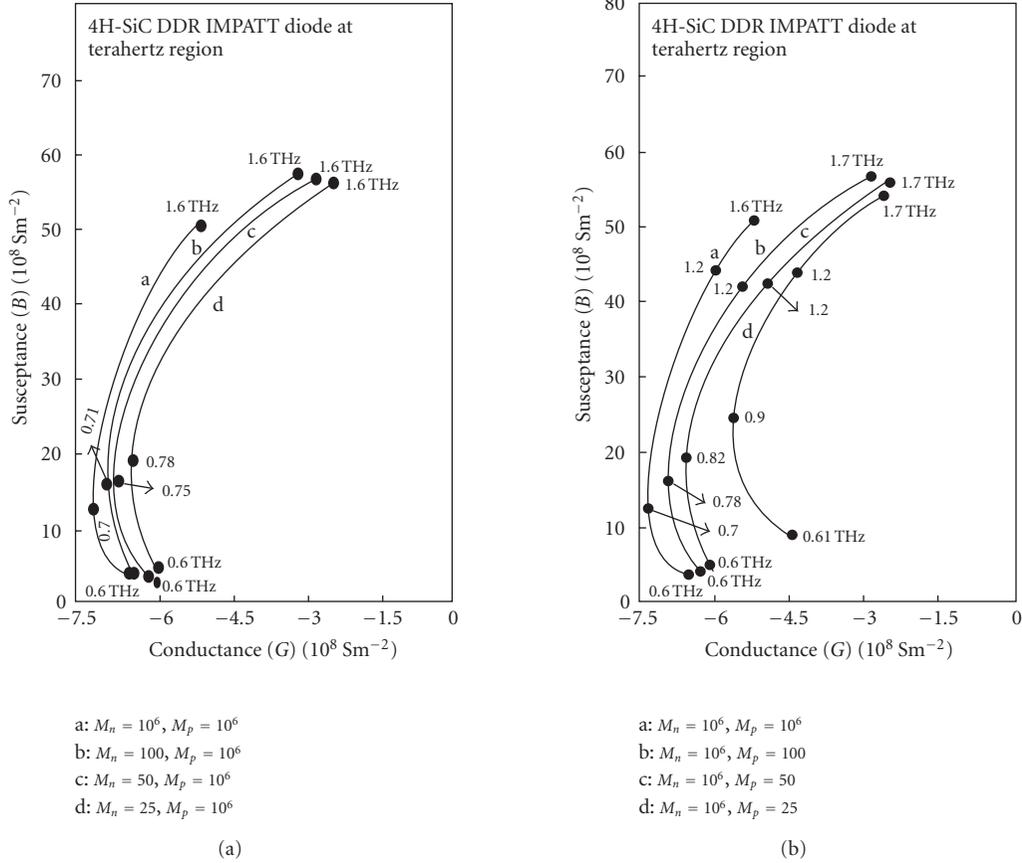


FIGURE 4: (a) Effect of electron dominated photo-current on 4H-SiC DDR IMPATT diode at terahertz region. (b) Effect of hole dominated photo-current on 4H-SiC DDR IMPATT diode at terahertz region.

TABLE 2: DC and small-signal properties of 4H-SiC IMPATT diode.

Diode parameters	4H-SiC DDR IMPATT
E_m (10^8 V m $^{-1}$)	5.25
V_B (V)	55.3
η (%)	10.5
f_p (THz)	0.7
$-G_p$ (10^8 S m $^{-2}$)	7.40
$-Q_p$	1.6
P_{RF} ($R_s = 0.0 \Omega$) (10^{11} W m $^{-2}$)	2.8
P_{RF} ($R_{s,total} = 0.7 \Omega$) (10^{11} W m $^{-2}$)	2.5

material, a desired contact resistivity $\sim 10^{-7} \Omega \text{ cm}^2$ may be achieved in reality. Hence, more realistic values of effective parasitic series resistance ($R_{S,total}$, including the contribution of contact resistance) become approximately $0.7 \times 10^{-10} \Omega \text{ m}^2$ for 4H-SiC IMPATT. Measurement of such a low contact resistance may be possible with transmission line measurement (TLM) technology. The effect of $R_{S,total}$ on P_{RF} of the diode is shown in Figure 2. It is reflected in Figure that even in the presence of aforesaid $R_{S,total}$ an appreciable RF power density of $2.5 \times 10^{11} \text{ W m}^{-2}$ may be obtained from the simulated THz device.

Figure 3 shows the small-signal impedance plot for the THz diode. The graphs show that the device possesses negative resistance for all frequencies above the avalanche frequency (f_a), where its reactance is capacitive. This is due to the fact that, in the oscillating frequency range, the magnitude of Z_R is found to be small compared to Z_X . This is also evident from Figure 3, that the values of $|-Z_R|$ and $|-Z_X|$ decrease as the operating frequency increases.

The effects of electron and hole dominated photocurrents on the THz performance of the WBG IMPATT are presented in Table 4. The table shows that the values of $|-G_p|$ of the diode decrease with the lowering of M_n and M_p . At the same time, the frequency range over, which the device exhibits negative conductance, shifts towards higher frequencies with the lowering of M_n as well as M_p . The output data for illuminated TM and FC flat DDR IMPATT diodes (see Table 4) indicate that the value of negative conductance at peak frequency $|-G_p|$ decreases by 6.7% when M_n reduces from 10^6 to 25 for SiC-based THz device, while for the similar lowering of M_p , $|-G_p|$ decreases by 23.0%. The identical trend is reflected in Figures 4(a) and 4(b), where the admittance plots of SiC-based photoilluminated THz device are plotted. The figures show that the effect of hole dominated photocurrent in modulating the admittance characteristics is much prominent than the

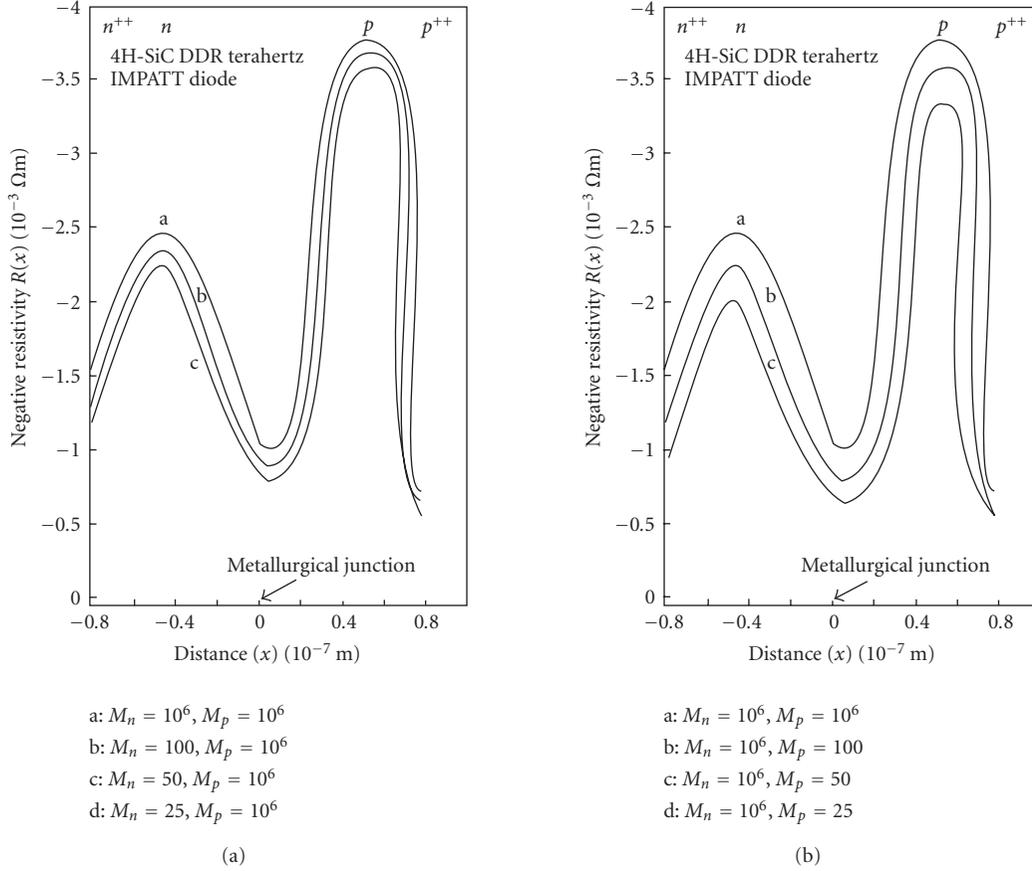


FIGURE 5: (a) Effect of electron dominated photo-current on negative resistivity profile of 4H-SiC DDR IMPATT diode. (b) Effect of hole dominated photo-current on negative resistivity profile of 4H-SiC DDR IMPATT diode.

TABLE 3: Series resistance of 4H-SiC IMPATT at 0.7 THz.

Negative conductance ($-G$) (10^8 Sm^{-2})	Susceptance (B) (10^8 Sm^{-2})	Load conductance (10^8 Sm^{-2})	Series resistance (R_S) ($10^{-10} \Omega \text{ m}^2$)	Negative resistance ($-Z_R$) ($10^{-10} \Omega \text{ m}^2$)
7.4	12.0	6.7	0.49	3.8

electron dominated photocurrent. The optimum frequency of oscillation (f_p) for the illuminated SiC diode increases by 11.4% as M_n reduces from 10^6 to 25. However, for the similar variation of M_p , the upward shift of f_p is much higher (29.0%).

Figures 5(a) and 5(b) show the profiles of negative resistivity at the peak frequencies corresponding to different values of M_n ($M_p = 10^6$) and M_p ($M_n = 10^6$) for SiC-based diode, respectively. Negative resistivity profiles give a physical insight into the region of the depletion layer that contributes to RF power. In each case, the profiles are characterized by two negative resistivity peaks in the middle of the two drift layers of the diode interspaced by a dip in the avalanche region. It is observed from the figures that due to the enhancement of electron and hole photocurrents, the negative resistivity peaks in the electron, and hole drift layers are depressed gradually. It is also found that the

decrease in the magnitude of the negative resistivity peaks is more pronounced for variation of M_p corresponding to hole dominated photocurrent than for the same variation of M_n corresponding to electron dominated photocurrent.

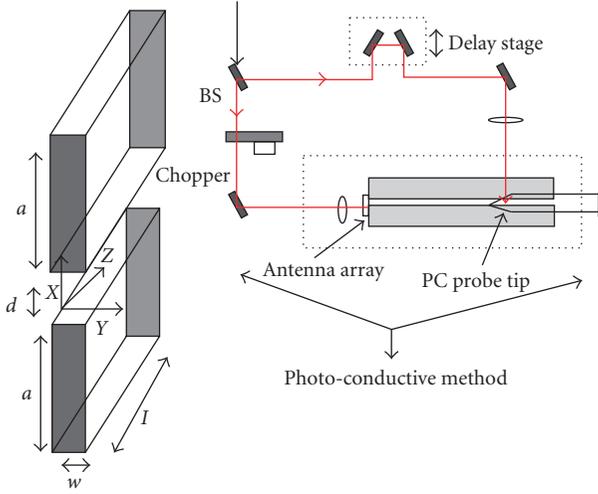
Simulation study also depicts that in case of 4H-SiC DDR IMPATT, as M_n is decreased from 10^6 to 25, there occurs a 54.0% decrease in the diode negative resistance ($-Z_{Rp}$). On the other hand, as M_p changes from 10^6 to 25, $-Z_{Rp}$ reduces by 74.3% (see Table 4).

The variations of power output with optimum frequency for different values of M_n and M_p are also shown in Table 4. Again, it is found from the study that the magnitude of device quality factor at optimum frequency of oscillation $| -Q_p |$ increases appreciably for a decrease of M_p compared to the same change of M_n values (see Table 4).

The enhancement of leakage currents by electron and hole dominated photocurrents thus leads to a decrease of

TABLE 4: Variation of small-signal properties of 4H-SiC DDR IMPATT under optical-illumination.

M_n	M_p	f_p (THz)	$-G_p$ (10^8 Sm^{-2})	$-Z_{Rp}$ ($10^{-10} \Omega\text{m}^2$)	P_{RF} (10^{11} Wm^{-2})	$-Q_p$
10^6	10^6	0.70	7.4	3.8	2.82	1.6
100	-	0.71	7.2	2.6	2.75	2.08
50	-	0.75	7.1	2.1	2.71	2.39
25	-	0.78	6.9	1.7	2.63	2.75
10^6	100	0.78	7.0	2.5	2.67	2.14
-	50	0.82	6.4	1.6	2.45	2.97
-	25	0.90	5.7	0.95	2.18	4.2

FIGURE 6: Waveguide geometry: $d = 270 \mu\text{m}$, $w = 300.0 \mu\text{m}$, and $l = 140.0 \text{ mm}$.

$-Z_{Rp}$, $-G_p$, and P_{RF} along with a simultaneous upward shift of f_p . While the photo-generated leakage current dominated by holes modulates the DC and high-frequency properties of the device appreciably, that dominated by electrons has relatively less effect in modulating the same. For the mm-wave Si DDR, it was reported that the electron saturation current was more dominant in changing the P_{RF} and the f_p [7]. In Si, the electron ionization rate is greater than the hole ionization rate. This means that by controlling the DDR IMPATT action through more ionizing carriers in Si, a higher frequency shift may be achieved. By the same logic, it can also be concluded that in 4H-SiC, holes are more ionizing carriers than electrons, since 4H-SiC DDR IMPATT under optical illumination at THz region is more sensitive to hole dominated photocurrent. This relative predominance of hole leakage current in optical control of 4H-SiC DDR IMPATT performance can be attributed to the inequality of the ionization coefficient values in 4H-SiC [10].

The thermal conductivity of 4H-SiC ($\sim 500 \text{ W m}^{-1} \text{ K}^{-1}$) is very high. This helps the SiC IMPATT to dissipate the generated heat quickly from the junction. Moreover, if a diamond heat sink is employed for the practical realization of 4H-SiC THz IMPATT device, the effective thermal conductivity will increase to such a high value that the junction temperature will not increase significantly over the ambient (300 K), though the device yields a P_{RF} of $2.5 \times 10^{11} \text{ Wm}^{-2}$.

Due to lack of any experimental data on SiC THz IMPATT diode, the simulation results could not be compared. However, the nature of variation of THz properties of the designed diode under photoillumination has a trend agreement with the experimental results of Si-based IMPATT diode at lower frequency region. It may be mentioned here that a large-signal computer simulation may provide improved quantitative information regarding the influence of optical illumination on the frequency tuning as well as RF power output, but the nature of response to optical illumination, as predicted by the small-signal analysis, will remain unchanged.

4. CONCLUSION

The current simulation study establishes that 4H-SiC-based DDR IMPATT diode can generate high power even at a frequency as high as 0.7 THz. In the earlier reported paper [14], the frequency of operation was limited to 0.5 THz only. Moreover, in the present paper, the authors have realistically simulated the value of total parasitic series resistance for THz SiC IMPATT, which includes the contribution both from the diode as well as from ohmic contact resistance, while, in the previous article [14], the series resistance due to contact metals was not considered. Based on the information of total series resistance, provided in this paper, one can get more accurate idea of exploitable power level from the SiC DDR diode at THz region. Further, the effect of external radiation on this device at 0.7 THz seems to be significantly prominent. It is thus expected that the photo-irradiated 4H-SiC DDR IMPATT at still higher frequencies will have better frequency tuning capability. A discussion on feasibility of experimental validation was not incorporated in the previous article [14]. In the present paper, the authors will discuss below the possibility of experimental verification of optical illumination on the 4H-SiC DDR diode. The realization of 4H-SiC THz IMPATT diode may be done through the following process steps.

Device fabrication following usual process steps like photolithography, deposition of metal contacts, and mesa formation by reactive ion etching.

Characterization of THz properties may be possible with a dielectric coplanar wave guide, the device being an integral part of the waveguide.

For terahertz (0.1–1 THz) power transmission, a metallic slit waveguide, fabricated by sawing a $270 \mu\text{m}$ wide slit

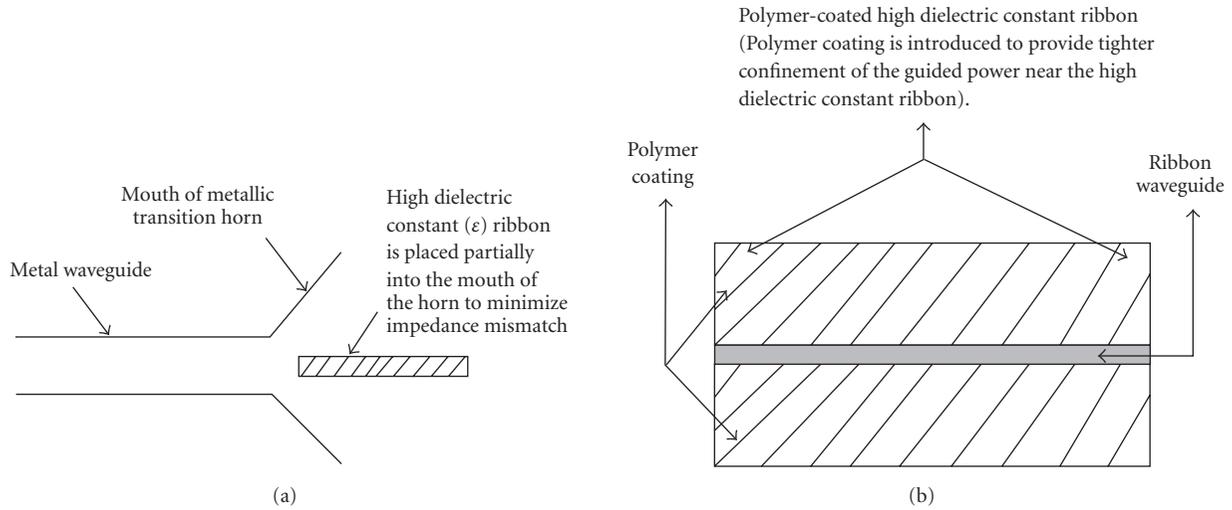


FIGURE 7: (a) Rectangular metal waveguide to high dielectric constant ribbon waveguide transition [17]. (b) Longitudinal cross-sectional geometry of a polymer-coated high dielectric constant ribbon. The thickness and the width of the high dielectric constant ribbon are $0.0635\lambda_0$ and $0.635\lambda_0$, respectively. The thickness of the polymer coating is approx. $0.25\lambda_0$, and the width is approx. $0.635\lambda_0$. The dielectric constant of the ribbon is 10 while that of the polymer is 2.04 [17].

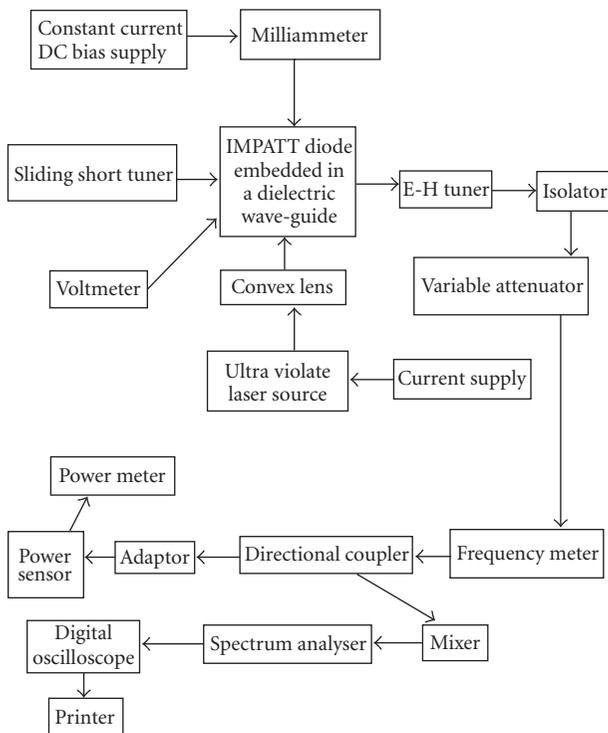


FIGURE 8: Experimental setup for optical illumination experiment on 4H-SiC IMPATT diode at THz region.

through a 140 mm wide and $300\mu\text{m}$ thick silicon slabs may be used (see Figure 6) [18]. For higher frequency (3 THz) power transfer, a ribbon-like structure fabricated from ceramic alumina may be utilized [17]. The schematic diagrams of the structures are shown in Figures 7(a) and 7(b).

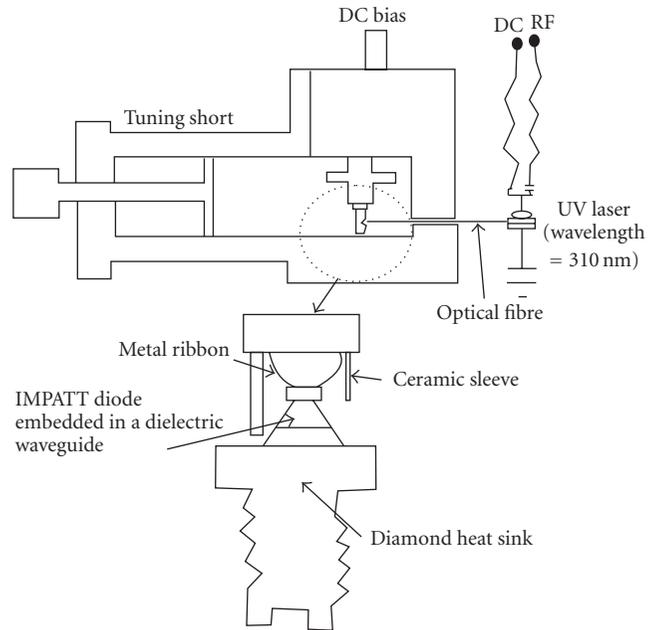


FIGURE 9: Optical experimental setup for THz IMPATT diode.

Measurements of THz power and frequency may be done with a THz vector network analyzer (VNA) or, by employing photoconductive method (as shown in right-hand diagram of Figure 6).

An arrangement similar to that shown in Figures 8 and 9, appropriately modified to include the photoconductive detection of THz power and frequency, can be used for optical illumination experiment on 4H-SiC IMPATT. The intensity of radiation may be experimentally increased by a convex lens as indicated in Figure 8.

It is concluded that these findings may be utilized for realizing optically integrated THz modules for effective applications in THz communication and in interstellar explorers.

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