A Communication-Assisted Distance Protection for AC Microgrids considering the Fault-Ride-Through Requirements of Distributed Generators

Deming Wang, Fei Li, and Yingliang Li

1Shaanxi Railway Institute, Weinan 714000, China
2School of Electronic Engineering, Xi’an Shiyou University, Xi’an 710065, China

Correspondence should be addressed to Deming Wang; wangdming@foxmail.com

Received 21 September 2023; Revised 14 November 2023; Accepted 1 December 2023; Published 16 December 2023

Copyright © 2023 Deming Wang et al. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

The conventional overcurrent protection is ineffective in isolating faults in microgrids due to the low fault current levels contributed by inverter-interfaced distributed generations (IIDGs). To extend the microgrid protection methods, the distance protection is considered as a common alternative to detect faults. However, the fault-ride-through (FRT) requirements and the high impedance fault (HIF) detection challenge the application of the conventional distance protection. To solve the two problems, a new inverse-time distance (ITD) protection for medium-voltage AC microgrids was proposed in this paper. The primary ITD protection was designed to meet the FRT requirements, and the backup protection was coordinated to isolate faults. The fault resistance and distributed generation infeed effect on the measured impedance were mitigated by a communication-assisted method proposed in this paper. Owing to the shorter communication channel time in distribution feeders and lower communication investments without a synchronous clock, the proposed method can be used to improve the ITD protection performance. An 11 kV AC microgrid was simulated with different fault types in the islanded mode and grid-connected mode. Time domain simulation verified the effectiveness of the proposed ITD protection.

1. Introduction

Microgrid containing both distributed generation (DG) and load has attracted interest for their salient features. A microgrid can be regarded as a controlled subsystem that reduces transmission losses, diversifies power suppliers, enhances power quality, and improves system reliability [1, 2]. Despite its advantages, the integrated DGs increase burden on the feeder protection system in microgrids [1, 3].

DG integrated via a voltage source converter (VSC) into a microgrid is the main type in the current practice, and it limits the output current to protect its power electronic device [4–6]. The fault current provided by an inverter-interfaced distributed generation (IIDG) is much lower than the one supplied by the upstream network, and the IIDG fault characteristics are different from a synchronous machine-based generation (SMDG), which brings challenges to the application of the conventional feeder protection system [7, 8]. What is more, a DG-based microgrid can operate either in grid-connected mode or islanded mode, increasing difficulties in applying the conventional feeder protection.

To overcome the problems in microgrid protection system, various strategies have been proposed by researchers. Some researchers considered using adaptive protection schemes to solve the problem in microgrids [9–11]. Oudalov and Fidigatti proposed an adaptive microgrid protection based on using numerical directional overcurrent relays and communication system [10]. Offline network status analysis and online transmitting signal operations were the main parts of the novel scheme. According to Sharaf et al. [11], a dual setting overcurrent protection scheme was presented based on the communication system and the microgrid protection problem was solved by using optimisation functions. The result could be affected by the initial values of the algorithm. It is necessary to calculate short-circuit currents and know all possible configurations of microgrids.
before using the adaptive protection schemes, which complicates the implementation of these approaches. The differential protection methodologies were also developed in medium-voltage AC microgrids [12–14]. A variable tripping time differential protection scheme (VTDPS) was presented by Aghdam et al. [13], which was based on a multiagent system and tested in a synchronous DG-integrated microgrid. Soleimanisardoo et al. [14] proposed a differential protection scheme to isolate faults in an islanded microgrid. The method detected the current frequency differences depending on the injected off-nominal frequency. Although the differential protection approaches can adapt different fault current levels and isolate faults at a high speed, the communication system infrastructure using a precise synchronous clock is relatively costly and the network configuration can affect the performance of the differential protection.

Other approaches were also investigated, including using fault current limiters (FCLs) or energy storage devices to alter the fault current level [15–17], utilising the methods based on the voltage detection [18–21], and implementing the distance protection methodologies [22–30]. Ibrahim et al. proposed an approach to coordinate the protection relays considering the presence of FCL [17]. The FCL impedance values are difficult to determine considering the mutual influence of DGs, and the investments of the storage devices are too significant to apply these approaches. Using the phase differences between the bus voltage and feeder current, Zhang and Mu developed a fault detection method in microgrids with IIDDG connection [21]. The voltage-based schemes are strongly dependent on the network topology and less sensitive in the grid-connected operation.

For the features that change very little in different network topologies, it is reasonable to use the distance protection to detect and clear faults in microgrids. Dewadasa et al. [22] proposed an inverse-time admittance (ITA) protection method, which can be regarded as an improved distance protection. A combined distance protection was developed by Lee et al. [24], utilising distance protection relay and directional relay in single-line fault detection. An adaptive distance protection was proposed for the unbalanced faults by Liang et al. [25], and the high-frequency current was used to improve the distance protection performance in [26]. However, it was verified by Liang et al. [25] that it was inefficient to use the harmonic current injection method in high impedance fault (HIF) detection. Since the measured impedance can be easily affected by the presence of the fault resistance, the proposed protection operating time increased during HIF. The communication channel was used to developed distance protection by Biswas and Centeno [27] and Hooshyar et al. [29] in the active distribution network.

Although the distance protection is more sensitive and adaptive compared to the overcurrent protection, there are two main challenges when using distance protection. Firstly, the operating time should meet the fault-ride-through (FRT) requirements, which means the feeder protection should respond faster than the FRT-required time. Secondly, the measured impedance can easily be affected by the presence of fault impedance and DG infeed, leading to a longer operating time or failure in isolating faults. To solve the two problems, a new inverse-time distance (ITD) protection is proposed in this paper. The new ITD protection can detect faults with a shorter time to meet the FRT requirements, and the backup protection can be coordinated to isolate faults. What is more, a communication-assisted method is proposed to mitigate the fault impedance and DG infeed effect on the measured impedance. The structure of this paper is shown as the following. The FRT requirements are presented in Section 2. Section 3 illustrates the proposed ITD protection as well as the mitigating the fault impedance effect method. In Section 4, a protection scheme involving the proposed ITD protection is presented. Case study using MATLAB Simulink is conducted, and the simulation results are presented in Section 5. The time domain simulation results verify the effectiveness of the proposed method.

2. New Inverse-Time Distance Protection

2.1. FRT Requirements. DGs should have the capability to provide reactive power and dynamically support the distribution network voltage during fault conditions, which is often referred to FRT requirements [21, 31]. The general FRT requirements are shown in Figure 1, in which the DG common coupling point (PCC) voltage versus time-after-fault curve is depicted as well as the ride-through zone. DGs should stay connected to the distribution network during a fault for a certain time. The FRT-required time depends on the drop of the PCC voltage (denoted as $V_{\text{pcc}}$), shown as the red line in Figure 1. FRT requirements in different countries possess similar characteristics, and the minimum time $t_{\text{clear}}$ is normally selected as 0.15 seconds [1, 4]. To meet the FRT requirements, the operating time of the feeder primary protection should be less than the FRT-required time.

2.2. The Characteristics of ITD Protection. For the operating time that varies along with the fault location, the inverse-time overcurrent relays are predominately implemented in distribution networks. The ITD protection was developed based on the inverse-time characteristics [32, 33], considering the FRT requirements. To illustrate the characteristics of ITD protection, a simplified network is presented in
2.2.1. The Primary Protection. The primary protection should detect faults with much less operating time. The characteristics of the primary protection can be described as

\[
t_1 = \frac{A}{\left|\left(kZ_1\right)/Z_m^2\right| - 1},
\]

where \(t_1\) is the operating time of the primary protection, \(A\) is the time dial setting of the primary protection, \(k\) is a reliable coefficient which can be selected as 1.2 to cover the whole feeder length, \(Z_1\) is the impedance of the feeder covered by the primary protection, and \(Z_m\) is the impedance seen by relays.

The new ITD primary protection is designed to protect the feeder at full length and relays located at the sending side of the series feeders need to be coordinated. The minimum coordination time interval (CTI) between two series relays can be set as 0.3 seconds [9].

\[
t_1(Z_m = Z_1) = 0.3.
\]

\(A\) can be calculated through (1) and (2). It is 0.132 and independent of the feeder length. The proposed ITD primary protection possesses one setting group for different feeder lengths, which is more convenient for application.

The characteristics of the ITD primary protection are presented in Figure 3. It can be seen that the maximum operating time of the ITD primary protection is 0.15 seconds during a fault occurring within 87.5% of the feeder length, which is sufficient for the FRT requirements of DGs.

2.2.2. The Backup Protection. To improve the reliability of the protection system, the ITD protection needs a backup protection scheme, which can be expressed as

\[
t_2 = \frac{A}{\left|\left(kZ_1\right)/Z_m^2\right| - 1} + \Delta t,
\]

where \(t_2\) is the backup protection operating time, \(\Delta t\) is a coordination parameter, and \(Z_2\) is the impedance of the feeders covered by the backup protection. It is noted that the backup protection can cover the local and adjacent feeder.

The coordination between the primary and backup protection should be considered, and the minimum CTI between the primary and backup protection is set as 0.3 seconds [34].

\[
t_2(Z_m = Z_1) = t_1(Z_m = Z_1) + 0.3.
\]

\(\Delta t\) can be obtained through (2), (3), and (4) and is given in

\[
\Delta t = 0.6 - \frac{A}{\left|\left(kZ_1\right)/Z_m^2\right| - 1}.
\]

It is to be noticed that relays involving ITD characteristics can detect faults in either side of the relays. The direction can be detected in microgrids, such as utilising the relative phase angle between the prefault and superimposed sequence fault current presented by Muda and Jena [35] or using the postfault current phasor by Hosseini et al. [36]. The main focus is the ITD protection, and the detection of
the fault direction is not discussed in this paper. It is noted that relays involving ITD protection only respond to forward faults.

To present the characteristics of the proposed ITD protection, the operating time versus fault location curve is depicted in the case when faults are occurring in the sample network (shown in Figure 2). The feeder BC length is the same as the feeder AB length, and the relay ITD characteristics are shown in Figure 4. It can be seen that the operating time of the primary protection can meet the FRT requirements and the backup protection can be coordinated with the primary protection to isolate faults.

2.3. Different Elements of ITD Protection. The ITD protection can detect phase-to-ground faults as well as phase-to-phase faults by utilising different protection elements.

2.3.1. Ground Distance Element. The ground distance element can be used to detect a phase-to-ground fault, which is expressed as

\[ Z_m = \frac{V_{p-g}}{I_{p-g} + 3k_0I_0}, \]

where \( V_{p-g} \), \( I_{p-g} \), and \( I_0 \) are the rms phase fault voltage, the rms phase fault current, and the rms zero-sequence fault current seen by the relays, respectively, and \( k_0 \) is the zero-sequence current compensation factor.

\[ k_0 = \frac{Z_0 - Z^1}{3Z^1}, \]

where \( Z_0 \) and \( Z^1 \) are the feeder zero-sequence impedance and the feeder positive-sequence impedance, respectively.

2.3.2. Phase Distance Element. The phase-to-phase faults can be detected by using a phase distance element shown as the following.

\[ Z_m = \frac{V_{p-p}}{I_{p-p}} \]

where \( V_{p-p} \) and \( I_{p-p} \) are the rms phase-to-phase fault voltage and the rms phase-to-phase fault current seen by the relays, respectively.

2.4. Method to Mitigate the Fault Impedance and DG Infeed Effect. The measured impedance can easily be affected by the presence of fault impedance and DG infeed. To mitigate the effect and improve the ITD performance, a communication-assisted method is proposed.

2.4.1. Mitigating the Fault Impedance Effect. A single phase-to-ground fault with the presence of fault impedance is studied, which is shown in Figure 5. \( Z_{Af} \) is the impedance between bus A and \( f_1 \), \( Z_{Bf} \) is the impedance between bus B and \( f_1 \), and \( Z_f \) is the actual fault impedance existing at the point \( f_1 \). \( I_S \) and \( I_{DG} \) are the fault current fed from the upstream network and DG, respectively.

The measured impedance seen by relay 1 and relay 2 can be expressed as

\[ Z_{m(1)} = Z_{Af} + \left( \frac{I_{DG}}{I_S} + 1 \right)Z_f. \]
\[ Z_{m(2)} = Z_{bf} + \left( \frac{I_{c}^S}{I_{DG}^S} + 1 \right) Z_t, \]  
\[ (10) \]

where

\[ I_{DG}^S = I_{DG} + 3k_0 \cdot I_0^b, \]
\[ I_S^c = I_S + 3k_0 \cdot I_0^b. \]
\[ (11) \]

\( I_0^b \) and \( I_0^S \) are the zero-sequence fault currents fed from the upstream network and DGs, respectively.

It is to be noticed that the sum of \( Z_{Af} \) and \( Z_{Bf} \) is the whole impedance of the feeder AB, and the value of \( Z_t \) can be calculated through (9) and (10).

\[ Z_t^c = \frac{Z_{m(1)} + Z_{m(2)} - Z_{AB}}{\left( \frac{I_{c}^S}{I_{DG}^S} + \frac{I_{DG}^S}{I_{c}^S} \right) + 2}, \]
\[ (12) \]

where \( Z_{AB} \) is the whole impedance of the feeder AB.

The measured impedance without fault impedance interference can be calculated by

\[ Z_{m(1)}^c = Z_{m(1)} - \left( \frac{I_{DG}^S}{I_S^c} + 1 \right) Z_t^c, \]
\[ (13) \]

\[ Z_{m(2)}^c = Z_{m(2)} - \left( \frac{I_S^c}{I_{DG}^S} + 1 \right) Z_t^c, \]

where \( Z_{m(1)}^c \) and \( Z_{m(2)}^c \) are the calculated impedance which can substitute the measured impedance \( Z_{m(1)} \) and \( Z_{m(2)} \) to improve the performance of ITD protection.

As for the intermittence and randomicity of DGs, the fault impedance can also be calculated by (14) in the case when DG is out of operation.

\[ Z_t^c = \frac{Z_{m(2)}}{I_S^c}, \]
\[ (14) \]

where \( Z_{m(2)} \) is the fault voltage seen by relay 2.

### 2.4.2. Mitigating the Infeed Effect.

When there is a DG infeed, the measured impedance seen by the relays can also be affected. Figure 6 shows the DG infeed effect, in which \( Z_b \) represents the infeed branch impedance and \( I_b \) denotes the infeed current.

The measured impedance seen by relay 1, relay 2, and relay 3 can be expressed as

\[ Z_{m(1)}^c = Z_{Ab} + \left( \frac{I_{c}^b}{I_S^c} + 1 \right) Z_{bf} + \left( \frac{I_{c}^c}{I_{DG}^c} + \frac{I_{DG}^c}{I_{c}^c} + 1 \right) Z_t, \]
\[ (15) \]

\[ Z_{m(2)}^c = Z_{bf} + \left( \frac{I_{c}^b}{I_{DG}^c} + \frac{I_{DG}^c}{I_{c}^b} + 1 \right) Z_t, \]

\[ I_{c}^b = I_b + 3k_0 \cdot I_0^b, \]

where \( I_0^b \) is the zero-sequence fault current of the infeed branch.

The values of \( Z_{bf} \) and \( Z_t \) can be calculated through

\[ Z_{bf}^c = \frac{M \cdot P - K \cdot N}{J \cdot P - K \cdot L}, \]
\[ Z_t^c = \frac{J \cdot N - M \cdot L}{J \cdot P - K \cdot L}, \]
\[ (16) \]

where

\[ M = Z_{m(11)} - Z_b, \]
\[ N = Z_{m(1)} + Z_{m(2)} - Z_{AB}, \]
\[ J = \frac{I_{c}^S}{I_b} + 1, \]
\[ K = \frac{I_{c}^c}{I_b} + \frac{I_{DG}^c}{I_b} + 1, \]
\[ L = \frac{I_{DG}^c}{I_S^c}, \]
\[ P = \frac{I_{c}^b}{I_S^c} + \frac{I_{DG}^b}{I_{DG}^c} + \frac{I_{c}^c}{I_{DG}^c} + \frac{I_{DG}^c}{I_{c}^c} + 2. \]
The measured impedance can be substituted by the calculated impedance shown in

\[ Z_{c,m1} = Z_{m1} - \left( \frac{I_c^b}{I_b^c} + \frac{I_c^{DG}}{I_b^{DG}} + 1 \right) Z_{c,bf} - \left( \frac{I_c^b}{I_b^c} \right) Z_{c,bf}, \]

\[ Z_{c,m2} = Z_{m2} - \left( \frac{I_c^b}{I_b^c} + \frac{I_c^{DG}}{I_b^{DG}} + 1 \right) Z_{c,lf}, \]

\[ Z_{c,m11} = Z_{m11} - \left( \frac{I_c^b}{I_b^c} + \frac{I_c^{DG}}{I_b^{DG}} + 1 \right) Z_{c,lf} - \left( \frac{I_c^b}{I_b^c} \right) Z_{c,lf}. \]

It can be seen that the philosophy of the proposed method is utilising the remote-local current ratio to mitigate the errors in impedance measurement. The current ratio can be easily obtained by transmitting current signals between the related relays and using high-speed processors in digital relays.

In comparison with the ITA protection proposed in [22], the ITD protection has two advantages. First of all, the ITD protection uses single inverse-time characteristics, which is much simpler than the ITA protection. Secondly, the performance of the ITA protection can be easily affected by the fault resistance and DG infeed, which is considered and improved in the ITD protection.

2.4.3. Phase-to-Phase Fault. Likewise, the effect of the fault impedance and infeed on the measured impedance during a phase-to-phase fault can be mitigated by using the similar method mentioned above.

The proposed ITD protection can detect different types of faults with less operating time to improve the reliability of the network. The communication-assisted method can mitigate the fault impedance and the DG infeed effect by utilising the remote-local current ratio. The new ITD protection can solve the problems aforementioned at the ending of Section 1, and it is effective in both grid-connected and islanded modes. A protection scheme involving the proposed ITD protection is presented in the next section.

3. The Proposed Protection Scheme

The proposed ITD protection can be used for medium-voltage AC microgrids either in grid-connected or islanded mode. A protection scheme involving new protection
methods has been established, and the schematic diagram is presented in Figure 7.

The communication channel is checked firstly, and voltage and current signals are transmitted between the related relays. Then, the remote-local current ratio is calculated by the digital relay to mitigate the fault impedance and DG infeed effect on the measured impedance. When the calculated impedance meets the primary protection requirement shown in (1), the fault would be detected and isolated with a less dependent time. The backup protection responds in the case when the primary protection fails to isolate the fault and the backup protection requirement is met. It is noted that the proposed method needs a robust communication system to transmit signals between the related relays. The communication channel time is discussed and compared with the relay operating time as the following. For the lengths of feeders that are normally short in microgrids, pilot wires, optical fibres, or Ethernet can be used to transmit signals. When the distance is less than 5 kilometers, the signal transmission takes less than 10 milliseconds [37], which is sufficient for ITD protection. On the other hand, the time-synchronised measurements are unnecessary for the proposed ITD protection, which means the proposed method is much more economical and convenient than the differential protection.

If a failure occurs in the communication system, the protection scheme transforms into other methods estimating the fault impedance without communication. Xu et al. presented the angle of the negative-sequence current distribution factor varying little during the fault and proposed an estimation algorithm to calculate the fault impedance both in phase-to-ground faults and phase-to-phase faults [38, 39]. These estimation methods can be used to calculate the fault impedance in the case when the communication system fails to respond.

4. Case Study

In order to investigate the effectiveness of the proposed method, a medium-voltage AC microgrid shown in Figure 8 was simulated in MATLAB Simulink.
The voltage level of the microgrid is 11 kV rms, and four DGs are interconnected with the network via coupling transformers. The output power of IIDG 1, IIDG 2, IIDG3, and SMDG are 0.50, 0.50, 0.40, and 0.80 MW, respectively. IIDGs are selected as photovoltaic generators. Feeders AB, BC, CD, AE, and BE are all same type overhead lines with a length of 3.0, 1.5, 2.0, 2.0, and 3.0 km, respectively. The resistance and reactance per unit length of the overhead line are 0.17 Ω/km and 0.38 Ω/km, respectively. The load connected at each bus is 0.4 MW to 0.8 MW with a power factor of 0.85 lagging. Relays represented as relay 1 to relay 10 are implemented at each side of the feeders, which involve the proposed ITD protection. The locations of the relays are shown in Figure 8 as well as the communication link between the related relays.

### 4.1. Fault Isolation in Islanded Mode

The microgrid can operate in islanded and grid-connected modes, depending on the statue of the common coupling point (PCC). When PCC is open, the microgrid is under the islanded mode. SMDG is under the V-f control strategy, while IIDGs are under the PQ control strategies.

When a single line-to-ground fault occurs at 87.5% of the feeder AB length, the voltages and currents seen by relay 1 and relay 2 are shown in Figures 9 and 10, respectively. It can be seen that the amplitudes of the fault currents are

![Figure 9: Voltages and currents seen by relay 1 during a single line-to-ground fault at 87.5% of the feeder AB.](image)

![Figure 10: Voltages and currents seen by relay 2 during a single line-to-ground fault at 87.5% of the feeder AB.](image)
lower than 50 A, which is the result that the fault currents are provided by DGs and limited by the DG capacity and control strategy.

The fundamental components of the voltage and current are extracted by using the fast Fourier transform (FFT), which are used to obtain the measured impedance. Due to the fault resistance and DG connection, the measured impedance values are inaccurate. To reduce the errors, the mitigating method proposed in this paper is implemented, and the calculated impedance of relay 1 and relay 2 is presented in Figure 11. It can be seen that the impedance values move into the expected zones where the values are $0.446 + j1.303 \Omega$ and $0.064 + j0.143 \Omega$, respectively.

The impedance values are utilised in ITA protection. The simulations of the impedance values of relay 1 and relay 2 are presented in Figure 12. The values of $1.2Z_I/Z_m$ stabilise at 1.398 and 9.638, respectively, after one cycle, and the calculation results are 1.371 and 9.600, respectively, which verifies the correctness of the simulation.

To verify the effectiveness of the proposed method, the phase-to-ground and phase-to-phase faults are both studied. The fault location is selected at 87.5% of the feeder AB length, and the fault resistance is considered. The ITD primary protection is implemented in relay 1 and relay 2, while the backup protection is in relay 1, relay 2, relay 4, relay 8, and relay 9. The simulation results of the relay operating
4.2. Fault Isolation in Grid-Connected Mode. When PCC is close, the microgrid is in the grid-connected mode and all DGs are under the PQ control strategy.

The fault location is selected at 12.5% of the feeder AB length, and different fault types are studied. The primary protection is implemented in relay 1 and relay 2, while the backup protection is in relay 1, relay 2, relay 4, and relay 9. The operation times of relays involving ITA protection are tabulated in Table 3. The average relay operating time of ITD method in the islanded mode were tested, and the results are compared in Figure 13. The \( Z_{fset} \) of zone 1 is \( 0.204 + j0.456 \Omega \) according to [30]. It can be seen that the measured impedance value is \( 0.146 + j0.275 \Omega \) and the value moves into the zone 1 reach when the fault resistance is 0.01 Ω. However, when the fault resistance increases to 50 Ω and 100 Ω, respectively, the measured impedance values move a lot beyond the zone 1 reach and it is inefficient in the HIF detection.

Likewise, faults occurring in the feeder AB in the islanded mode were tested, and the results are compared in Table 3. The average relay operating time of ITD method is 0.054 s, and the average relay operating time of the method proposed in [30] is 0.191 s. The ITD protection performs much better than the method proposed in [30].

The ITD protection proposed in this paper is compared with some prior-art methods, and the comparisons are presented in Table 4 as well. The FRT requirements, the HIF detection, the fault types, and the communication are contained in the comparison. For instance, the FRT requirements are considered in the proposed ITD protection, while they are not mentioned in the methods proposed in [25–27, 29]. The HIFs are studied in the ITD protection, but the HIF detection is inefficient in the methods proposed in [26, 27, 30]. The ITD protection performs better than the prior-art methods listed in Table 4.

4.3. Comparison with the Distance Protection. The ITD protection has been tested and compared with the method proposed in [30]. The FRT requirements were considered, while the HIF detection was inefficient in [30]. When a SLG fault occurs at 50% of the feeder AB, the measured impedances of relay 1 under different scenarios are simulated and presented in Figure 13. The \( Z_{f} \) of zone 1 is 0.204 + j0.456 Ω. However, the HIF detection was inefficient in [30]. When a SLG fault occurs at 50% of the feeder AB, the measured impedances of relay 1 under different scenarios are simulated and presented in Figure 13. The \( Z_{fset} \) of zone 1 is 0.204 + j0.456 Ω. However, the HIF detection was inefficient in [30].

\[
\begin{array}{cccccccccc}
\text{Fault resistance (Ω)} & 0.01 & 50 & 100 & 0.01 & 5 & 10 & 0.01 & 50 & 100 & 0.01 & 5 & 10 \\
\hline
\text{L-G} & 1.015 & 1.049 & 1.146 & 1.015 & 1.012 & 1.015 & 1.014 & 1.015 & 1.015 & 1.014 & 1.015 & 1.015 \\
\text{LL} & 0.001 & 0.002 & 0.003 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 \\
\text{L-L-G} & 0.002 & 0.003 & 0.004 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 \\
\text{LLL} & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 \\
\hline
\end{array}
\]

\[
\begin{array}{cccccccccc}
\text{Fault resistance (Ω)} & 0.01 & 50 & 100 & 0.01 & 5 & 10 & 0.01 & 50 & 100 & 0.01 & 5 & 10 \\
\hline
\text{L-G} & 1.059 & 1.057 & 1.058 & 1.056 & 1.059 & 1.056 & 1.057 & 1.056 & 1.056 & 1.058 & 1.056 & 1.056 \\
\text{LL} & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 \\
\text{L-L-G} & 0.002 & 0.003 & 0.004 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 & 0.002 \\
\text{LLL} & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 & 0.001 \\
\hline
\end{array}
\]
Figure 13: The measured impedance of relay 1 during a SLG fault at 50% of the feeder AB.

Table 3: Relay operating time (s) during a fault occurring in the feeder AB in the islanded mode.

<table>
<thead>
<tr>
<th>Fault location</th>
<th>Fault resistance (Ω)</th>
<th>ITTD method</th>
<th>Method in [30]</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Relay 1</td>
<td>Relay 2</td>
<td>Relay 1</td>
</tr>
<tr>
<td>12.5%</td>
<td>0.01</td>
<td>0.001</td>
<td>0.151</td>
</tr>
<tr>
<td></td>
<td>50</td>
<td>0.003</td>
<td>0.158</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>0.006</td>
<td>0.143</td>
</tr>
<tr>
<td>20.0%</td>
<td>0.01</td>
<td>0.009</td>
<td>0.066</td>
</tr>
<tr>
<td></td>
<td>50</td>
<td>0.006</td>
<td>0.082</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>0.009</td>
<td>0.066</td>
</tr>
<tr>
<td>50.0%</td>
<td>0.01</td>
<td>0.028</td>
<td>0.028</td>
</tr>
<tr>
<td></td>
<td>50</td>
<td>0.032</td>
<td>0.024</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>0.038</td>
<td>0.020</td>
</tr>
<tr>
<td>80.0%</td>
<td>0.01</td>
<td>0.104</td>
<td>0.004</td>
</tr>
<tr>
<td></td>
<td>50</td>
<td>0.110</td>
<td>0.003</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>0.116</td>
<td>0.003</td>
</tr>
<tr>
<td>87.5%</td>
<td>0.01</td>
<td>0.151</td>
<td>0.001</td>
</tr>
<tr>
<td></td>
<td>50</td>
<td>0.152</td>
<td>0.001</td>
</tr>
<tr>
<td></td>
<td>100</td>
<td>0.157</td>
<td>0.001</td>
</tr>
</tbody>
</table>

Table 4: Comparison with some prior-art methods.

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>FRT considered</td>
<td>No</td>
<td>No</td>
<td>No</td>
<td>Yes</td>
<td>No</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>HIF studied</td>
<td>Excellent</td>
<td>Poor</td>
<td>Poor</td>
<td>Average</td>
<td>Average</td>
<td>Poor</td>
<td>Excellent</td>
</tr>
<tr>
<td>Fault types studied</td>
<td>Unbalanced faults</td>
<td>LLL-G faults</td>
<td>All types</td>
<td>All types</td>
<td>All types</td>
<td>All types</td>
<td>All types</td>
</tr>
<tr>
<td>Communication assisted</td>
<td>No</td>
<td>No</td>
<td>Yes</td>
<td>No</td>
<td>Yes</td>
<td>No</td>
<td>Yes</td>
</tr>
</tbody>
</table>
5. Conclusion

The conventional distance protection approaches are ineffective in detecting HIFs in microgrids and rarely consider the FRT requirements. To solve the two problems, a communication-assisted ITD protection has been proposed in this paper, based on the inverse-time characteristics and distance protection. The ITD primary protection can meet the FRT requirements, while the backup protection can be coordinated with a minimum time interval of 0.3 seconds. The communication-assisted method proposed in this paper can reduce the measured impedance error and improve the performance of the protection. The effectiveness of the ITD protection has been verified by time domain simulations. Since the feeders are short and the transmission time is sufficient for the proposed ITD protection, it can be used to detect faults in AC microgrids.

Data Availability

Data will be made available on request.

Conflicts of Interest

The authors declare that they have no conflicts of interest.

Acknowledgments

This work was supported by the National Science Foundation of China (Grant No. U20B2029); the National Key R&D Program Project of China (2023YFC2810902); the Special Scientific Research Foundation of the Education Department of Shaanxi Province, China (No. 23JK0386); the Scientific Research Fund Project of Shaanxi Railway Institute (No. KY2022-20); and the Education and Teaching Research Project of Shaanxi Railway Institute (No. 2021J(G)-22).

References


