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Research Article

Regenerative Braking in Electric Vehicle Using Quadratic Gain Bidirectional Converter

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Received 30 September 2021; Revised 9 December 2021; Accepted 13 December 2021; Published 31 January 2022

Academic Editor: N. Prabaharan

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The manuscript proposes a quadratic gain bidirectional converter (QGBC). The proposed converter is operated in two different stages—step-up (motoring) and step-down (regenerative braking)—which can be employed in electric vehicle (EV) applications. The converter is operated in the continuous inductor current mode (CICM). For regenerative braking (RB) of permanent magnet brushless DC (PMBLDC) motor, the self-inductance of the motor is exploiting to step up the back electromagnetic force (EMF) of the motor to extract the energy even at low rotor speed. The design parameters of the converter are selected as battery voltage $V_s = 48$ V, output voltage $V_o = 200$ V, output power $P_o = 1$ kW, and switching frequency $f_s = 20$ kHz. The design system is simulated using MATLAB/Simulink. Finally, a 1 kW prototype is developed for validation and performance analysis of the converter. The converter operates at maximum efficiency of 95% during step-up operation of the converter. A DSP microcontroller TMS320F28335 is used to control the switches of the converter in the experimental setup.

1. Introduction

The advancement of technology and standards of living continues to enhance. Nowadays, most of the works depend on electrical energy. Many commercial applications, such as uninterrupted power supply, electric vehicles (EVs), and microgrids. Batteries and bidirectional DC-DC converters (BDCs) have the main role for these systems. In recent years, the whole world moving towards the electric vehicle as fossil fuel are devastating and concern of environmental effect. In the vehicle, the motor drive and power electronic converter should be capable of bidirectional energy interference with the battery and motor drive system. A high gain BDC is proposed in [1, 2]. The high gain BDCs are used in electric vehicles (EVs) as they convert low voltage to high voltage and recovery of energy during regenerative braking (RB) which will enhance the driving range of the vehicle. Figure 1 explicate the electrical system of an EV which consists of a battery, BDC, DC-AC converter, electrical machine, and

transmission system. Many high gain converter topologies have been proposed that can be categorized in different ways: isolated and nonisolated circuits. The transformerbased and isolated topology [3–5], a transformer or coupled inductor, is used for high gain. The problem with coupledinductor or transformer has leakage inductance. To overcome the problem of leakage inductance, a snubber circuit is used. The nonisolated BDC topologies [6, 7] are simple in design and controlling without galvanic isolation.

Due to the simple design and without galvanic isolation, the converter size, and low cost, the nonisolated BDC is most preferred. Due to circuit parasitic components, a high duty cycle, and voltage stress on switches, this converter has a restricted voltage gain. To get a high voltage gain, different topologies have been proposed a few years back [8] to reduce voltage stress. A coupled inductor is used, but due to the presence of leakage inductance which increases the voltage stress, additional clamping circuit is used to reduce the voltage stress. A multiport transformerless converter [9, 10]



FIGURE 1: Electrical system of an EVs powertrain.

is for EV application. In this converter, only three switches are used for bidirectional buck-boost operation with dual input and dual output ports, but the voltage gain is same as the conventional boost converter. For the converter for relatively high voltage conversion ratio, there are several coupled-inductor bidirectional converters presented in [11, 12]. In an integrated coupled-inductor converter low ripple current, low voltage stress and soft switching can be achieved by employing phase-shift angle modulation. A dual-switch buck/ boost converter [13] has minimal power loss and excellent efficiency; however, the switch is under a high voltage stress. In [14, 15], a switched quasi-Z-source BDC is presented by alteration in the place of the main power switch in a conventional two-level quasi-Z-source BDC for improvement of conversion efficiency, but it requires more number of the power switch. To get higher voltage gain without wider duty cycle operation, a voltage multiplier circuit (VMC) [16] is proposed. The number of diodes required for 2N-1 combinations for N diode-capacitor VMC and N fold of voltage gain are described in details. A switched-capacitor interleaved [17, 18] bidirectional converter for wide voltage conversion ratio in EVs is presented in this work. In interleaved bidirectional converter number of power switches, components and size are large for high voltage conversion ratio.

There are different resonant converters used for wide voltage gain and higher efficiency [19, 20]. In this fixed frequency, the phase-shift control technique is used, and soft switching is applied for minimum switching loss. There are different topologies of quadratic voltage gain converter proposed in [21, 22]. This quadratic converter has a ripple current on the low voltage side which affects the performance of the battery/supercapacitor. To reduce the ripple current on the low voltage side, a coupled inductor-based quadratic converter [23] is described. In the presence of leakage inductance of coupled inductor, the voltage stress on switches will increase and voltage gain is the same as the quadratic converter. To show the RB, the reversal of power flow from load to source is established. For reversal of power flow, the controlling of the voltage source inverter (VSI) to the power is flowing from load to source. The method for RB of brushless DC motor-based EV is done in [24, 25]. In this RB, we boost the back electromagnetic force (EMF) of PMBLDC machine to DC bus voltage with the

help of self-inductance of PMBLDC machine. Another technique for controlling VSI during RB is described in [26]. In this, a T-S sliding-mode approach control technique is used for RB in the electric vehicle. A bidirectional buck-boost converterbased permanent magnet DC machine drive system for RB in an EV is presented in [27].

In this paper, a quadratic gain bidirectional converter (QGBC) is connected to a load as shown in Figure 2. The load consists of a VSI fed PMBLDC motor, and the motor is coupled to a pulley and belt. The proposed converter has the following advantages: high voltage gain at a low duty cycle, fewer power switches, a simple construction, and easy control. Section 2 describes the topology and converter's operation, and Section 3 gives the details of converter design. The simulation and experimental results are described in Section 4, and the conclusion of the work is given in Section 5.

2. Topology and Operation of Converter

The proposed QGBC with battery and load is illustrated in Figure 2. Here, V_s is input voltage, V_c is voltage across capacitor C_1 , V_o is output voltage, I_{L1} is inductor current of L_1 , and I_{L2} is inductor current of L_2 . In the proposed converter, it consists of four power switches S_1 , S_2 , S_3 , and S_4 with antiparallel diode. The converter's operation in the step-up switches S_1 and S_2 is controlled, and converter's operation in the step-down switches S_3 and S_4 are controlled. The load on the converter consists of VSI fed PMBLDC Motor with a pulley and belt in the converter's step-up operation.

2.1. Step Up or Motoring Mode Operation. The converter's step-up operation is described in two different stages. In the step-up mode, energy is transferred from battery to load through controlled switches S_1 and S_2 . The working waveform of converter's operation in the step-up mode is described in Figure 3.

Stage I (t_o, t_1) : in this stage, the S_1 and S_2 switches are ON and S_3 and S_4 switches are OFF for the time interval of DT_s . The inductor L_1 current is increasing, and energy is transferred to inductor from battery. The converter's operation in this stage is shown in



FIGURE 2: Proposed quadratic gain bidirectional DC-DC converter.



FIGURE 3: Working waveform of converter's operation in step-up.

Figure 4(a). Capacitor C_o supplies energy to load, inductor L_2 current increases, and capacitor C_1 charges. Stage II (t_1, t_2) : in this stage, all the switches S_1, S_2, S_3 , and S_4 are OFF for time interval $(1 - D)T_s$. The stored energy in inductor L_1 and inductor L_2 is transferred to load. During this interval, both inductors' current decreases. The converter's operation in this stage is shown in Figure 4(b). Upon application of Volt-sec balance principle on inductor L_1 and L_2 , the following equation is obtained:

$$DV_{s} + (1 - D) \left(V_{s} + V_{c} - V_{o} \right) = 0, \tag{1}$$

$$D(V_{c} - V_{o}) + (1 - D)V_{c} = 0,$$
(2)

$$V_o = \frac{V_s}{(1-D)^2}.$$
 (3)



FIGURE 4: Current flowing path during step-up mode operation of the converter. (a) Stage I. (b) Stage II.

By eliminating V_c from equations (1) and (2), the output voltage during step-up operation is obtained in equation (3) quadratic in nature. Voltage across capacitor C_1 is obtained as

$$V_{c} = DV_{o} = \frac{DV_{s}}{(1-D)^{2}}.$$
 (4)

2.2. Step Down or Regenerative Braking Mode Operation. The step-down mode operation of converter is described in two different stages. In RB operation of converter, the energy is transferred from load to battery by controlling switches S_3 and S_4 . The working waveform of the converter during regenerative braking is described in Figure 5.

Stage I (t_o, t_1) : in this stage, the switches S_3 and S_4 are ON and switches S_1 and S_2 are OFF during the time interval DT_s . During this interval, both inductors' current increases, and energy is transferred from higher voltage to lower voltage. The converter's operation in this stage is shown in Figure 6(a).

Stage II (t_1, t_2) : in this stage, all switches S_1, S_2, S_3 , and S_4 are OFF during the interval $(1 - D)T_s$. During this interval, the stored energy in inductor L_1 is transferred to load and the energy stored in inductor L_2 is transferred to capacitor C_1 . The converter's operation in this stage is shown in Figure 6(b).

$$D(V_o - V_c - V_s) + (1 - D)(-V_s) = 0,$$
(5)

$$-DV_{c} + (1 - D)(V_{o} - V_{c}) = 0, (6)$$

$$V_s = D^2 V_o. (7)$$

By eliminating V_c from equations (4) and (5), the output voltage of the converter during the step-down mode is obtained in equation (6) quadratic in nature.

2.3. Working of VSI during Regenerative Braking of PMBLDC Motor. In the RB (step-down), the energy has to flow from the PMBLDC machine to the battery. During RB, the PMBLDC machine acts as a generator, and kinetic energy stored in the vehicle can be transferred to the battery by reversal of current flow. Only by controlling the converter can it not siphon the mechanical energy from the PMBLDC machine, and the machine is needed to be operated in the II quadrant. Instead of direct rectification, a back EMF boosting technique is applied in this work during regenerative braking. The self inductances of the PMBLDC machine are charged by shorting all three phases together. All the switches on the high side are turned OFF, and switches on the low side are turn ON and OFF together. When all the switches are on the low side, the energy is stored in the self-inductance of the PMBLDC machine, and all switches are OFF; the stored energy in these inductances is transferred to the converter's output capacitor (C_o). The equivalent circuit of two active phases of the VSI braking and switching PWM of VSI during regenerative phase is shown in Figures 7(a) and 7(b), respectively.

3. Converter Design and Stability Analysis

3.1. Converter Design Consideration. The converter design is done as per its step-up and step-down operations, and the converter operates in boundary condition mode for step-up. In the boundary condition mode, the minimum value of inductor L_2 is zero at the beginning of stage I. The working waveform of the converter in boundary condition is shown in Figure 8(a). In boundary condition operation, the current in inductor L_1 is continuous. The inductor's computation is dependent on the current ripple.

Let us consider the current in inductor L_2 is i_{L2} ; thus, the change in current is described as

$$\frac{\mathrm{d}i_{L2}}{\mathrm{d}t} = \frac{V_{L2}}{L_2}.$$
(8)

Assuming a linear variation and consider $i_{L2_{min}} = 0$, the following equation is obtained:

$$\frac{\Delta i_{L2}}{\Delta t} = \frac{i_{L2\text{max}} - i_{L2\text{min}}}{\Delta t} = \frac{i_{L2\text{max}}}{\Delta t} = \frac{V_{L2}}{L_2}.$$
(9)

When the switch S_2 is turned ON, considering $\Delta t = DT_s$, $T_s = 1/f_s$, and $V_{L2} = V_o - V_c$, the maximum current of inductor L_2 is given as

$$i_{L2\max} = \frac{V_0 - V_c}{L_2 f_s} D.$$
 (10)



FIGURE 5: Working waveform of converter in step-down operation.



FIGURE 6: Current flowing path during step-down mode operation of the converter. (a) Step I. (b) Step II.

To avoid the boundary condition mode, it must satisfy these conditions:

$$i_{L2\min} \ge 0 \Longrightarrow I_{L2\min} \ge I_{L2avg} - \frac{i_{L2\max}}{2}.$$
 (11)

Substituting equations (3), (4), and (11) into equation (10), the critical value of inductor L_2 is a function of duty cycle, D

$$L_2 \ge \frac{V_o}{2I_{L2avg}f_s} (1-D)D.$$
 (12)

Assuming constant output voltage V_o and switching frequency f_s , the value of duty cycle (*D*) which gives maximum value of L_2 . The value of *D* is obtained by calculating first derivative of equation (12) with respect to *D* and equals to zero, the *D* is obtained as 1/2. Therefore, the value of L_2 is given as



FIGURE 7: (a) Equivalent circuit consisting of two active phases of VSI during regenerative braking. (b) Switching signal of VSI during regenerative braking.



FIGURE 8: Working waveform of converter in boundary condition mode during step-up operation. (a) Boundary condition. (b) Discontinuous conduction.

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$$L_2 \ge \frac{V_o}{8I_{L2avg}f_s}.$$
 (13)

Similiar methods can be used to calculate the value of inductor L_1 . The critical value of inductor L_1 can be obtained as

$$L_1 \ge \frac{V_o}{2I_{Llavg} f_s} D (1 - D)^2.$$
(14)

The value of *D* is obtained to maximize L_1 by calculating the first derivative of equation (14) with respect to *D* and equals to zero. Then, the value of *D* is obtained as 1/3.

$$L_1 \ge \frac{2V_o}{27I_{L1avg}f_s}.$$
(15)

Consider linear variation, capacitor C_1 can be calculated based on output current.

$$I_o = C_1 \frac{\Delta V_o}{\Delta t},\tag{16}$$

$$I_o = I_{L1} (1 - D)^2 = I_{L2} (1 - D).$$
(17)

When switch S_2 is turned ON, the value of $\Delta t = DT_s$, $T_s = 1/f_s$, and using equation (17), the capacitor C_1 is calculated as

$$C_1 = \frac{DI_o}{\Delta V_o f_s} = \frac{I_{Llavg}}{\Delta V_o f_s} D \left(1 - D\right)^2.$$
(18)

The value of D is obtained to maximize C_1 by calculating the first derivative of equation (18) with respect to D and equals to zero. Then, the value of D is obtained as 1/3.

$$C_1 = \frac{4I_{L1avg}}{27\Delta V_o f_s}.$$
(19)

The value of capacitor C_o is calculated as

$$I_{co} = C_o \frac{\Delta V_{co}}{\delta t},\tag{20}$$

$$I_{L2} - I_o = C_o \frac{\Delta V_o}{(1 - D)T_s},$$
(21)

$$C_o = \frac{I_{L1avg}}{\Delta V_o f_s} D(1-D)^2.$$
(22)

The value of duty cycle D is obtained to maximize C_o by calculating the first derivative of equation (22) with respect to D and equals to zero. Then, the value of D is obtained as 1/3.

$$C_o = \frac{4I_{L1avg}}{27\Delta V_o f_s}.$$
(23)

The voltage stress on switches S_1 , S_2 , S_3 , and S_4 during step-up and step-down operation of converter are obtained as

$$V_{S1} = V_{S3} = \frac{DV_s}{(1-D)},$$

$$V_{S2} = V_{S4} = \frac{V_s}{(1-D)^2}.$$
(24)

The different stages for the discontinuous conduction mode (DCM) in step-up operation are explained as follows. In DCM, there are four stages, the first stage is alike to CCM and no need to be explained it again. In second stage, when all the energy stored in L_2 is transferred to load and current through D_4 become zero at t_2 before next switch S_2 is ON. In the third stage, all the energy stored in L_1 is transferred to capacitor C_1 and current through D_3 becomes zero at t_3 before switch S_1 is ON. Figure 8(b) shows the working waveform of the four stages in DCM. As per this, at steady state, the average diode current i_{D4} over one switching period is equal to $i_{L2,avg}$.

$$I_o = i_{D4,\text{avg}} = \frac{i_{L2,\max}}{2} D_1,$$
 (25)

The voltage across L_1 and L_2 considering step-up and DCM operation

$$DV_{s} + (1 - D)(V_{s} + V_{c} - V_{o}) = 0,$$

$$D(V_{c} - V_{o}) + D_{1}V_{C} = 0.$$
(26)

The voltage gain in DCM is calculated as

$$\frac{V_o}{V_s} = \frac{(D+D_1)}{(D+D_1)(1-D) + D(D-1)}.$$
(27)

The equivalent load resistance as R_o and substituting (17) into (25), and using (25), the duty D_1 is given by

$$D_1 = \frac{2L_2 f_s (1-D)}{R_o D}.$$
 (28)

From working waveform of Figure 8(b), it is observed from the DCM operation that it is required that

$$D_1 < (1 - D).$$
 (29)

Thus, the critical value of the duty cycle to avoid the converter operates in DCM is given by

$$D > \frac{2L_2 f_s}{R_o}.$$
 (30)

3.2. Stability Analysis. The stability of any system is defined as the ability to produce bounded output when a bounded input is applied to it. The general state space representation of a system is given by

$$\dot{x}(t) = Ax(t) + Bu(t),$$

$$y(t) = Cx(t) + Du(t).$$
(31)

The state space equation of the converter in step-up for ON and OFF condition is

TABLE 1: Converter parameters.

Parameter	Value
Po	1 kW
V	200 V
f_s	20 kHz
L_1	0.37 mH
L_2	1.25 mH
$\overline{C_1}$	$47 \mu\text{F}$
C _o	100 µF

$$\dot{x}(t) = A_1 x(t) + B_1 x(t),$$

$$\begin{pmatrix} i_{L1} \\ \\ i_{L2} \\ \\ \dot{v}_{c} \\ \\ v_{co} \end{pmatrix} = \begin{pmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & \frac{1}{L_{2}} & \frac{-1}{L_{2}} \\ \\ 0 & \frac{1}{C_{1}} & 0 & 0 \\ \\ 0 & \frac{-1}{C_{o}} & 0 & \frac{-1}{R_{o}C_{o}} \end{pmatrix} \begin{pmatrix} i_{L1} \\ \\ i_{L2} \\ \\ v_{c} \\ \\ v_{co} \end{pmatrix} + \begin{pmatrix} \frac{1}{L_{1}} \\ 0 \\ \\ 0 \\ \\ 0 \end{pmatrix} (v_{s}),$$

$$\dot{x}(t) = A_2 x(t) + B_2 x(t),$$

$$\begin{pmatrix} i_{L1} \\ \vdots_{L2} \\ v_{c} \\ v_{co} \end{pmatrix} = \begin{pmatrix} 0 & 0 & \frac{1}{L_{1}} & \frac{-1}{L_{1}} \\ 0 & 0 & \frac{-1}{L_{2}} & 0 \\ \frac{-1}{L_{2}} & 1 & 0 & 0 \\ \frac{-1}{C_{1}} & \frac{1}{C_{1}} & 0 & 0 \\ \frac{1}{C_{o}} & 0 & 0 & \frac{-1}{R_{o}C_{o}} \end{pmatrix} \begin{pmatrix} i_{L1} \\ i_{L2} \\ v_{c} \\ v_{co} \end{pmatrix} + \begin{pmatrix} \frac{1}{L_{1}} \\ 0 \\ 0 \\ 0 \end{pmatrix} (v_{s}),$$

$$y(t) = C_{1}x(t) + D_{1}u(t),$$

$$y(t) = C_{2}x(t) + D_{2}u(t),$$

$$(v_{o}) = (0 \ 0 \ 0 \ 1) \begin{pmatrix} i_{L1} \\ i_{L2} \\ v_{c} \\ v_{co} \end{pmatrix} + (0) (v_{s}).$$
(32)

Now, applying the state-space averaging technique,

$$A = A_{1}D + A_{2}(1 - D),$$

$$B = B_{1}D + B_{2}(1 - D),$$

$$A = \begin{pmatrix} 0 & 0 & \frac{(1 - D)}{L_{1}} & \frac{-(1 - D)}{L_{1}} \\ 0 & 0 & \frac{-1}{L_{2}} & \frac{D}{L_{2}} \\ \frac{-(1 - D)}{C_{1}} & \frac{1}{C_{1}} & 0 & 0 \\ \frac{(1 - D)}{C_{0}} & \frac{(-D)}{C_{0}} & 0 & \frac{-1}{R_{0}C_{0}} \end{pmatrix}, \quad (33)$$

$$B = \begin{pmatrix} \frac{1}{L_{1}} \\ 0 \\ 0 \\ 0 \\ 0 \end{pmatrix},$$

$$C = (0 \ 0 \ 0 \ 1),$$

$$D = (0).$$

Small signal model is derived by applying perturbations, and the overall state and output equation of the converter is obtained as2

$$\begin{pmatrix} \tilde{i}_{L1} \\ \tilde{i}_{L2} \\ \tilde{v}_{c} \\ \tilde{v}_{co} \end{pmatrix} = \begin{pmatrix} 0 & 0 & \frac{(1-D)}{L_{1}} & \frac{-(1-D)}{L_{1}} \\ 0 & 0 & \frac{-1}{L_{2}} & \frac{D}{L_{2}} \\ \frac{-(1-D)}{C_{1}} & \frac{1}{C_{1}} & 0 & 0 \\ \frac{(1-D)}{C_{0}} & \frac{-D}{C_{0}} & 0 & \frac{-1}{R_{0}C_{0}} \end{pmatrix} \begin{pmatrix} \tilde{i}_{L1} \\ \tilde{v}_{c} \\ \tilde{v}_{co} \end{pmatrix} \\ + \begin{pmatrix} \frac{v_{co} - v_{c}}{L_{1}} & \frac{1}{L_{1}} \\ \frac{v_{co}}{L_{2}} & 0 \\ \frac{i_{L1}}{C_{1}} & 0 \\ \frac{-(i_{L1} - i_{L2})}{C_{0}} & 0 \end{pmatrix} \begin{pmatrix} \tilde{d} \\ \tilde{v}_{s} \end{pmatrix}.$$

(34)

After substituting all passive elements value from Table 1 and duty cycle D=0.51, duty to output voltage transfer function G(s) is obtained as follows:

$$G(s) = \frac{\widetilde{v_o(s)}}{\widetilde{d(s)}} = \frac{-3.46 \times 10^5 s^3 + 5.38 \times 10^8 s^2 - 5.45 \times 10^{12} s + 2.42 \times 10^{16}}{1.11 s^4 + 2.8 \times 10^2 s^3 + 4.4 \times 10^7 s^2 + 8.6 \times 10^9 s + 2.96 \times 10^{13}}.$$
(35)

From the above transfer function, it is observed that the all the poles and two zeros lie on the left half and one zero lies on the right half of S-plane of in root locus. Transfer function of a PI controller is

$$G_{\rm PI} = \frac{K_p s + K_i}{s}.$$
 (36)

Ziegler–Nichols' closed loop tunning method is used to tune the PI controller, the value of $K_p = 1.93 \times 10^{-4}$ and $K_i = 0.172$ substituted in (36), and therefore, the closed loop transfer function (CLTF) of the converter is obtained as follows:

$$CLTF = \frac{\widetilde{v_o(s)}}{\widetilde{d(s)}} = \frac{-66.91 \, s^4 + 4.42 \times 10^4 s^3 - 9.6 \times 10^8 s^2 + 3.73 \times 10^{12} s + 4.16 \times 10^{15}}{s \left(1.11 s^4 + 2.8 \times 10^2 s^3 + 4.4 \times 10^7 s^2 + 8.6 \times 10^9 s + 2.96 \times 10^{13}\right)}.$$
(37)

The magnitude and phase plot is obtained from CLTF are shown in Figure 9. In magnitude plot, there two updown glitch, and it occurs due to two pair of complex conjugate pole ($P_{1,2} = -26.8 \pm j6243$, $P_{3,4} = -99.1 \pm j821$) and one pair of conjugate zero ($Z_{1,2} = -860 \pm j4544$) in the open loop transfer function G(s) of the converter. From Bode diagram, one can see phase margin (PM = 40.8°) and gain margin (GM = 2.81 dB), so it can say that the system is stable.

4. Computational and Experimental Results

4.1. Computational Results. The bidirectional quadratic converter along with VSI for the PMBLDC machine is simulated in MATLAB/Simulink. The vehicular load is simulated through a pulley and belt during motoring operation and an inertial load of 0.1 kg·m² during regenerative braking operation. The PMBLDC machine having parameters is given in Table 2. The proposed converter is simulated and designed for 1 kW power at an output voltage of 200 V with battery voltage at 48 V and switching frequency of 20 kHz. The DC-DC converter and inverter switching PWM generated in MATLAB simulink are illustrated in Figures 10(a) and 10(b), respectively. The parameters of the converter are calculated, and its value is given in Table 1. The type of battery used in experimental setup and its parameters is given in Table 3.

The system is simulated for 5 s with 0 s to 3 s in step-up (motoring) and 3 s to 5 s in the step-down (RB) mode. The steady-state inductor's current in the converter's step-up operation is shown in Figure 11(a), and the step-down operation is shown in Figure 11(b). In step up, when switches S_1 and S_2 are ON, both inductor's current increases and OFF both inductor's current decreases. Similarly, in step down, when switches S_3 and S_4 are ON, both inductor's current increases, current increases, and when it is OFF, both inductor's current decreases and both inductor current is negative. The

steady-state output voltage V_o , capacitor C_1 , capacitor voltage V_c , and battery voltage V_s during step-up operation of converter are shown in Figure 12. The battery voltage $V_s = 48$ V, capacitor voltage $V_c = 102$ V, and output $V_o = 220$ V at duty cycle 51% can be seen in Figure 12.

The voltage stress on switches S_1 , S_2 , S_3 , and S_4 during converter's step-up operation are shown in Figure 13. $V_{s1} = V_{s3} = 100$ V are voltage stresses on switches S_1 and S_3 , respectively, and voltage stress on switch S_2 and S_4 are $V_{s2} = V_{s4} = 200$ V (Figures 13(a) and 13(b)). The speed characteristics of PMBLDC machine during motoring mode from time 0 s to 3 s are illustrated in Figure 14. From graphical descriptions, the speed of motor reaches up to 3600 rpm at time 3 s. The RB is applied to reduce the motor speed from 3600 rpm to 500 rpm as described in Figure 14.

The stator current of PMBLDC machine when it acts as motor can be seen in Figure 15(a) and when it act as generator can be seen in Figure 15(b).

4.2. Hardware Results. A model of the proposed converter is designed and developed for the experimental validation as shown in Figure 16. The converter component's values used in the prototype are the same as chosen in simulation parameters.

In this paper, the control strategy is realized by using the TMS320F28335 DSP microcontroller for proposed converter and driving PMBLDC machine in both motoring and RB. A PMBLDC machine coupled with a flywheel is used to emulate the RB action. To reduce the impulse torque condition and safety of the system, a pulley belt system is employed for mechanical coupling of the motor and inertial load. Figure 17 shows the system voltages in steady-state, battery voltage $V_s = 50$ V, capacitor voltage $V_c = 100$ V, and output voltage $V_a = 200$ V in step-up operation of converter.



FIGURE 9: Magnitude and phase plot is obtained from CLTF.

TABLE	2:	Motor	parameters.
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FIGURE 10: Switching signal of DC-DC converter and inverter in MATLAB Simulink. (a) Switching PWM of DC-DC converter. (b) Switching PWM of inverter.

The steady-state inductors current I_{L1} and I_{L2} during step-up operation of converter are shown in Figure 18. In step-up operation of converter, the steady-state inductor L_1 current $I_{L1} = 10.5$ A and inductor L_2 current $I_{L2} = 5.25$ A can be seen in Figure 18. The inductor current at discontinuous condition mode and boundary condition mode is shown in

TABLE 3:	Battery	parameters.
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Parameter	Value
Туре	Li-NMC
Open circuit voltage	50.5 V
% SOC	90%
Ah rating	25 Ah
Nominal discharge current	10 A



FIGURE 11: Inductor's current during (a) step-up and (b) step-down operation of the converter.



FIGURE 12: Steady-state voltages in converter's step-up operation.



FIGURE 13: Illustration of voltage stress on all switches. (a) Voltage stress on switch S_1 and S_2 . (b) Voltage stress on switch S_3 and S_4 .



FIGURE 14: PMBLDC machine speed characteristics.

Figures 19(a) and 19(b), respectively. The PMBLDC machine is coupled with pulley, and in order to apply load torque, a belt which is connected through a digital weighing machine at the

both end. The steady-state three phase stator current of PMBLDC machine in motoring mode during converter's step-up operation is shown in Figure 20.



FIGURE 15: Illustration of current of the stator of PMBLDC machine. (a) When act as motor. (b) When act as generator.



FIGURE 16: Experimental setup.

A load torque of 1.5 N-M is applied on PMBLDC machine through belt, and all three phase currents $I_a = I_b = I_c = 4$ A are flowing which is shown in Figure 20.

The voltage stress on switches S_1 and S_2 are shown in Figure 21 and switches S_3 and S_4 are shown in Figure 22 during converter's step-up operation. In the step-down operation of the converter, the PMBLDC machine is loaded with a flywheel to show the power flow from load to battery during RB. A physical mode transition switched is used to change the mode of operation from motoring to RB and vice-versa. When RB is applied



FIGURE 17: Plot of system battery voltage (channel-2), capacitor voltage (channel-3), and output voltage (channel-1) during step-up operation.

through the mode transition switch, the change in the inductor's current and output voltage of the converter is shown in Figure 23.



FIGURE 18: Plot of steady-state inductor current I_{L1} (channel-4) and I_{L2} (channel-3) during step-up operation.



FIGURE 19: Inductor's current in (a) discontinuous conduction mode and (b) boundary condition mode. (a) Inductor's current I_{L1} (channel-3), I_{L2} (channel-4), and output voltage V_o (channel-1). (b) Inductor's current I_{L1} (channel-3), I_{L2} (channel-4), and output voltage V_o (channel-1).

Figure 24 show the inductor L_1 and L_2 current during step-down operation of converter, and it is negative which means power is flowing from load to source. The steady-state inductor L_1 current $I_{L1} = -3$ A and inductor L_2 current $I_{L2} =$ -1.5 A during RB of PMBLDC machine and converter operate in step-down. The stator current of PMBLDC motor during RB is shown in Figure 25. A compression of different BDCs is done in Table 4. Efficiency curve of the converter in step-up and step-down operation is shown in Figures 26 and 27, respectively.



FIGURE 20: Steady-state three phase stator current of PMBLDC machine in motoring mode.



FIGURE 21: Plot of voltage stress on switches $V_{\rm s1}$ (channel-2) and $V_{\rm s2}$ (channel-1).



FIGURE 22: Plot of voltage stress on switches $V_{\rm s3}$ (channel-2) and $V_{\rm s4}$ (channel-1).



FIGURE 23: Inductor current I_{L1} (channel-3), I_{L2} (channel-4), and output voltage V_o (channel-1) when RB is applied.



FIGURE 24: Inductor's current I_{L1} (channel-3) and I_{L2} (channel-4) during step-down operation of converter.



FIGURE 25: Three phase stator current I_a (channel-2), I_b (channel-3), I_c (channel-4), and V_o (channel-1) during regenerative braking.

converters.
bidirectional
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Comparision
4
TABLE

	Maximum efficiency (%)	96.9	95.3	93	06	95
	Voltage stress on power switches	$(V_L/(1-D))(V_L/(1-D)^2)(NV_L/(1-D)^2)$ Where $N = N_2/N_1$	Ι	$(1+n)V_{H}/(1+n D) \& V_{H}/(1+n D)$	$V_L^{\prime}(1-D)^2$	$(V_L/(1-D))(V_L/(1-D)^2)$
ision of different bidirectional converters.	Voltage ratio buck	$(1 - D)^2 / N$	$(V_{01}/V_3) = (n D/2)(V_{02}/V_3) = (n D/2(1 - D))$	(1 - D)/2(1 + n D + k D)	D^2	D^2
TABLE 4: Compar	Voltage ratio boost	$N/(1-D)^2$	$(V_{02}/V_1) = (1/(1-D))(V_{03}/V_1) = (n/(1-D)^2)$	2(1 + n D + k D)/(1 - D)	$1/(1-D)^2$	$1/(1-D)^2$
	Total components	1 inductor; 1 transformer; 3 capacitors; 6 power switches 2 inductore: 1	coupled inductor; 4 capacitors; 1 power switch; 1 power diode	4-winding coupled inductors; 3 capacitors; 4 power switches	 coupled inductor; 3 capacitors; 4 power switches 	2 inductors; 2 capacitors; 4 power switches
	Reference	Converter in [4]	Converter in [5]	Converter in [6]	Converter in [23]	Proposed converter



FIGURE 26: Converter efficiency during step-up operation at output voltage $V_o = 200$ V and input voltage $V_s = 48$ V.



FIGURE 27: Converter efficiency during step-down operation at output voltage $V_o = 50$ V and input voltage $V_s = 200$ V.

5. Conclusion

A QGBC is designed, developed, and tested for RB application in EV. The power flow direction is controlled successfully by changing the working mode of the VSI and the QGBC. In the regenerative braking mode, the inertial load's mechanical energy is changed to electrical energy and fed back to the battery, as evident from the results. A control strategy is implemented to boost the back EMF of the PMBLDC motor by controlling the VSI and using the selfinductance of the motor. The quadratic gain bidirectional DC-DC converter operates at maximum efficiency of 95% at an output voltage of 200 V during the converter's step-up operation. The implemented strategy and the system configuration present a frugal and practical approach to eliminate the drawbacks of regenerative braking in the buck mode of BDC.

Abbreviations

V_1 :	Input voltage
V_o :	Output voltage
V_{c1} :	Voltage across capacitor C_1
$D_{1,2}$:	Diode
$L_{1,2}$:	Inductor
$C_{1,o}$:	Capacitor
<i>S</i> _{1,2,3,4} :	Switch
D:	Duty ratio
$I_{L1,2}$:	Inductor current
$I_{c1,co}$:	Capacitor current
J:	Moment of inertia
$T_{1,2,3,4,5,6}$:	Transistor
F_s :	Switching frequency
T_{on} :	ON time
RB:	Regenerative braking

$E_{\rm vs}$:	Electric vehicles
$T_s:$	Switching time
PMBLDC:	Permanent magnet brushless DC
QGBC:	Quadratic gain bidirectional converter
VSI:	Voltage source inverter
EMF:	Electromotive force
ICE:	Internal combustion engine
BDCs:	Bidirectional DC to DC converters
CICM:	Continuous inductor current mode.

Data Availability

The data will be available on request to Prof. R. K. Saket (rksaket.eee@iitbhu.ac.in).

Conflicts of Interest

The authors declare that they have no conflicts of interest.

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