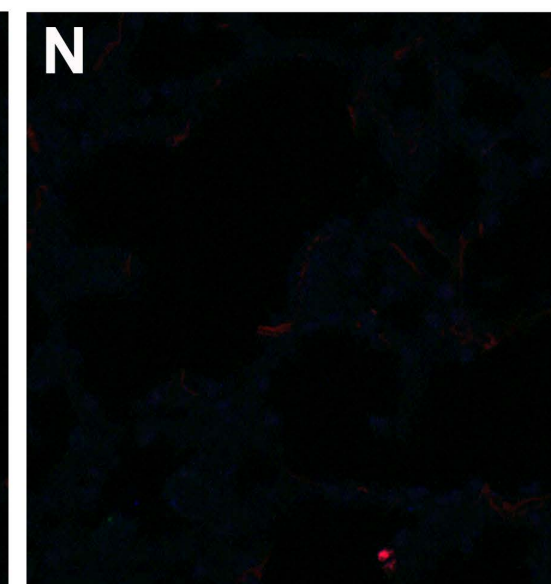
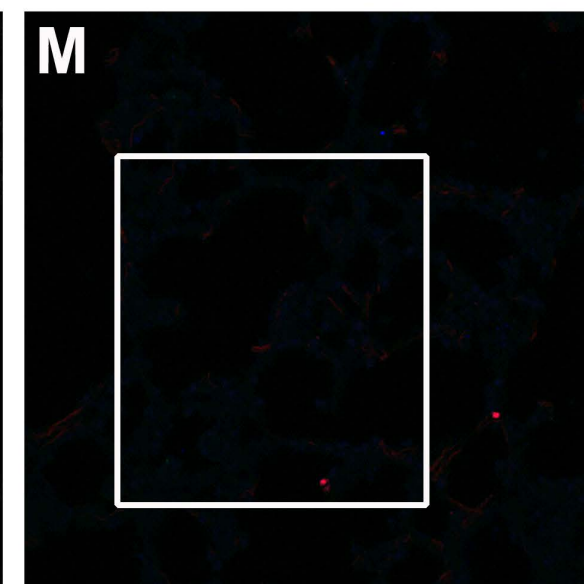
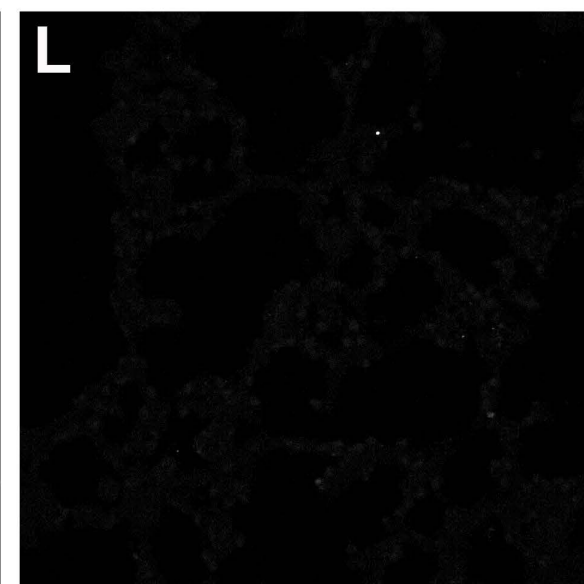
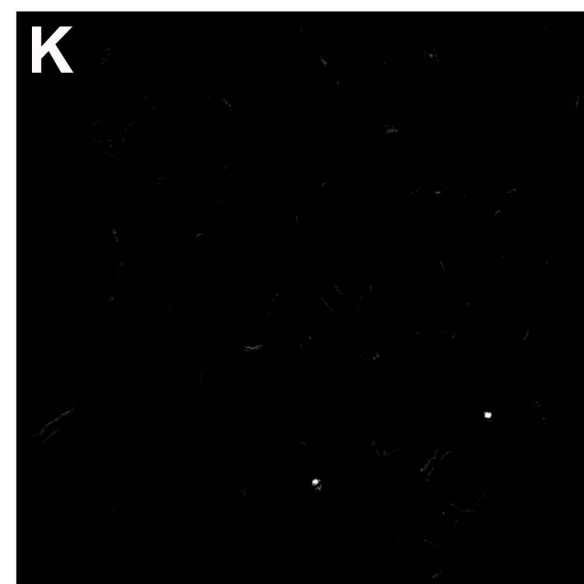
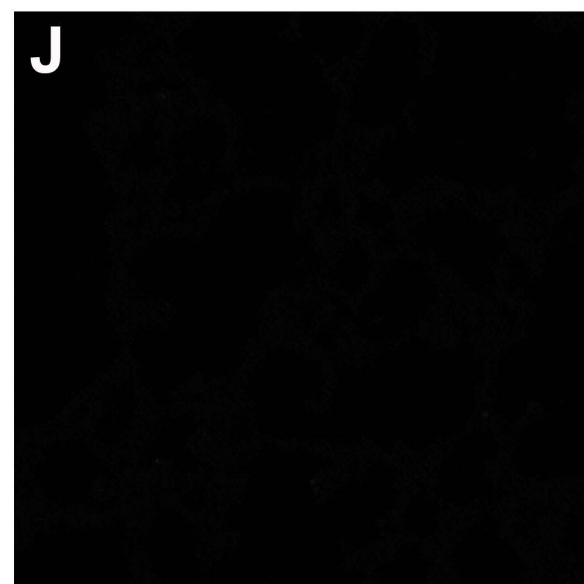
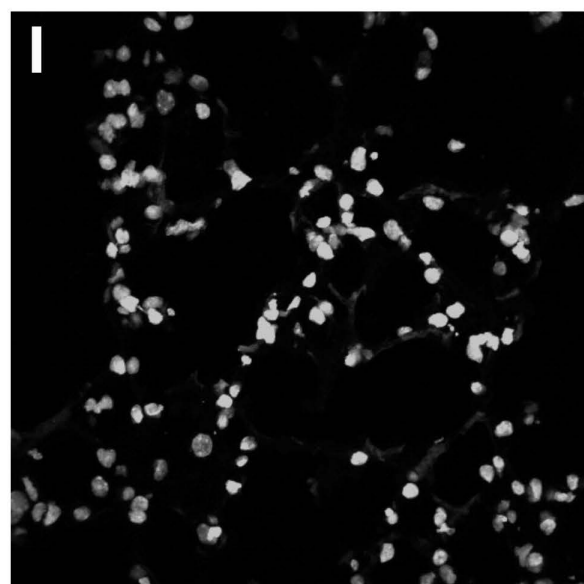
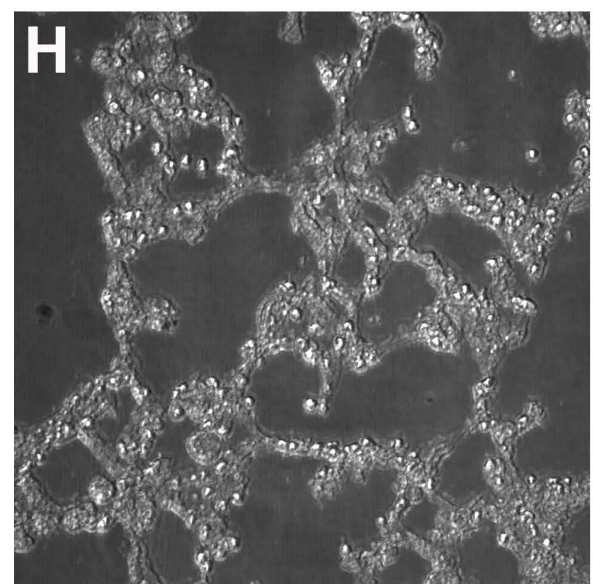
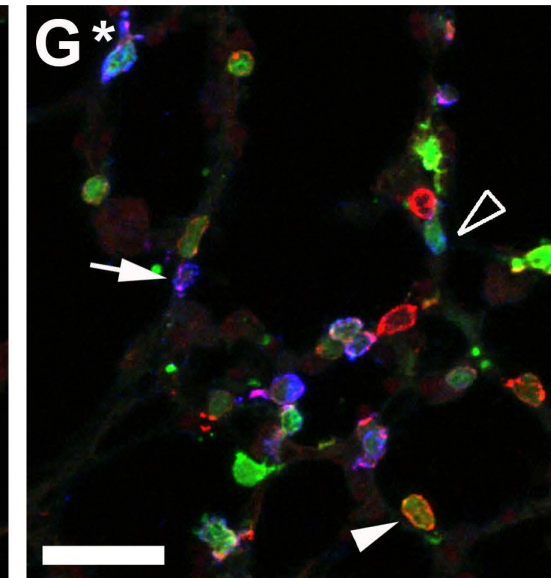
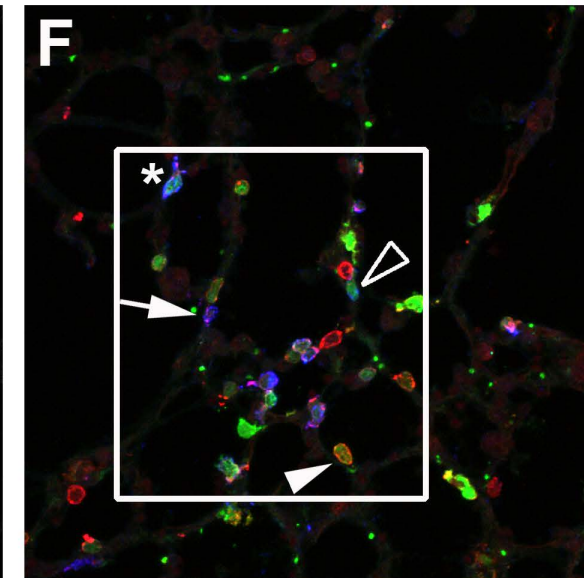
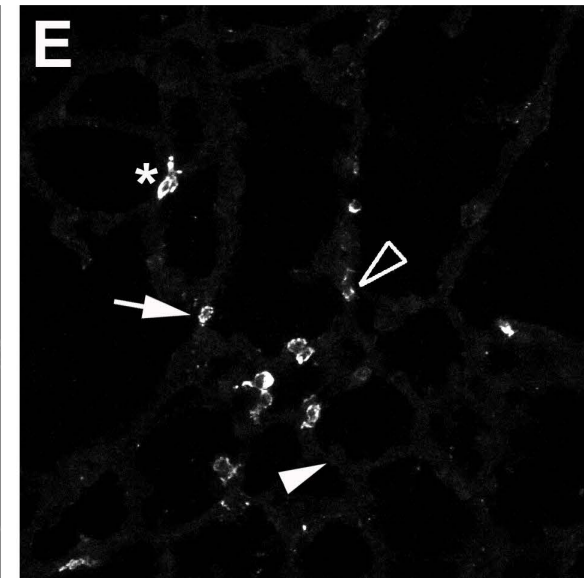
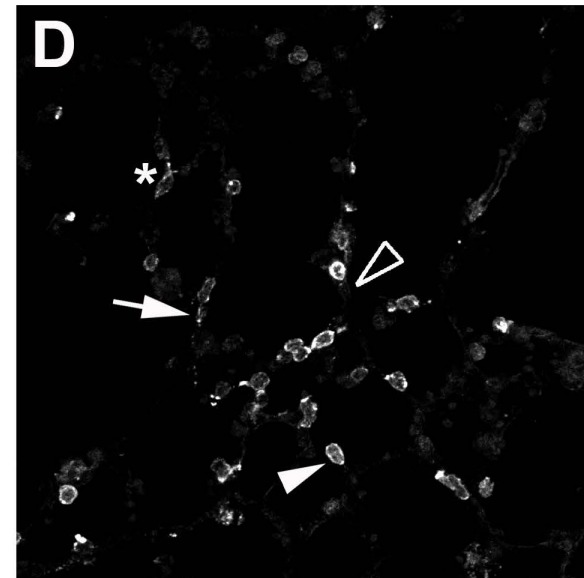
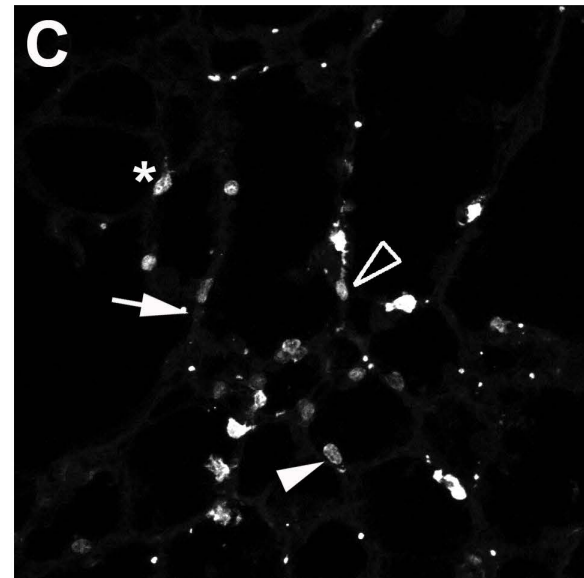
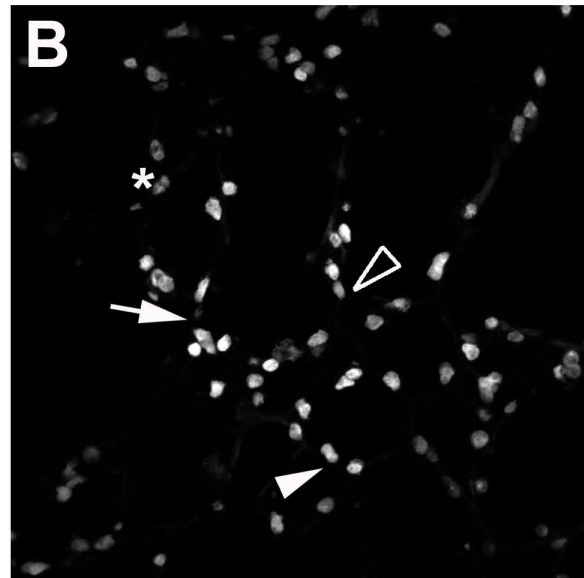
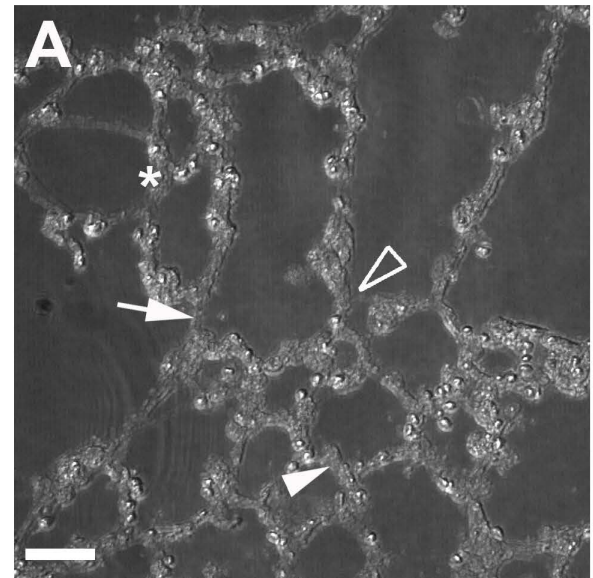
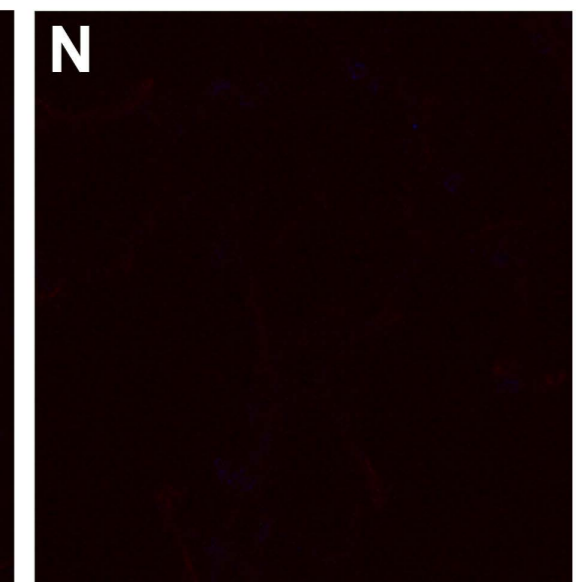
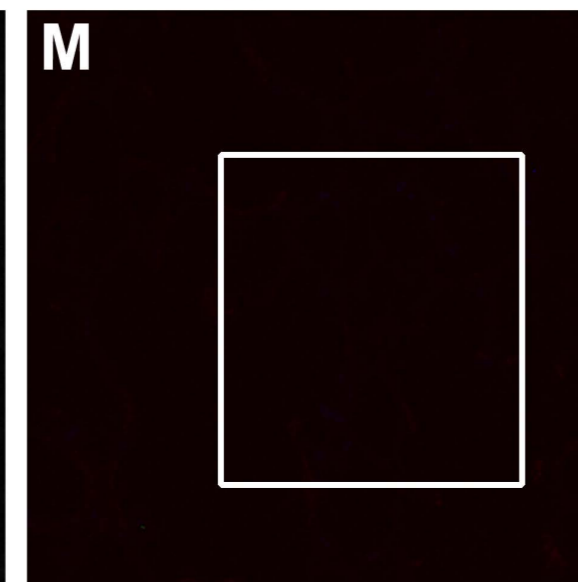
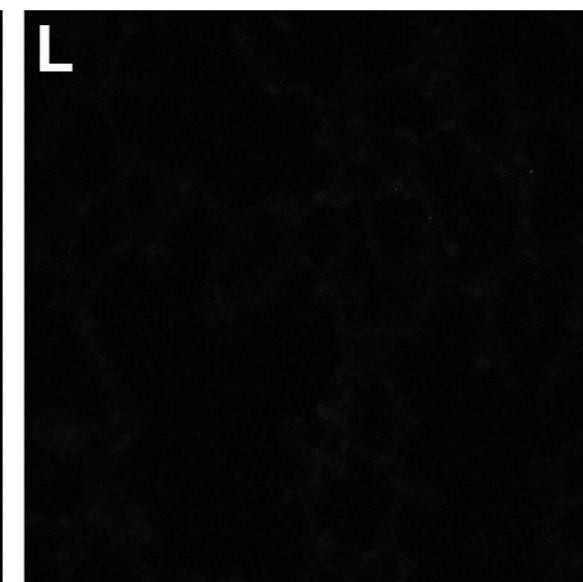
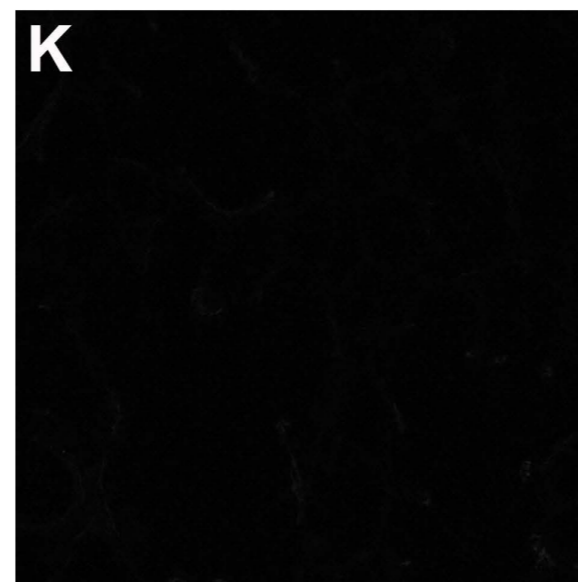
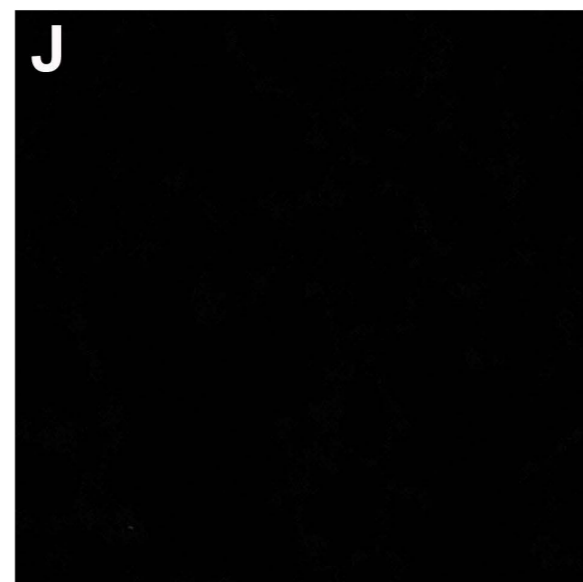
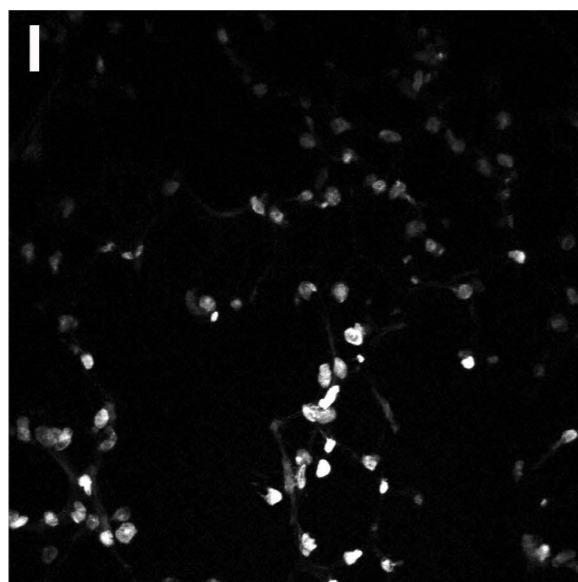
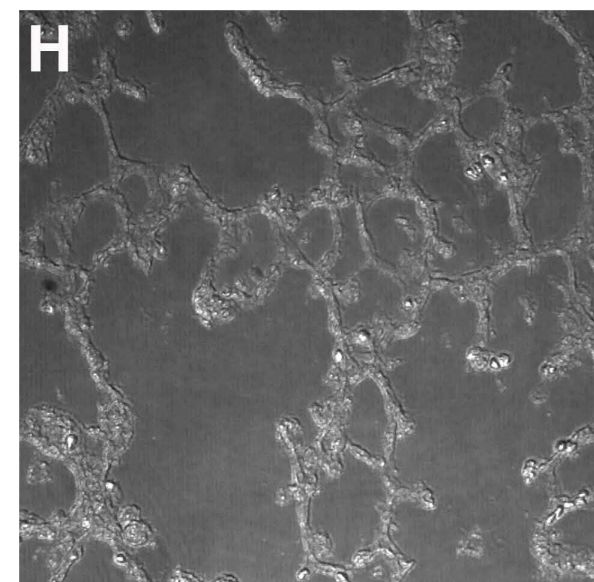
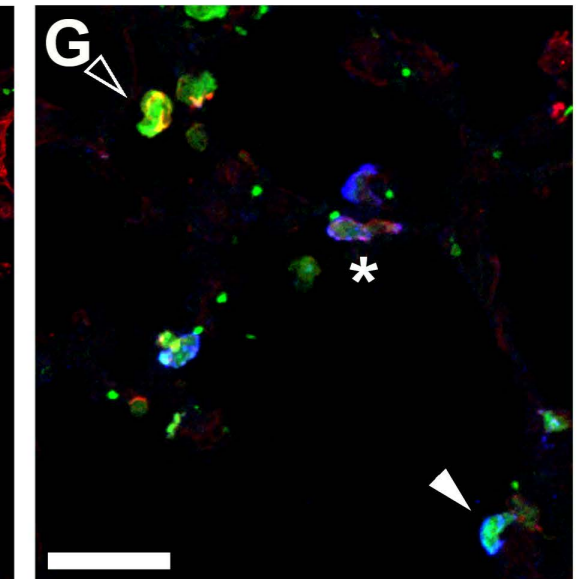
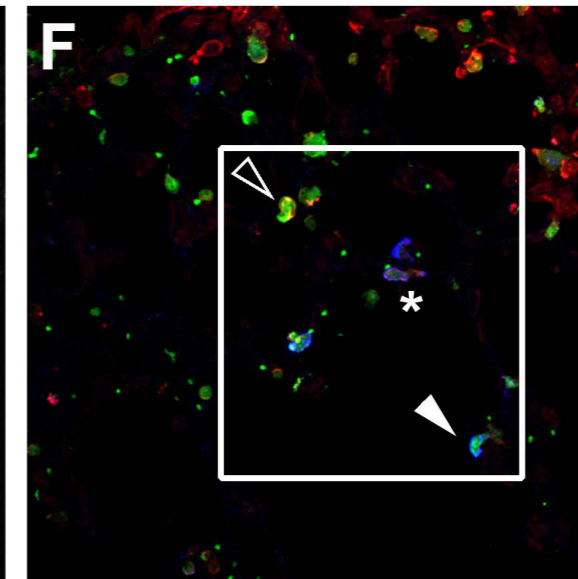
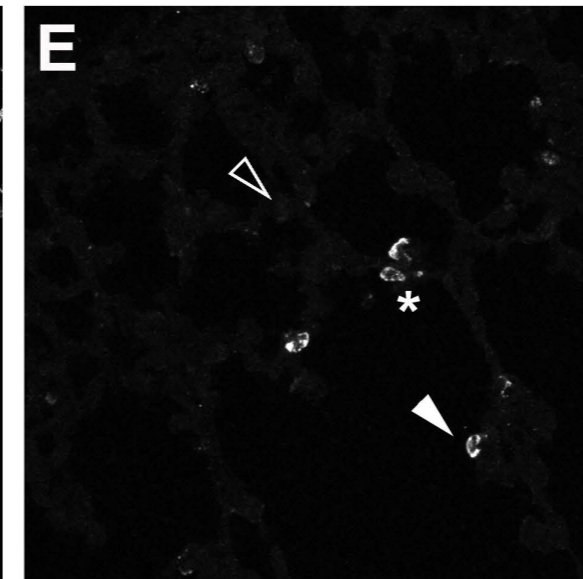
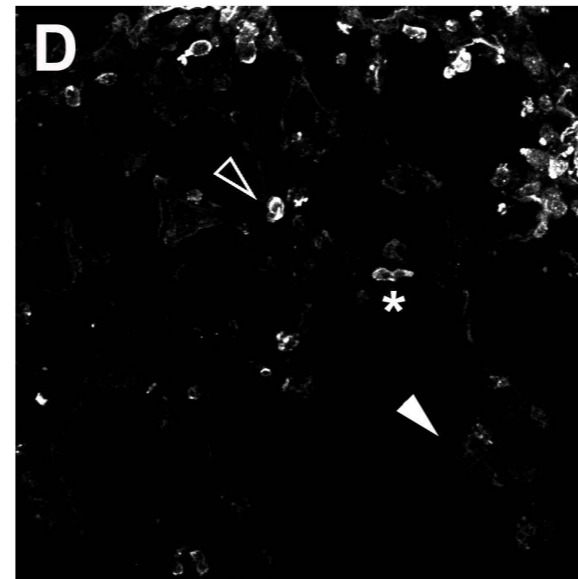
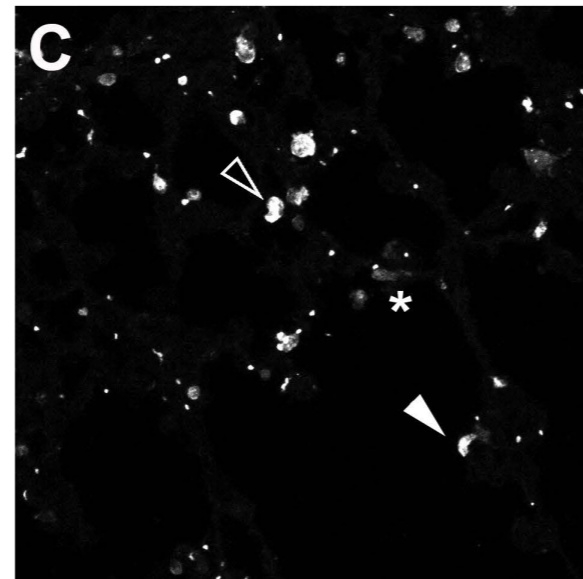
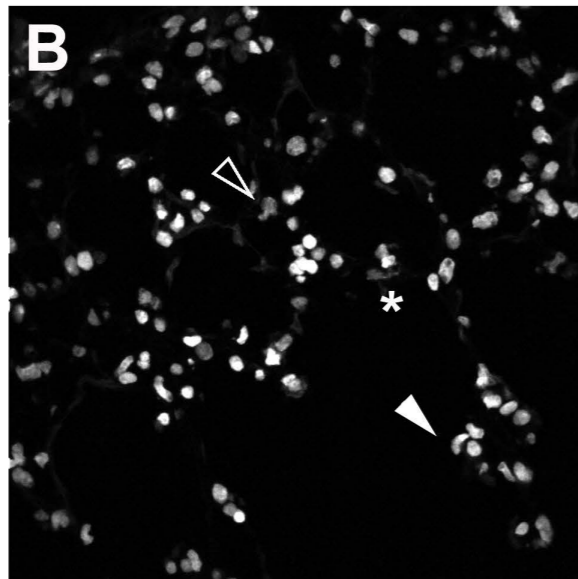
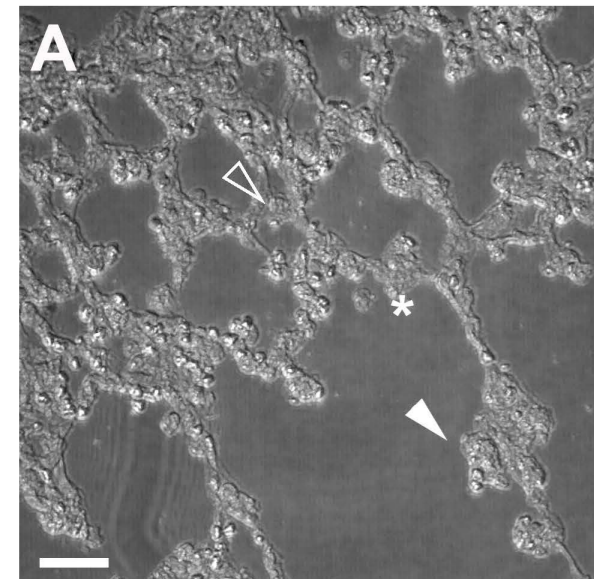
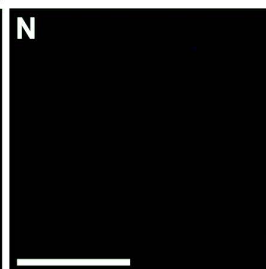
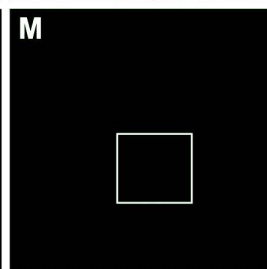
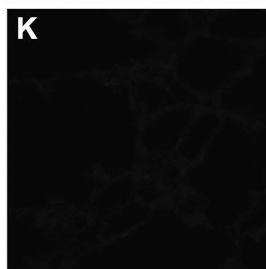
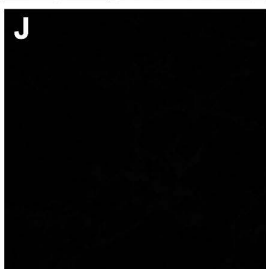
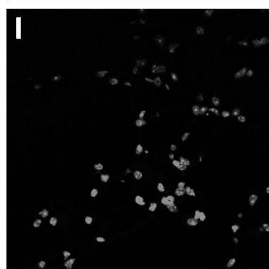
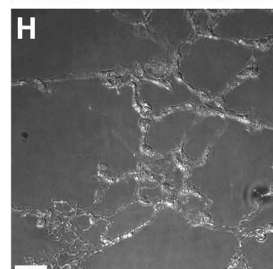
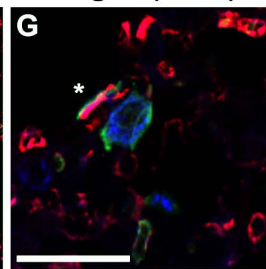
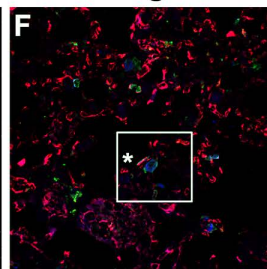
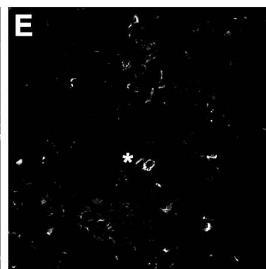
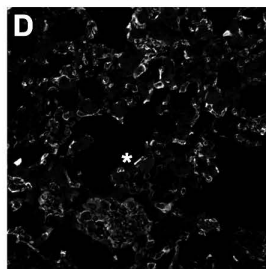
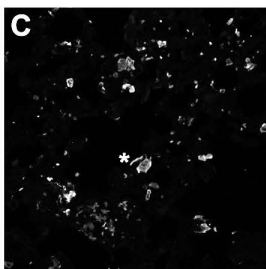
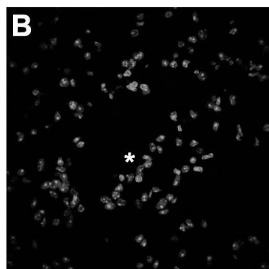
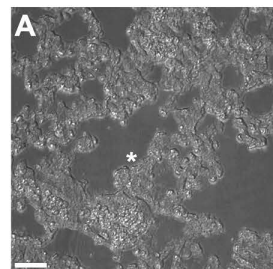


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DIC**HO****GFP****COL I****DDR2****Merged****Merged (Inset)**

DIC**HO****GFP****COL I****DDR2****Merged****Merged (Inset)**

DIC**HO****CD45****DDR2****Merged****Merged (Inset)**