We present communication frameworks, models, and protocols of smart grid Internet of Things (IoT) networks based on the IEEE and IEC standards. The measurement, control, and monitoring of grid being achieved through phasor measurement unit (PMU) based wide area measurement (WAM) framework. The WAM framework applied the IEEE standard C37.118 phasor exchange protocol to collect grid data from various substation devices. The existing frameworks include the IEC 61850 protocol and programmable logic controllers (PLCs) based supervisory control and data acquisition (SCADA) system. These protocols have been selected as per the smart grid configuration and communication design. However, the existing frameworks have severe synchronization errors due to the communication delays of IoT networks in the smart grid. Therefore, this article designs the timing mechanism and a delay model to reduce the timing delay and boost real-time measurement, monitoring, and control performance of the smart grid WAM applications. The result shows that the proposed model outperformed the existing WAM system.

1. Introduction

The electrical substation is a standout among the most critical parts of the electrical transmission and distribution frameworks. As indicated by [1], the substations are classified into switchyard substation, client substation, framework substation, and conveyance substation. The switchyard substations are situated at generations, which interface the generators to the utility matrices that give the off-site capacity to the plants. Generator switchyards tend to have substantial establishments that are commonly built and developed by the power plant operators and are liable to arranging, financing, and development accomplishments not quite the same as those of predictable substation projects—the substation functions as the essential source of electric power supply for heavy consumers such as factories. The specific necessities and business case, for this kind of usages, rely more on the customer’s essentials than utility needs. In the meantime, the intelligent grid substations are network connected from the generation to the distribution through a communication framework to maintain the power grid intelligent devices and sensors deployed at the various components. The main purpose of the network-connected framework is to collect the grid data from various substation components, so that...
the power grid becomes smart with the facility of ICT infrastructure. To accomplish the smart grid operation, the communication infrastructure plays a massive role in connecting, monitoring, and controlling. The communication infrastructure plays a massive role in connecting, monitoring, and controlling the smart grid system. Communication infrastructure is the key factor to have real-time accessibility on the grid. This is because proper communication architecture can deliver a large amount of operation data, communications, processing, and control for the remote monitoring for grids and the consumptions through the communication infrastructure. Figure 1 presents a hierarchical distributed communication infrastructure for the smart grid that includes smart homes and the communication architecture from the perspective of generation, transmission, distribution, and consumption [2]. The architecture considers SCADA, PMUs, massive-scale SMs, and SMDs to obtain and measure the voltage amplitude, current phasor, and power quality of generation and distribution information at a high sampling level of effectiveness in controlling, estimating, and ongoing checking of the keen matrix. SCADA is one of the mainstream frameworks that use its uncommon convention and equipment configuration to control and estimate the substation framework. The PLC-based SCADA system has been implemented in the existing power grid automation.

For the automation purpose the various components of substations have been connected to the wide area measurement system. Choosing the right convention and correspondence system is essential to accomplish the best level of effectiveness in controlling, estimating, and ongoing checking of the keen matrix. SCADA is one of the mainstream frameworks that use its uncommon convention and equipment configuration to control and estimate the substation framework.

The existing automation architecture is based on PLCs with the SCADA system. This SCADA framework incorporates the IEDs, the correspondence protocols like IEC 61850 that have detailed data demonstrating the information out of devices that use this protocol. The IEC 61850’s strategy transport embraced connecting estimations devices, similar to instrument transformers, actuators, protection systems, and circuit breakers, to the IEDs of the sound amid an essential station. Indeed, these devices are associated with the controller of the inlet by cable connections. The IEC 61850 randomly proposes a captivated technique bus to curtail the cabling and upkeep cost. In any case, the quality does not maintain with the devoted topology of the bus bay: one technique transport will interconnect all the circle devices of the station, or a process bus per bay will connect the devices specifically.

The main contribution of this paper is as below:

1. The communication framework and the delay modeling with the timing mechanism
2. The key determinant performance factors are total measured delay, and real-time delays are modeled and analyzed the performance focusing propagation delays, receive delays, initial delays, skews, and phasor offsets parameters
3. The comparison evaluation with the C37.118 standard

\section{Smart Grid Communication Systems and Protocols}

The IEC 61850 communication protocol has expressively improved the substation automation system (SAS), by introducing the control structure in a communication framework to measure, control, and monitoring [3]. The IEC 61850 protocol suits two arranged communication phases: the substation bay and the system bay that reliably communicates the SCADA framework to the strait embellishment of the basic substation. The instrumentation are transformers; the protection unit and MU to IEDs are connected to the system bay. The two foundations have specific necessities, and consequently, the advances and plans need to send the two bays are routinely intriguing. Conventionally, the substation bay is executed utilizing a performing fiber optical gigabit Ethernet, with ring topology to redesign the receptiveness of the correspondence. The strategic bay needs to deal with a more significant proportion of information starting from transformers or merging unit (MU) that sent on the substation. Generally, a high-speed gigabit Ethernet (1/10) network system is incorporated to accomplish the communication infrastructure. The IEC 61850-9-2 LE represents a course of action of guidelines to empower the interfacing of instruments and MUs to the bay system [4]. The IEC 61850-9-2LE discusses the rate of SV that is focused on protection applications: the estimation contraptions reliable with this profile need to test the current or the voltage multiple times for each system cycle (50 Hz in EU, 60 Hz in the USA). The instruments made for also asking for applications, like quality control monitoring, test the characteristics multiple times for the lattice cycle. The MUs should be synchronized with one another to achieve precision underneath four μs. The synchronization of the equipment should be set up using a 1-PPS committed banner, generally using a GPS recipient and enclosed to each contraption [5]. A progressive package number is utilized to recover the testing cycle of each instrument. In any case, the progressing standard for the propelled substations for this standard perseveres through the synchronization challenges.

The IEEE C37.118 standard uses synchrophasors in estimating smart grid devices and components. This is to manage the synchrophasor estimation. At the required significances, the configuration parameters analyses in different conditions and displays immaterial precision [5]. The standard depicts by class P suggests security applications require helpful reaction time. Anyway, class M gives the base element of accuracy for the estimation applications, which do not require an intelligent reaction time yet perfect precision over class P. The contrasts between the two classes are recognizable in the execution of work in progress changes of amplitudes or stages. For this kind of test, the implementation required for the P class, concerning reaction time, is on an elementary level higher than M class and is accessible on the detailing pace of synchrophasors in any case, and it relies just upon the straightforward rehash of the framework [3, 6]. The circled current and voltage transformers should completely synchronize. The IEEE C37.118 synchronization calculation involves a synchronization exactness of end
gadgets underneath the microsecond, enough to estimate the synchrophasors. Moreover, the voltage and current on the framework ought to be assessed on different occasions for each cycle (50 Hz), to give an adequate number of tests. Right now, the estimation focus point can make up to two megabits for every second of development, which the system structure ought to precisely sensible with no adversity came in liberal stacked conditions.

IEEE C37.118 is messages that are structured with the data of information, configuration, header, and control message. Information messages are utilized to send supportable estimations. Data from different PMUs are transmitted in a special message related to a particular timestamp [7, 8]. Configuration messages are machine-arranged and contain information about the game-plan parts and data sorts. Configuration messages are of three explicit categories: CFG-1, CFG-2, and CFG-3. CFG-1 finds out for kind of data and point of configuration of PMU/PDU. CFG-2 displays the synchrophasor estimations, which are starting at now being transmitted/declared. In any case, CFG-3 resembles CFG-2, yet contains some extra flexibility and information about PMU qualities and estimations. Header messages contain expressive information sent by the PMU by techniques for PDC. Request messages are used to control the endeavor of contraption sending synchrophasor estimations. Like this, data, configuration, and header messages are passed on in machine-clear methodology while the header explains information in the fathomable connection. Moreover, data, configuration, and header are sent by the data sources while control messages are received by the data sources. The IEEE C37.118 correspondence design is introduced in Figure 2 [8]. IEEE C37.118 encounters the nonattendance of standard data names checks autodisclosure and self-depiction without learning of configuration message, customization weakens interoperability and joining support, and no apparent security framework or even more all it depends on GPS for the outside timestamps [9–16].

3. Recent Issues and Challenges of Communication Systems in Smart Grid

The significant advancement has been accomplished for the communication infrastructure with the special technologies that include artificial intelligence, Internet of Things (IoT), 4G, and 5G data access [15–29]. This advancement enriched the communication infrastructure for smart grid protection, measurement, and control system in a sophisticated manner. However, the smart grid maintains high-level communication with specific devices with the applications to support substation automation. The above discussion can be discussed with the specific illustration of WAM for SCADA and PMU system issues. The issues and the challenges of SCADA and PMU based WAM system for substation automation are discussed below.

3.1. Issues and Challenges of PLC and SCADA System for the Digital Substation. The network infrastructure of the WAM system is designed with the consideration of multiple device
associations to access substation devices, reduce maintenance
cost, and support the execution of additional substation
mechanization applications [25–29]. The coordination of
sequential protocol substation devices onto an Ethernet
structure is a significant test. There are different devices
called Terminal Servers, Serial Device Servers, or Console
Servers. These devices symbolize short sequential data mes-
sages into TCP/IP bundles to send over Ethernet, and each
sequential stream is related to a reliable TCP/IP session.
Terminal Servers may be connected to be worked at the
client or server zone to change over Serial-IP streams back
to the sequential plan. Then again, some expert servers or
remote PCs may interface clearly to Serial-IP streams over
IP/Ethernet affiliations. The execution over wide area net-
works (WANs) can be an issue with Serial-IP organize
fuse in perspective on limited information exchange limit,
e.g., 56 kbps or fragmentary T1 (under 1.5 Mbps) layout
hand-off or gave electronic organizations. Various SCADA
has that usage serial remote devices have short reviewing
breaks; except for on the off chance that they get a
response from a remote IED in less than 100 milliseconds,
they may expect a framework issue. Some Serial-IP framework
cannot dependably achieve this low dormancy. One
segment that impacts orchestrate torpidity can be the tradi-
tional overhead of TCP/IP epitome. As showed up in
Figure 3, sequential SCADA messages may be only two or
three bytes in length; anyway, TCP/IP tradition headers
increase the length of the Serial-IP packages by demand of
degree. This can be relieved with a SCADA frame forwarding
technique that uses layout hand-off-based embodiment with
only two or three bytes of the header to multiplex sequential
SCADA development on a WAN framework. SCADA frames
implemented together using IP-based WAN infrastructure.
The SCADA frame forwarding packet formats have been
designed with the link header, IP header, TCP header, message
and data information field, and FCS. This frame forwarding is
used to reduce the overhead by saving 90% of bandwidth and
also reduce the latency on the wide area network.

Generically, cybersecurity is another essential require-
ment from the extruders hacking for the entire internal

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**Figure 2**: Functionalities of IEEE C37.118 protocol (a) and data structure (b) [8].

![Ethernet header](22 bytes)

![IP header](24 bytes)

![UDP header](8 bytes)

![Payload](Sync (2 bytes)

![Frame size (2 bytes)]

![IDCODE (2 bytes)]

![SOC (4 bytes)]

![FRACSEC (4 bytes)]

![Data](Sync (2 bytes)

![Frame size (2 bytes)]

![IDCODE (2 bytes)]

![SOC (4 bytes)]

![FRACSEC (4 bytes)]

![Data](CHK (2 bytes)

![Ethernet trailer (4 bytes)]]

![SYNC (2 bytes)]

![STAT (2 bytes)]

![Phasor (8 bytes)]

![Freq (2 bytes)]

![Dfreq (2 bytes)]

![Analog (2 bytes)]

![Digital (2 bytes)

![SYNC (2 bytes)]

![Frame size (2 bytes)]

![IDCODE (2 bytes)]

![SOC (4 bytes)]

![FRACSEC (4 bytes)]

![Data](CHK (2 bytes)

![Ethernet trailer (4 bytes)]]
systems and smart grid processes. Researchers have been emphasizing the effort on cybersecurity-related projects to comply with current industry standards. Another critical issue is network reliability. For the high network, reliability is required in SCADA and other operational systems in grid operations. In addition, substation networking becomes advanced and integrated. Therefore, the network outages can affect many systems and control elements that need to be minimized.

3.2. Issues and Challenges of IEC 61850-90-5 Communication System. The current communication convention IEC 61850 for substation computerization inside the grid framework today is being utilized with the help of up to 12 RS232/485 and 6 Ethernet ports, SCADA server (see Figure 4). Although there are timing and control issues inside the present framework applications, the framework has been confronting difficulties utilizing IEC 61850. One point to be communicated about is that the usage of IEC 61850 inside a repetition setting, at both the gear and correspondences levels, and its impact on the general inertness of a framework, including the potential effect and issues concerning control and assurance in such an excess domain. One must ensure that the methodology will give appropriate reaction times and adaptability for future expansion (s), without precluding the adaptability to deal with considerably further developed conceivable control outcomes that may be required during a future keen network.

The IEC 61850-90-5 is inherited from the IEC 61850 main version that had been implemented for substation digitalization. The IEC 61850 is a network correspondence convention that will, in general, show power framework parts, thought of interconnected administrations, and correspondence conventions just as frameworks. It was sketched out because of the interoperability and combination between power control structure, device configurations, self-association, and item autorevelation, the persistent quality framework through retransmission, reduced substation cost through multicast, and multicast, and multicast network communication system, and reinforce for the machine-to-machine interchanges. Even though IEC 61850 furthermore has weaknesses, including the nonappearance of security constituent and confined correspondence, IEC 61850-90-5 gets all of the features of IEC 61850 while in like manner vanquishing its constraints. The critical differences between IEC 61850 and IEC 61850-90-5 showed up in Figure 5. IEC 61850-90-5 joins a security segment considering Group Domain of Interpretation (GDOI) and besides allows the transmission of time-essential shows over wide-area networks.

3.3. Issues and Challenges of IEEE C37.118 Protocol with SCADA and WAM System. Using high-level data-efficient communication system, many smart grids established the communication framework and IEEE C37.118 while in like manner vanquishing its constraints. The critical differences between IEC 61850 and IEC 61850-90-5 showed up in Figure 5. IEC 61850-90-5 joins a security segment considering Group Domain of Interpretation (GDOI) and besides allows the transmission of time-essential shows over wide-area networks.

![Figure 3: Serial IP protocol overhead vs. SCADA frame forwarding](image-url) (reproduced under the Creative Commons Attribution License/public domain).
of the lattice-observing framework because of offset and mistake events in estimating the ongoing information [17, 18, 38, 39]. The nonattendance of standard data names stays away from autorevelation and self-association without the learning of configuration messages: vendor specific features and customization cripple interoperability and blend support. There is no inherent security component to ensure digital assault [4, 12, 16–18, 27, 30, 33]. The other significant issue is the implementation of the PMUs using GPS in the smart grid. The study suggests that if the GPS connectivity is missing a little longer, the measurement and control will be complicated; therefore, the effect will be on the efficiency. However, it is observed that latency, phasor delays, and internal processing delays are still key concerns in smart grid communications. Therefore, delay modeling is critical to calibrate the existing WAM systems for smart grid applications [30, 34–37, 39]. The next section will discuss and propose the communication delay modeling by focusing and consider the few delay parameters that can be calibrated to the existing system to enhance the grid measurement performance.

4. Proposed Delay Modeling for the Smart Grid Applications

A crucial prerequisite for every data transmission of the smart grid communication system’s proper operation is timing and phase synchronization. The method by which a receiver finds the correct pulse width of time at which to sample the incoming signal is known as timing synchronization. Phase synchronization is the process by which a receiver adjusts the frequency and phase of its local carrier oscillator to match that of the transmitter. However, due to the synchronization issue of the existing IEEE C37.118 in the WAM system timing synchronization requires delay
modeling. In order to delay modeling, a timing synchronization diagram is designed along with the network architecture. The communication delay is modeled using the following proposed timing diagram of existing, where the number of PMU, PDC, and the external timestamp source is considered (see Figure 6). Figure 7 demonstrates the timing diagram [4]. The timing diagram is used for the proposed delay modeling. The delay is estimated from the above timing diagram, the time differences between the external timing source to all PMUs, PDCs, and the entire WAM communication system. To measure the total delay of the system, the first need is to consider small partial delays, propagation delays, receive delays and the phase errors, and the skew errors. The following equation is for the entire timing measurement where all sorts of delays are considered. The list of symbols that are used in Figure 7, and Eqn. (1) to Eqn. (6) are defined in Table 1.

$τ_{\text{timing signal}}^{PMU_{A} \rightarrow i} = \Phi_{\text{skew error}}^{PMU_{A}} + \left( t_{PMU_{A} \rightarrow i} + \frac{\alpha}{2} + \frac{\Delta_{\text{prop uplink}}^{PMU_{A} \rightarrow i} - \Delta_{\text{prop downlink}}^{PMU_{B} \rightarrow i}}{2} \right)$

$+ \Delta_{\text{phase error}}^{PMU_{A} \rightarrow i} + \frac{\Delta_{\text{delay uplink}}^{PMU_{A} \rightarrow i} - \Delta_{\text{delay downlink}}^{PMU_{B} \rightarrow i}}{2}$

(1)

$\Delta_{\text{total delay}}^{PMU_{A} \rightarrow i} = \left( t_{PMU_{B} \rightarrow c} - \Delta_{PMU_{B} \rightarrow c} \right) + \left( \Delta_{\text{prop uplink}}^{PMU_{B} \rightarrow c} - \Delta_{\text{prop downlink}}^{PMU_{B} \rightarrow c} \right)$

$+ \left( \Delta_{\text{phase error}}^{PMU_{B} \rightarrow c} \right) + \left( \Delta_{\text{delay uplink}}^{PMU_{B} \rightarrow c} - \Delta_{\text{delay downlink}}^{PMU_{B} \rightarrow c} \right)$

(2)
Table 1: Definition of the symbols that are used in Eqn. (1) to Eqn. (6).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sync_start</td>
<td>Synchronization start message frame</td>
</tr>
<tr>
<td>Ack</td>
<td>Acknowledgement</td>
</tr>
<tr>
<td>$\tau_{\text{timing, signal}}$</td>
<td>Timing signal of PMU A to the $i$th number</td>
</tr>
<tr>
<td>$\phi_{\text{skew, error}}$</td>
<td>Phase skew error of PMU A to the $i$th number</td>
</tr>
<tr>
<td>$\alpha$</td>
<td>Initial receive delay</td>
</tr>
<tr>
<td>$\rho_{\text{prop, uplink}}$</td>
<td>Propagation delay for uplink</td>
</tr>
<tr>
<td>$\rho_{\text{prop, downlink}}$</td>
<td>Propagation delay for downlink</td>
</tr>
<tr>
<td>$\gamma_{\text{PMU}, i}$</td>
<td>Receive delay at PMU$_i$</td>
</tr>
<tr>
<td>$\gamma_{\text{PMU}, j}$</td>
<td>Receive delay at PMU$_j$</td>
</tr>
<tr>
<td>$\Phi_{\text{PMU}, i}$</td>
<td>Total skew error</td>
</tr>
<tr>
<td>$\Delta_{\text{phase, offset}}$</td>
<td>Total phase offset</td>
</tr>
<tr>
<td>$\gamma_{\text{PMU}, i}$</td>
<td>Desired signal of PMU$_A$ to PMU$_i$</td>
</tr>
</tbody>
</table>

The total skew or the related frequency error can be expressed in Eqn. (3).

$$\Phi_{\text{skew, error}}_{\text{PMU}, i} = \gamma_{\text{PMU}, i} - \gamma_{\text{PMU}, A}.$$  

(3)

To measure the absolute delay of the entire communication system, the phase error needs to be estimated. Consequently, the total estimated phase errors can be expressed in Eqn. (4) considering Eqns. (1), (2), and (3). Therefore, the real-time delay can be modeled in Eqn. (5) using Eqn. (4).

$$\rho_{\text{prop, PMU}, i} = \frac{\rho_{\text{prop, uplink}} - \rho_{\text{prop, downlink}}}{2},$$  

(4)

$$\Delta_{\text{Measured, delay}}_{\text{PMU}, i} = \frac{1}{M} \sum_{i=1}^{M} \gamma_{\text{PMU}, i} + \Delta_{\text{Total, delay}}_{\text{PMU}, i} + \Phi_{\text{skew, error}}_{\text{PMU}, i},$$  

(5)

$$D_{\text{real-time, delay}}_{\text{PMU}, i} = \frac{\gamma_{\text{PMU}, A} - \Delta_{\text{Measured, delay}}_{\text{PMU}, i}}.$$  

(6)

5. Result and Discussion

The performance analysis was accomplished by applying the Monte Carlo simulation approach in MATLAB [16, 38]. The performance of the proposed delay model is simulated and then compared with the existing IEEE C 37.118-based WAM system. The performance was measured with the integration of the proposed delay model into the existing IEEE C37.118 and then compared with IEEE C37.118 assessed without the proposed delay model. The total measured delay and the real-time delay are considered as the performance metric of delay measurement. The simulation scenario considered 50 PMUs, and the 50 PMUs, 20 PDCs, 20 trials, and the designed communication topology. The communication network is shown in Figure 8. The initial random receive delay reference was set to 15 $\mu$s, 20 $\mu$s, and 30 $\mu$s. The number of samples was 10,000. Setting the initial delay at 10 micro-

seconds ($\mu$s) and the network bandwidth with an external timing server set to 10 Mbps, the total delay is represented in Figure 8. In addition, with the setting of 30 $\mu$s initial delays, the total delay measurement is shown in Figure 9. It is visible that the total measured delay is marginally acceptable using the proposed model. However, the existing IEEE C37.118 shows the extra delays.

Figure 10 shows the delay performance of the proposed model function of the number of occurrences and the exchanges of phasor signals among different PMUs and PDCs in the communication framework. This figure shows the actual delay in terms of asymmetric bandwidth rates; if the bandwidth increases from 1 Mbps to 10 Mbps, the communication delays are lower. For the convenience of the analysis, the receive delay processing time was varied from 10 $\mu$s to 30 $\mu$s, and the simulation processing time was assumed 1 hour. Therefore, the study suggests that the delays can be lower if the bandwidth reaches higher to higher. The observations of the proposed model result show that the communication delays are less than 0.5 seconds while bandwidth
The proposed model performs better than the existing C37.118 IEEE synchrophasors standard because of the design consideration of the timing controlling and parameters of delay modeling of the communication framework. The main parameter selection considers the propagation delays, receive delays, frequency errors, and phase errors so that it can finally estimate the real-time precisely communication delays. Therefore, in terms of delay modeling, the proposed model performs better in comparison with the C37.118 IEEE standard.

6. Conclusions
This article has investigated the smart grid automation systems and their protocols. This is to trace out the traditional communication system’s limitations and advantages in smart grids. The algorithms discussed the functionalities and characteristics. The main contributions of this paper are the proposed real-time delay measurement model and then the performance analyzing over the existing IEEE C37.118. The main achievement of the proposed model is the mitigation of the communication delays in the wide area measurement system. The result of the proposed model suggests that the unexpected delays can affect the efficiency of the smart grid application. The performance of the proposed model was found better in enhancing the precise real-time measurement and monitoring the pre-fault situation and the exigence occurrence of the smart grid applications. The future work of this study is to calibrate the delay parameter into the real-time smart grid WAM system.

Data Availability
The data used to support the findings of this study are available in the manuscript.

Conflicts of Interest
The authors declare no conflict of interest regarding this paper.

Authors’ Contributions
Data, methodology, simulation, result analysis, software, validation, and manuscript writing are done by Mohammad Kamrul Hasan. Review and editing are done by Shayla Islam, Muhammad Shafiq, Fatima Rayan Awad Ahmed, Sonya Khidir Mohmmed Ataelmman, Nissrein Babiker Mohammed Babiker, and Khairul Azmi Abu Bakar.

Acknowledgments
This research is supported by the research grant of the Universiti Kebangsaan Malaysia (UKM) under the grant GGPM 2020-028 and FRGS/1/2020/ICT03/UKM/02/6.

References


